

General Description

The CMP3205 is a N-channel Power MOSFET. It has specifically been designed to minimize input capacitance and gate charge. The device is therefore suitable in advanced high-efficiency switching applications.

Features

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|-----------------------|--|------------|-------|
| V_{DS} | Drain-Source Voltage | 60 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| $I_D@T_C=25^\circ C$ | Continuous Drain Current ¹ | 100 | A |
| $I_D@T_C=100^\circ C$ | Continuous Drain Current ¹ | 68 | A |
| I_{DM} | Pulsed Drain Current ² | 300 | A |
| EAS | Single Pulse Avalanche Energy ³ | 600 | mJ |
| I_{AS} | Avalanche Current | 62 | A |
| $P_D@T_C=25^\circ C$ | Total Power Dissipation | 190 | W |
| T_{STG} | Storage Temperature Range | -55 to 175 | °C |
| T_J | Operating Junction Temperature Range | -55 to 175 | °C |

Thermal Data

| Symbol | Parameter | Typ. | Max. | Unit |
|-----------------|--|------|------|------|
| $R_{\theta JA}$ | Thermal Resistance Junction-ambient ¹ | --- | 62.5 | °C/W |
| $R_{\theta JC}$ | Thermal Resistance Junction-case | --- | 0.79 | °C/W |

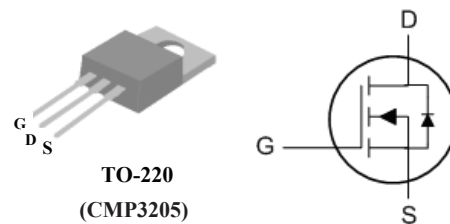
Product Summary

| BVDSS | RDSON | ID |
|-------|-------|------|
| 60V | 8.0mΩ | 100A |

Applications

- LED POWER CONTROLLER
- DC-DC & DC-AC CONVERTERS
- HIGH CURRENT, HIGH SPEED SWITCHING
- SOLENOID AND RELAY DRIVERS
- MOTOR CONTROL, AUDIO AMPLIFIERS

TO220 Pin Configuration



N-Ch 60V Fast Switching MOSFETs

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------------|--|---|------|-------|------|------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250uA | 60 | --- | --- | V |
| ΔBV _{DSS} /ΔT _J | BVDSS Temperature Coefficient | Reference to 25°C, I _D =1mA | --- | 0.057 | --- | V/°C |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =10V, I _D =62A | --- | 7.8 | 8.0 | mΩ |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _D =250uA | 2 | 3 | 4 | V |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =Max rating, V _{GS} =0V | --- | --- | 1 | uA |
| | | V _{DS} =Max rating, V _{GS} =0V @125°C | --- | --- | 10 | |
| I _{GSS} | Gate-Source Leakage Current | V _{GS} =±20V, V _{DS} =0V | --- | --- | ±100 | nA |
| g _{fs} | Forward Transconductance | V _{DS} =25V, I _D =62A | --- | 42 | --- | S |
| R _g | Gate Resistance | V _{DS} =0V, V _{GS} =0V, f=1MHz | --- | 3.3 | --- | Ω |
| Q _g | Total Gate Charge | I _D =62A V _{DS} =48V V _{GS} =10V | --- | 71 | --- | nC |
| Q _{gs} | Gate-Source Charge | | --- | 16 | --- | |
| Q _{gd} | Gate-Drain Charge | | --- | 28 | --- | |
| T _{d(on)} | Turn-On Delay Time | V _{DS} =30V I _D =62A R _G =4.7Ω, V _{GS} =10V | --- | 16 | --- | ns |
| T _r | Rise Time | | --- | 57 | --- | |
| T _{d(off)} | Turn-Off Delay Time | | --- | 85 | --- | |
| T _f | Fall Time | | --- | 71 | --- | |
| C _{iss} | Input Capacitance | V _{DS} =25V, V _{GS} =0V, f=1MHz | --- | 2422 | --- | pF |
| C _{oss} | Output Capacitance | | --- | 522 | --- | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 166 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| I _S | Continuous Source Current ¹ | V _G =V _D =0V, Force Current | --- | --- | 100 | A |
| I _{SM} | Pulsed Source Current ² | | --- | --- | 300 | A |
| V _{SD} | Diode Forward Voltage ² | V _{GS} =0V, I _S =62A, T _J =25°C | --- | --- | 1.5 | V |

Note :

1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%

3.The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=62A