

General Description

The CMP5950 uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

Features

- P-Channel
- Low ON-resistance.
- Fast Switching
- 100% avalanche tested

Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|--------------------------|--------------------------------------|------------|-------|
| V_{DS} | Drain-Source Voltage | -100 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| $I_D @ T_C = 25^\circ C$ | Continuous Drain Current | -35 | A |
| I_{DM} | Pulsed Drain Current | -105 | A |
| I_{AS} | Avalanche Current | -35 | A |
| $P_D @ T_C = 25^\circ C$ | Total Power Dissipation | 65 | W |
| T_{STG} | Storage Temperature Range | -55 to 150 | °C |
| T_J | Operating Junction Temperature Range | 150 | °C |

Thermal Data

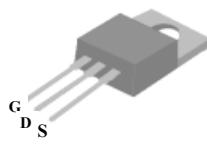
| Symbol | Parameter | Typ. | Max. | Unit |
|-----------------|-------------------------------------|------|------|------|
| $R_{\theta JA}$ | Thermal Resistance Junction-ambient | --- | 62.5 | °C/W |
| $R_{\theta JC}$ | Thermal Resistance Junction-case | --- | 2 | °C/W |

Product Summery

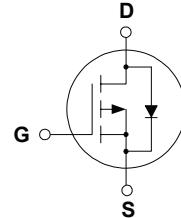
| BVDSS | RDSON | ID |
|-------|-------|------|
| -100V | 42mΩ | -35A |

Applications

- Inverters
- Motor drive
- DC / DC converter

TO220 Pin Configuration

TO-220
(CMP5950)



Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|--------------------------|-----------------------------------|---------------------------------------------------------------------------|------|------|-----------|------------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$ | -100 | --- | --- | V |
| $R_{\text{DS(ON)}}$ | Static Drain-Source On-Resistance | $V_{\text{GS}}=-10\text{V}$, $I_D=-10\text{A}$ | --- | 39 | 42 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=-4.5\text{V}$, $I_D=-8\text{A}$ | --- | 43 | 48 | |
| $V_{\text{GS(th)}}$ | Gate Threshold Voltage | $V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$ | -1 | --- | -3 | V |
| I_{DSS} | Drain-Source Leakage Current | $V_{\text{DS}}=-100\text{V}$, $V_{\text{GS}}=0\text{V}$ | --- | --- | -1 | μA |
| I_{GSS} | Gate-Source Leakage Current | $V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$ | --- | --- | ± 100 | nA |
| g_{fs} | Forward Transconductance | $V_{\text{DS}}=-10\text{V}$, $I_D=-10\text{A}$ | --- | 24 | --- | S |
| Q_g | Total Gate Charge | $I_D=-20\text{A}$ | --- | 75 | --- | nC |
| Q_{gs} | Gate-Source Charge | $V_{\text{DS}}=-50\text{V}$ | --- | 13 | --- | |
| Q_{gd} | Gate-Drain Charge | $V_{\text{GS}}=-10\text{V}$ | --- | 16 | --- | |
| $T_{\text{d(on)}}$ | Turn-On Delay Time | $V_{\text{DS}}=-50\text{V}$ | --- | 25 | --- | ns |
| T_r | Rise Time | $I_D=-10\text{A}$ | --- | 90 | --- | |
| $T_{\text{d(off)}}$ | Turn-Off Delay Time | $R_L=5.6\Omega$ | --- | 310 | --- | |
| T_f | Fall Time | $V_{\text{GS}}=-10\text{V}$ | --- | 100 | --- | |
| C_{iss} | Input Capacitance | $V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$ | --- | 4500 | --- | pF |
| C_{oss} | Output Capacitance | | --- | 270 | --- | |
| C_{rss} | Reverse Transfer Capacitance | | --- | 200 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|-------------------------|------------------------------------------------------|------|------|------|------|
| t_{rr} | Reverse Recovery Time | $I_S=-8\text{A}$ $dI/dt=-100\text{A}/\mu\text{s}$ | --- | 60 | --- | ns |
| Q_{rr} | Reverse Recovery Charge | | --- | 150 | --- | nC |
| V_{SD} | Diode Forward Voltage | $V_{\text{GS}}=0\text{V}$, $I_S=-20\text{A}$ | --- | --- | -1.2 | V |