

GENERAL DESCRIPTION

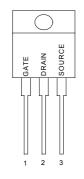
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

FEATURES

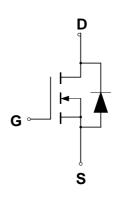
- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ◆ I_{DSS} Specified at Elevated Temperature

PIN CONFIGURATION

TO-220/TO-220FP Front View



SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current — Continuous	I _D	7.0	Α
- Pulsed	I _{DM}	20	
Gate-to-Source Voltage — Continue	V_{GS}	±20	V
Non-repetitive	V_{GSM}	±40	V
Total Power Dissipation	P _D		W
TO-220		147	
TO-220FP		50	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 150	$^{\circ}\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J$ = 25 $^\circ$ C	E _{AS}	245	mJ
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 7A, L = 10mH, R_G = 25\Omega)$			
Thermal Resistance — Junction to Case	θ_{JC}	1.0	°C/W
 Junction to Ambient 	θ_{JA}	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T∟	260	$^{\circ}\!\mathbb{C}$

⁽¹⁾ VDD = 50V. ID = 10A

⁽²⁾ Pulse Width and frequency is limited by TJ(max) and thermal response



ORDERING INFORMATION

Part Number	Package
CMT07N60	TO-220
CMT07N60FP	TO-220 Full Pak

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, T_J = $25^{\circ}\!\!\! \text{C}$.

			CMT07N60			
Cha	aracteristic	Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	600			V	
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I _{DSS}				μA
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$					100	
$(V_{DS} = 480 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}C$	C)				100	
Gate-Source Leakage Current-Forwa	ard	I_{GSSF}			100	nA
$(V_{gsf} = 20 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Rever	rse	I_{GSSR}			100	nA
$(V_{gsr} = 20 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.0		4.0	V
$(V_{DS} = V_{GS}, I_D = 250 \mu A)$						
Static Drain-Source On-Resistance (·	R _{DS(on)}			1.2	Ω
Forward Transconductance ($V_{DS} = 40$) V, I _D = 3.5A) *	g FS	4.0			mhos
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	C_{iss}		1380	1800	pF
Output Capacitance	$(v_{DS} - 23 \text{ v}, v_{GS} - 0 \text{ v},$ f = 1.0 MHz)	C_{oss}		115	150	pF
Reverse Transfer Capacitance	1 – 1.0 ((112)	C_{rss}		23	30	pF
Turn-On Delay Time	(V _{DD} = 300 V, I _D = 7.0 A.	$t_{d(on)}$		30	70	ns
Rise Time	$V_{DD} = 300 \text{ V}, I_D = 7.0 \text{ A},$ $V_{GS} = 10 \text{ V},$	t _r		80	170	ns
Turn-Off Delay Time	$V_{GS} = 10 \text{ V},$ $R_{G} = 9.1\Omega) *$	$t_{d(off)}$		125	260	ns
Fall Time	N _G = 9.122)	t _f		85	180	ns
Total Gate Charge	()/ - 400 \/ - 7.0 A	Q_g		38	50	nC
Gate-Source Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 7.0 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	Q_gs		6.4		nC
Gate-Drain Charge	V _{GS} = 10 V)	Q_{gd}		15		nC
Internal Drain Inductance	L _D		4.5		nH	
(Measured from the drain lead 0.25						
Internal Drain Inductance	L _S		7.5		nH	
(Measured from the source lead 0.						
SOURCE-DRAIN DIODE CHARACT	ERISTICS					
Forward On-Voltage(1)	/I =7.0 A	V_{SD}			1.4	V
Forward Turn-On Time	$(I_S = 7.0 \text{ A},$ $d_{IS}/d_t = 100\text{A/}\mu\text{s})$	t _{on}		**		ns
Reverse Recovery Time	$a_{iS}/a_t = TOUA/\mu S$	t _{rr}		415		ns

^{*} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

^{**} Negligible, Dominated by circuit inductance



TYPICAL ELECTRICAL CHARACTERISTICS

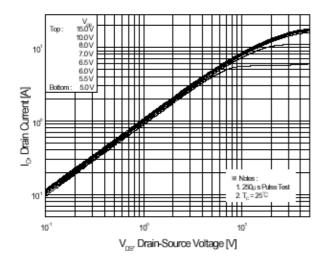


Figure 1. On-Region Characteristics

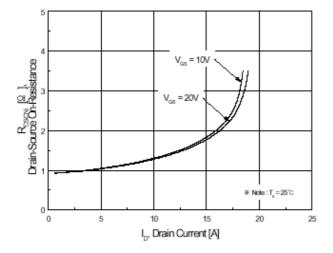


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

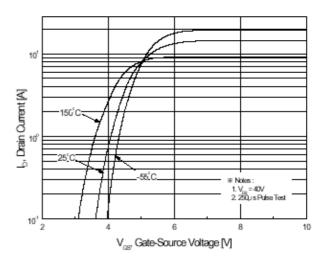


Figure 2. Transfer Characteristics

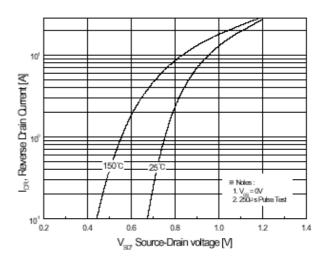


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature



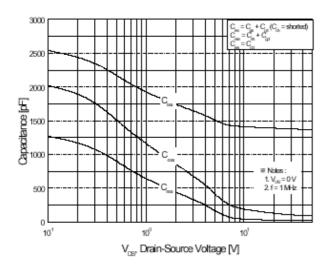


Figure 5. Capacitance Characteristics

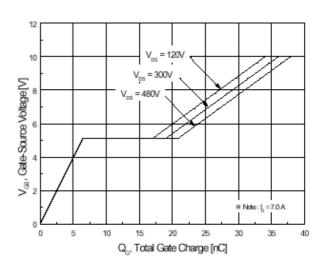


Figure 6. Gate Charge Characteristics

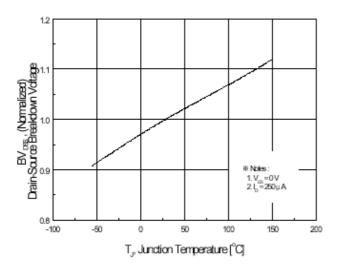


Figure 7. Breakdown Voltage Variation vs Temperature

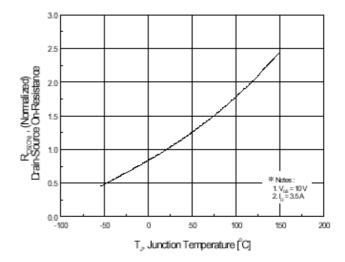
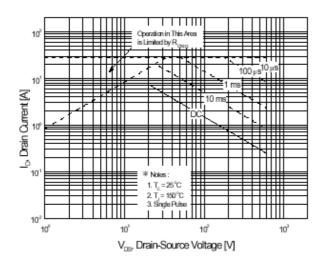


Figure 8. On-Resistance Variation





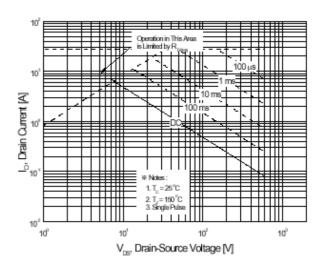


Figure 9-1. Maximum Safe Operating Area

Figure 9-2. Maximum Safe Operating Area

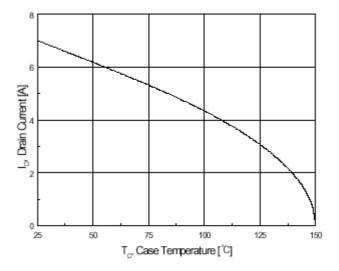
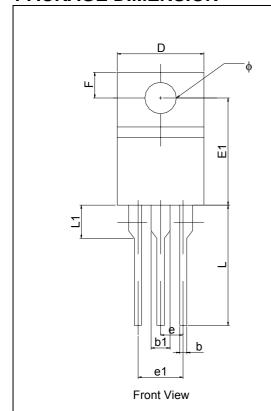
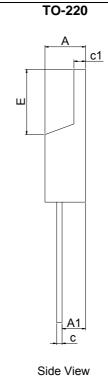


Figure 10. Maximum Drain Current vs Case Temperature



PACKAGE DIMENSION

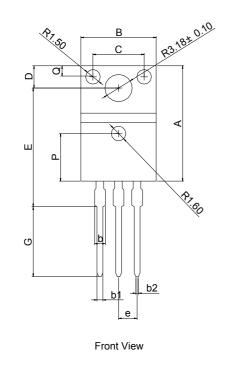


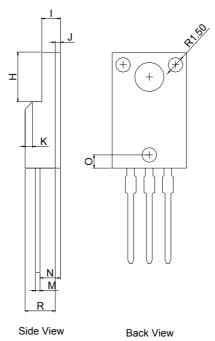


PIN 1: GATE PIN 2: DRAIN PIN 3: SOURCE

and the c	DIMENSIONS IN MILLIMETERS		DIMENS	IONS IN I	NCHS	
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX
Α	4.47	-	4.67	0.176		0.184
A1	2.52		2.82	0.099		0.111
b	0.71		0.91	0.028		0.036
b1	1.17		1.37	0.046		0.054
С	0.31		0.53	0.012		0.021
c1	1.17		1.37	0.046		0.054
D	10.01		10.31	0.394		0.406
E	8.50		8.90	0.335		0.350
E1	12.06		12.46	0.475		0.491
е		2.54			0.100	
e1	4.98		5.18	0.196		0.204
F	2.59		2.89	0.102		0.114
L	13.40		13.80	0.528		0.543
L1	3.56		3.96	0.140		0.156
ф	3.79		3.89	0.149		0.153

TO-220FP





	DIMENSIONS IN MILLIMETERS		DIMENS	HONS IN I	NCHS	
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX
А	15.67		16.07	0.617		0.633
В	9.96		10.36	0.392		0.408
С		7.00			0.275	
D	3.20		3.40	0.126		0.134
E	15.60		16.00	0.614		0.630
G	9.45		10.05	0.372		0.396
н	6.48		6.88	0.255		0.279
- 1	2.34		2.74	0.092		0.108
J		0.70			0.028	
к		1.00			0.039	
М	0.45		0.60	0.018		0.024
N	2.56		2.96	0.101		0.117
0		1.80			0.071	
P		6.50			0.256	
Q		1.50			0.059	
R	4.50		4.90	0.177		0.193
ь			1.47			0.058
b1	0.70		0.90	0.028		0.035
b2	0.25		0.45	0.010		0.018
е		2.54			0.100	



IMPORTANT NOTICE

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