

General Description

N-channel enhancement mode field-effect power transistor in a plastic envelope suitable for surface mounting. The device is intended for use in Switched Mode Power Supplies (SMPS), motor control, welding, DC/DC and AC/DC converters, and in general purpose switching applications.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Mounting Information Provided for the DPAK Package
- 100% avalanche tested
- Green Device Available

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current ¹	12	A
$I_D@T_C=100^\circ C$	Continuous Drain Current ¹	7	A
I_{DM}	Pulsed Drain Current ²	40	A
EAS	Single Pulse Avalanche Energy ³	64	mJ
I_{AS}	Avalanche Current	10	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	50	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	100	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction -Case ¹	---	3	$^\circ C/W$

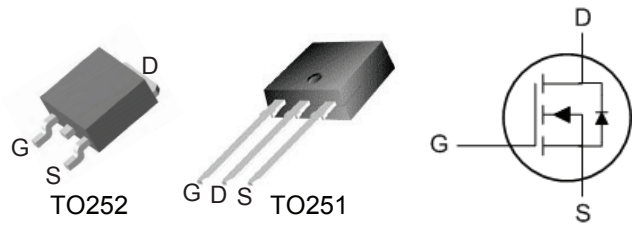
Product Summary

BVDSS	RDSON	ID
100V	0.165 Ω	12A

Applications

- PWM Motor Controls
- LED controller
- Power Supplies
- DC-DC & DC-AC Converters

TO252 / TO251 Pin Configuration



Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$, $I_D=250\mu A$	---	113	---	MV/ $^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=6A$	---	0.128	0.165	Ω
		$V_{GS}=4.5V, I_D=6A$	---	0.164	0.22	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	3	4	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	4.4	---	mV/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V, V_{GS}=0V, T_J=25\text{ }^\circ\text{C}$	---	---	1	μA
		$V_{DS}=100V, V_{GS}=0V, T_J=150\text{ }^\circ\text{C}$	---	---	10	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=10V, I_D=6A$	---	8.9	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	1.1	---	Ω
Q_g	Total Gate Charge	$V_{DS}=80V, V_{GS}=5V, I_D=6A$	---	13.5	---	nC
Q_{gs}	Gate-Source Charge		---	3.2	---	
Q_{gd}	Gate-Drain Charge		---	7.4	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=50V, V_{GS}=10V, R_G=9.1\Omega$ $I_D=12A$	---	9.6	---	ns
T_r	Rise Time		---	45	---	
$T_{d(off)}$	Turn-Off Delay Time		---	40	---	
T_f	Fall Time		---	21	---	
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1\text{MHz}$	---	498	---	pF
C_{oss}	Output Capacitance		---	114	---	
C_{rss}	Reverse Transfer Capacitance		---	38	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ¹	$V_G=V_D=0V$, Force Current	---	---	12	A
I_{SM}	Pulsed Source Current ²		---	---	40	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=12A, T_J=25\text{ }^\circ\text{C}$	---	---	1.45	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=50V, V_{GS}=10V, L=1mH, I_L=12A$