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 $\underline{XOS} > CO-448$

CO-448 HCMOS, ACMOS and FCT Clock Oscillators

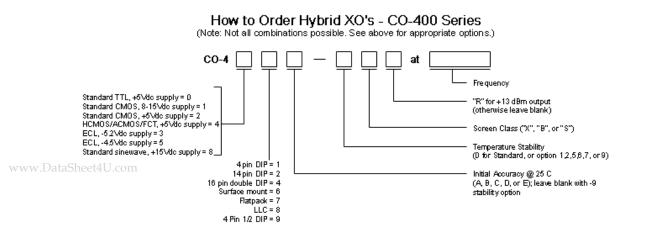


Features:

- 100 kHz to 50 MHz Frequency Range
- Sealed Ceramic Leadless Chip Carrier
- HCMOS/ACMOS/FCT/ACT Compatible
- Lowest Profile

SPECIFICATIONS								
Series	CO-448: Leadless Chip Carrier							
Frequency	100 kHz-50 MHz							
Supply	5 Vdc ± 5%							
Accuracy (Maximum Error at 25°C)	CO-448A ±50 ppm CO-448C ±25 ppm *Settability via external capacitor: (<60 MHz only: except 449E ≤20 MHz)							
Temperature Stability	STANDARD:	0°C	to	+70°C:	±25 ppm			
Improved accuracy/stability available on some models. For example, for ± 7 ppm over 0°C to $+50°$ C and for ± 10 ppm over 0°C to $+70°$ C. Improvement is also available over wider temperature ranges. Please contact factory.	Option 1:	-55°C	to	+85°C:	±50 ppm			
	Option 2:	-55°C	to	+125°C:	±50 ppm			
	Option 5:	0°C	to	+50°C:	±5 ppm			
	Option 6:	0°C	to	+50°C:	±10 ppm			
	Option 7:	-55°C	to	+125°C:	±100 ppm			
	*Option 9 :	-55°C	to	+200°C:	±300 ppm			
	(Option 9: N/A in CO-448 or above 20 MHz in CO-440 Series) *Specified stability includes initial accuracy: do not specify A,B,C,D or E accuracy.							
Aging Rate (typical after 30 days)	3 ppm first year 2 ppm/year thereafter							

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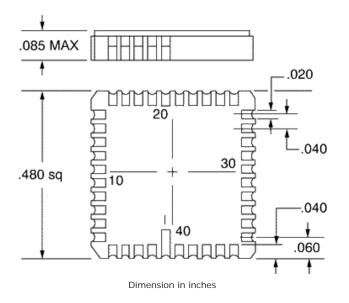
SCREEN TESTING OF ABOVE MODELS

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	MIL-STD-883 METHOD	Standard CLASS X	Options			
SCREEN TEST			CLASS D	CLASS B	CLASS S	
Stabilization Bake (150°C)	_	х	х	х	Class S screen test requirements include 24	
Seal Test (Gross and Fine)	1014, Cond A2	х	х	х	hour additional bake-out, 80 hour additional burn-in, thermal shock. PIND test	
Temperature Cycling (Thermal Shock)	1010, Cond B		х	х	and radiographic inspection in addition to Class B	
Burn-in, operating 160 hours @125°C	_		х	х	Screening. Has major cost impact.	
Acceleration (5000g in Y ₁ axis)	2001, Cond A			х		

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 Pinouts

 Pin
 Function

 4
 +5Vdc

 10
 +5Vdc

 31
 Ground

 37
 Ground

 39
 Output

 Other
 N/C

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http://www.vectron.com/products/xo/CO-448.htm