

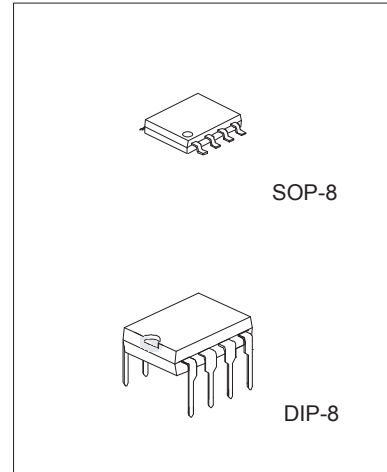
## Dual Operational Amplifier

### DESCRIPTION

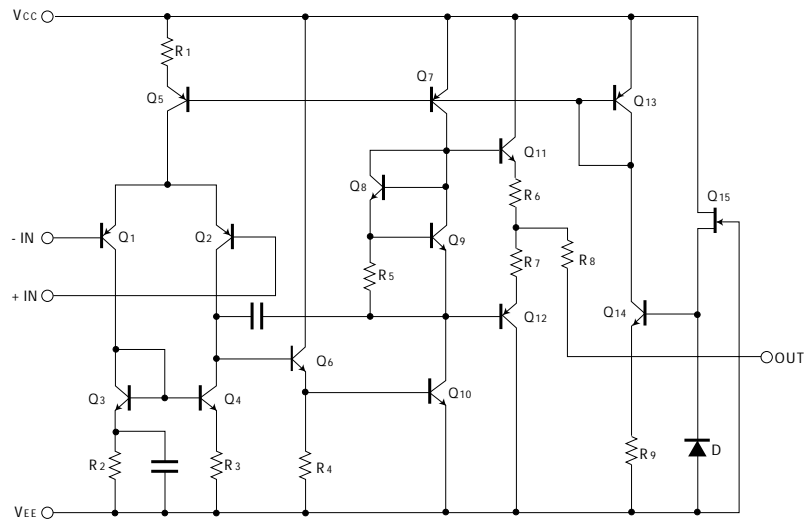
The CO4558 is a high performance monolithic dual operational amplifier

### FEATURES

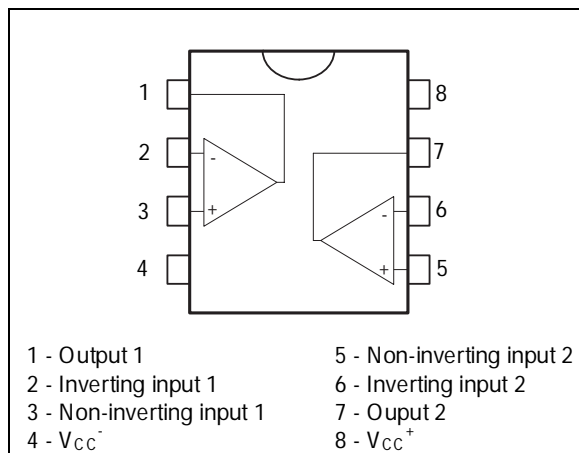
- No frequency compensation required
- No latch-up
- Large common mode and differential voltage range
- Parameter tracking over temperature range
- Gain and phase match between amplifiers
- Internally frequency compensated
- Low noise input transistors
- Pin to pin compatible with MC1458 / LM358



### BLOCK DIAGRAM (ONE SECTION ONLY)



### PIN CONFIGURATION

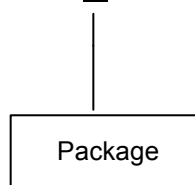


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## Dual Operational Amplifier

### ORDERING INFORMATION

CO4558N



Blank SO-8  
 N=PDIP8  
 A=SO-8 & taping

### MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V <sub>CC</sub>	± 22	V
Differential Input Voltage	V <sub>I(DIFF)</sub>	± 18	V
Input Voltage	V <sub>I</sub>	± 15	V
Operating Temperature	TOPR	0 ~ +70	°C
Power Dissipation	P <sub>D</sub>	600 400	mW
Storage Temperature Range	TSTG	-65 ~ +150	°C

### ELECTRICAL CHARACTERISTICS (V<sub>CC</sub>=15.0V, V<sub>EE</sub>= -15V, T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDUCTION	MIN	TYP	MAX	UNIT
Supply Current, all Amp, no load	I <sub>CC</sub>			3.5	5.6	mA
Input offset voltage	V <sub>IO</sub>	R <sub>S</sub> ≤ 10kΩ		2	6	mV
Input offset current	I <sub>IO</sub>			5	200	nA
Input bias current	I <sub>BIAS</sub>			30	500	nA
Large signal voltage gain	G <sub>V</sub>	V <sub>O(p-p)</sub> = ±10V, R <sub>L</sub> ≤ 2kΩ	20	200		V/mV
Common Mode Input Voltage Range	V <sub>I(R)</sub>		±12	±13		V
Common Mode Rejection Ratio	CMRR	R <sub>S</sub> ≤ 10kΩ	70	90		dB
Supply Voltage Rejection Ratio	PSRR	R <sub>S</sub> ≤ 10kΩ	76	90		dB
Output Voltage swing	V <sub>O(p-p)</sub>	R <sub>L</sub> ≥ 10kΩ		±12	±14	V
Power Consumption	P <sub>C</sub>			70	170	mW
Slew Rate	SR	V <sub>i</sub> = ± 10V, R <sub>L</sub> ≥ 2kΩ, C <sub>L</sub> ≤ 100pF	1.2	2.2		V/μs
Rise Time	T <sub>RISE</sub>	V <sub>i</sub> = ± 20mV, R <sub>L</sub> ≥ 2kΩ, C <sub>L</sub> ≤ 100pF		0.3		μs
Overshoot	OS	V <sub>i</sub> = ± 20mV, R <sub>L</sub> ≥ 2kΩ, C <sub>L</sub> ≤ 100pF		15		%
Input Resistance	R <sub>i</sub>		0.3	2		MΩ
Output Resistance	R <sub>o</sub>			75		Ω
Total Harmonic Distortion	THD	f=1kHz, A <sub>v</sub> =20dB, R <sub>L</sub> =2kΩ, V <sub>o</sub> =2V <sub>pp</sub> , C <sub>L</sub> =100pF		0.008		%
Channel Separation	V <sub>o1</sub> /V <sub>o2</sub>			120		dB

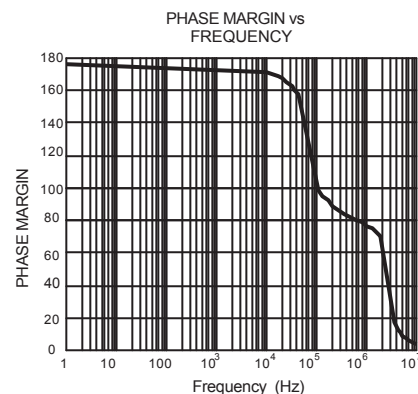
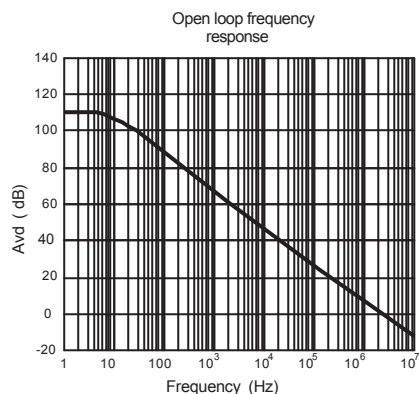
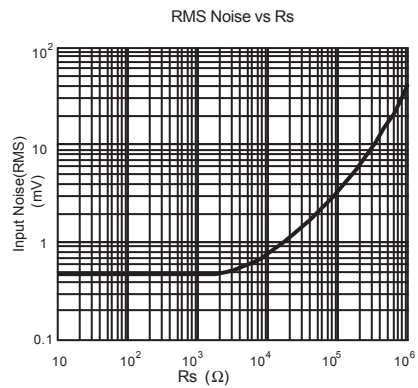
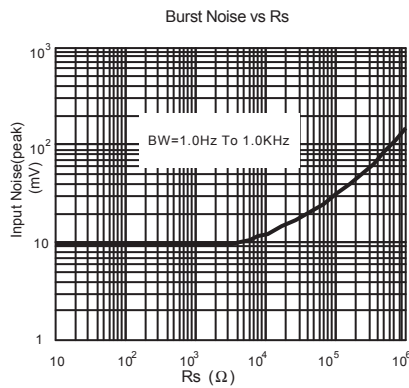
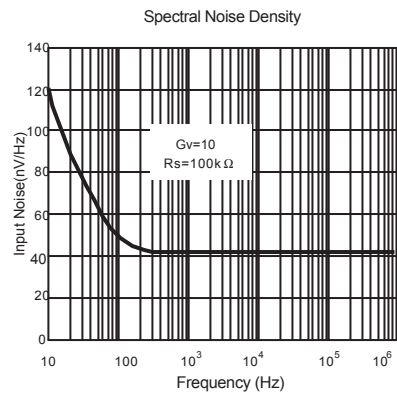
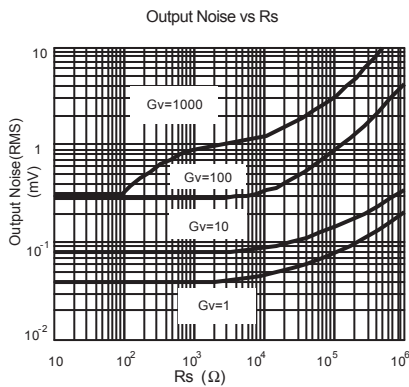
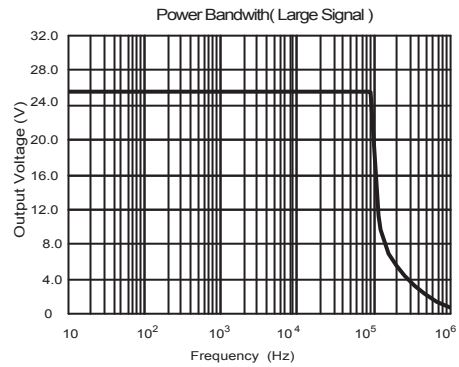
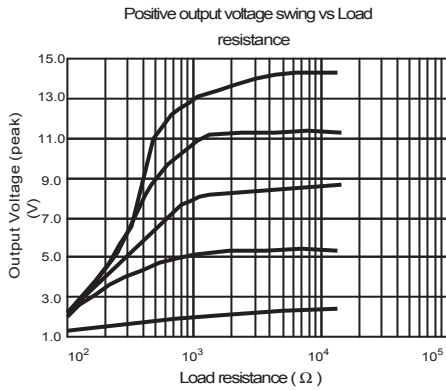
### FREQUENCY CHARACTERISTICS (T<sub>a</sub>=25°C, V<sub>CC</sub>=15V, V<sub>EE</sub>=-15V)

PARAMETER	SYMBOL	TEST CONDUCTION	MIN	TYP	MAX	UNIT
Unity Gain Bandwidth	BW		2.0	2.8		MHz

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## Dual Operational Amplifier

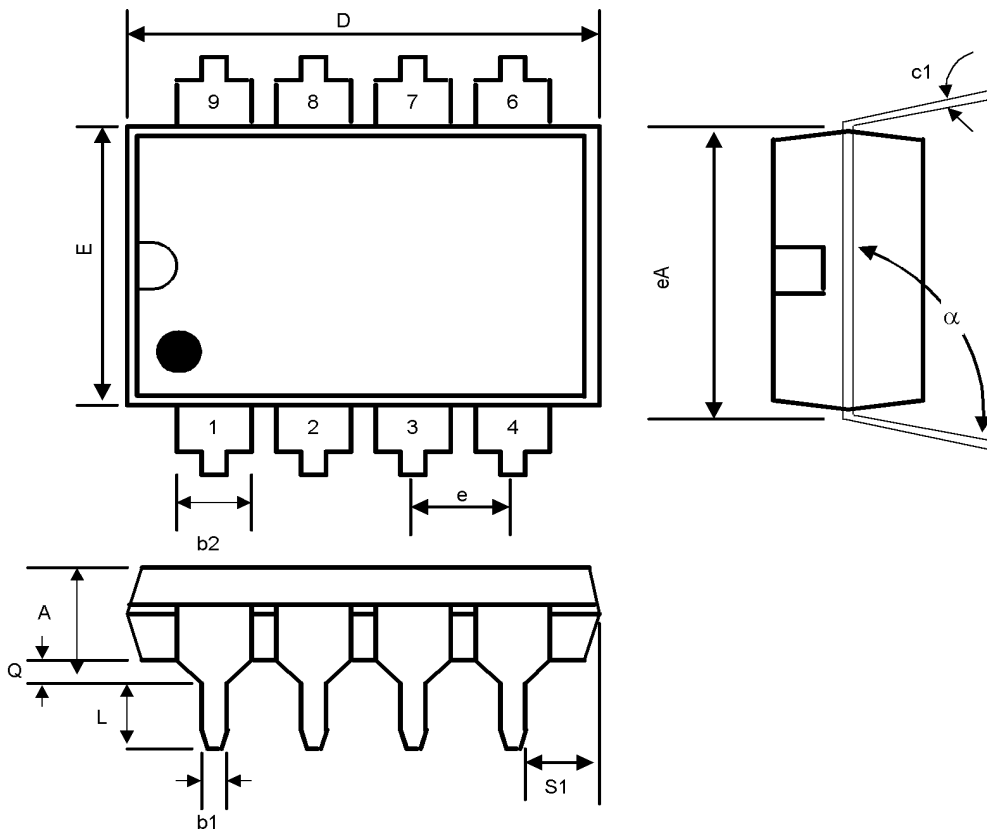
### TYPICAL PERFORMANCE CHARACTERISTICS



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## Dual Operational Amplifier

### Package Outlines : DIP-8

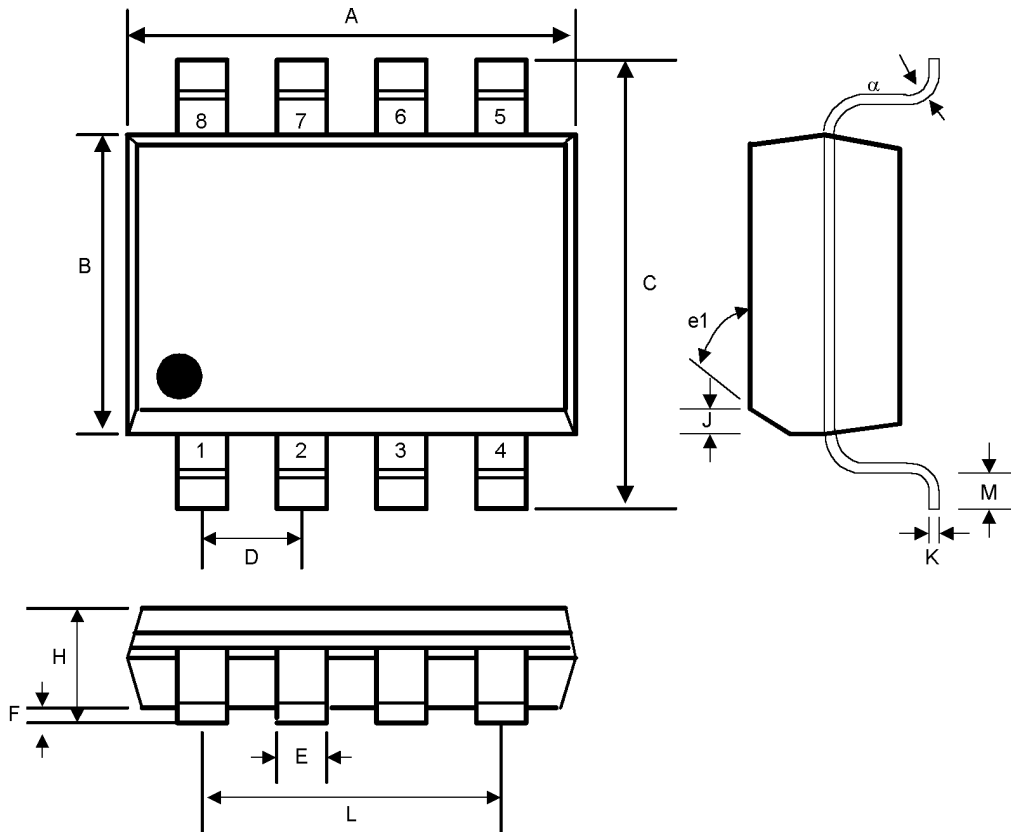


SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN	MAX	MIN	MAX	
A	-	0.200	-	5.08	-
b1	0.014	0.023	0.36	0.58	-
b2	0.045	0.065	1.14	1.65	-
c1	0.008	0.015	0.20	0.38	-
D	0.355	0.400	9.02	10.16	-
E	0.220	0.310	5.59	7.87	-
e	0.100 BSC		2.54 BSC		-
eA	0.300 BSC		7.62 BSC		-
L	0.125	0.200	3.18	5.08	-
Q	0.015	0.060	0.38	1.52	-
s1	0.005	-	0.13	-	-
	90 <sup>0</sup>	105 <sup>0</sup>	90 <sup>0</sup>	105 <sup>0</sup>	-

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## Dual Operational Amplifier

### Small Outline SOP-8



SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN	MAX	MIN	MAX	
A	0.188	0.197	4.80	5.00	-
B	0.149	0.158	3.80	4.00	-
C	0.228	0.244	5.80	6.20	-
D	0.050 BSC		1.27 BSC		-
E	0.013	0.020	0.33	0.51	-
F	0.004	0.010	0.10	0.25	-
H	0.053	0.069	1.35	1.75	-
J	0.011	0.019	0.28	0.48	-
K	0.007	0.010	0.19	0.25	-
M	0.016	0.050	0.40	1.27	-
L	0.150 REF		3.81 REF		-
e1	45°		45°		-
	0°	8°	0°	8°	-

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