**CPH6324** 



Ordering number : ENN0000

# **Ultrahigh-Speed Switching Applications**

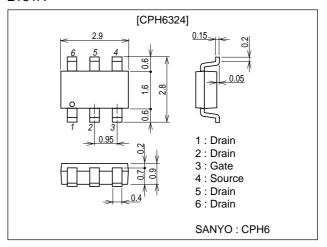
## **Preliminary**

#### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

## **Package Dimensions**

unit : mm 2151A



## **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-8	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1200mm <sup>2</sup> X0.8mm)	1.6	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### **Electrical Characteristics** at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	O III
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0	-60			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0			±10	μΑ
Gate-to-Source Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1A	1.6	2.4		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =-1A, V <sub>GS</sub> =-10V		275	360	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-1A, V <sub>G</sub> S=-4V		400	560	mΩ

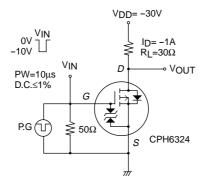
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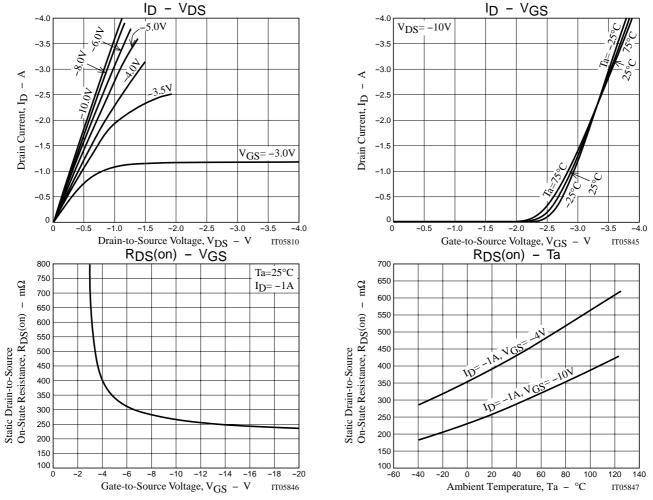
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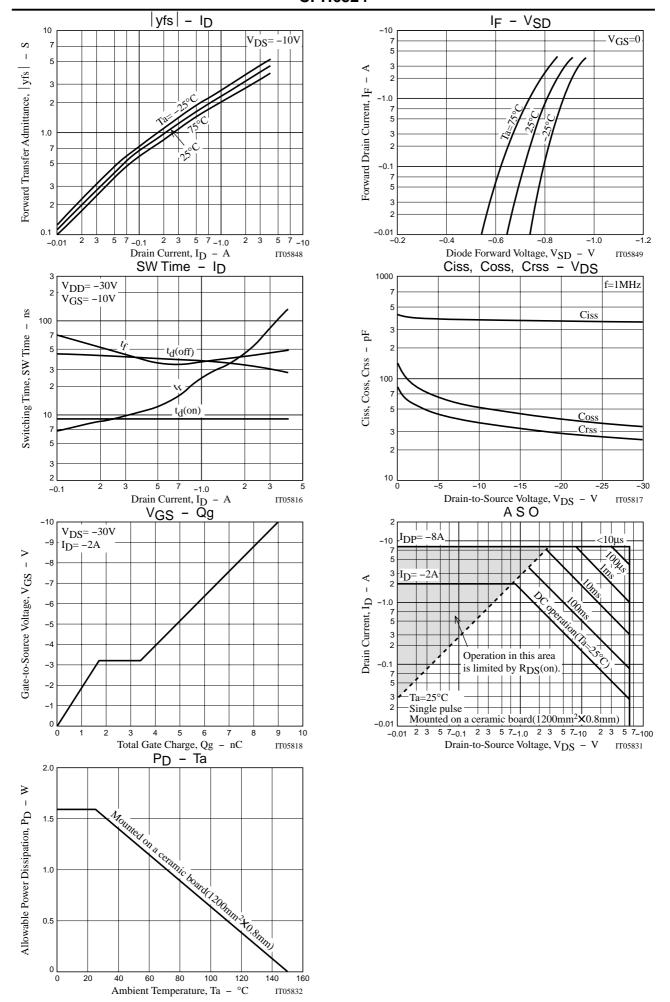
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Input Capacitance	Ciss	V <sub>DS</sub> =-20V, f=1MHz		365		pF
Output Capacitance	Coss	V <sub>DS</sub> =-20V, f=1MHz		39		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-20V, f=1MHz		30		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		9		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		24		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		38		ns
Fall Time	tf	See specified Test Circuit.		38		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-2A		9		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-2A		1.7		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-2A		1.7		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-2A, V <sub>GS</sub> =0		-0.84	-1.2	V

## **Switching Time Test Circuit**







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