



CPH6901

N-Channel Silicon Junction FET
**Low-Frequency General-Purpose Amplifier,
 Differential Amplifier, Analog Switch Applications**

Features

- Composite type with 2 FET contained in a CP package currently in use, improving the mounting efficiency greatly.
- The CPH6901 is formed with two chips, being equivalent to the 2SK303, placed in one package.
- Optimal for differential amplification due to excellent thermal equilibrium and pair capability.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSX}		30	V
Gate-to-Drain Voltage	V _{GDS}		-30	V
Gate Voltage	I _G		10	mA
Drain Current	I _D		10	mA
Allowable Power Dissipation	P _D	1 unit	200	mW
Total Power Dissipation	P _T		300	mW
Junction Temperature	T _J		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Gate-to-Drain Breakdown Voltage	V(BR) _{GDS}	I _G =-10μA, V _{DS} =0	-30			V
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =-20V, V _{DS} =0			-1.0	nA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =1μA	-0.3	-0.8	-1.25	V
Gate-to-Source Voltage Difference	ΔV _{GS}	V _{GS Large} -V _{GS Small} V _{DS} =10V, I _D =1mA			50	mV
Drain Current	I _{DSS}	V _{DS} =10V, V _{GS} =0(FET1)	1.2		3.0	mA
Drain Current Ratio		I _{DSS Small} / I _{DSS Large}	0.9			
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, V _{GS} =0, f=1MHz	3.0	4.5		mS
Forward Transfer Admittance Ratio		y _{fs Small} / y _{fs Large}	0.9			
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0, f=1MHz		5.0		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =10V, V _{GS} =0, f=1MHz		0.9		pF
Static Drain-to-Source On-State Resistance	R _{DS(on)}	V _{DS} =10mV, V _{GS} =0		250		Ω

The specifications shown above are for each individual transistor.

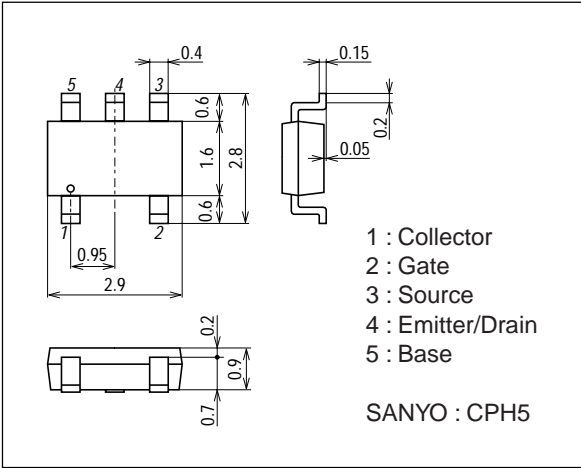
Marking : 1G

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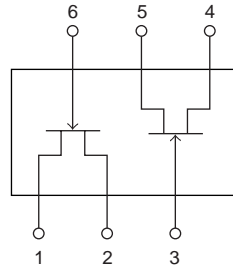
Package Dimensions

unit : mm

2248

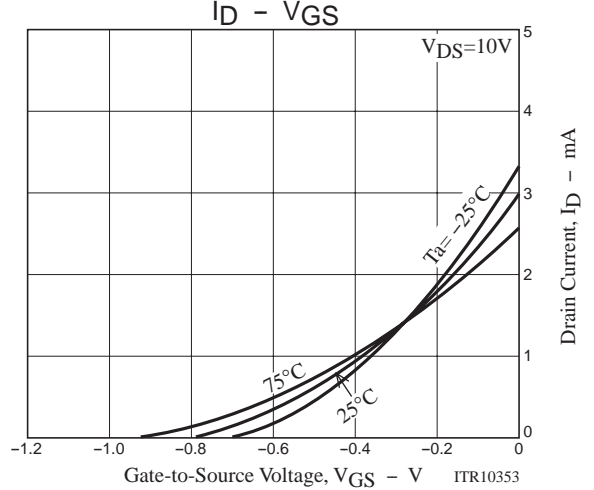
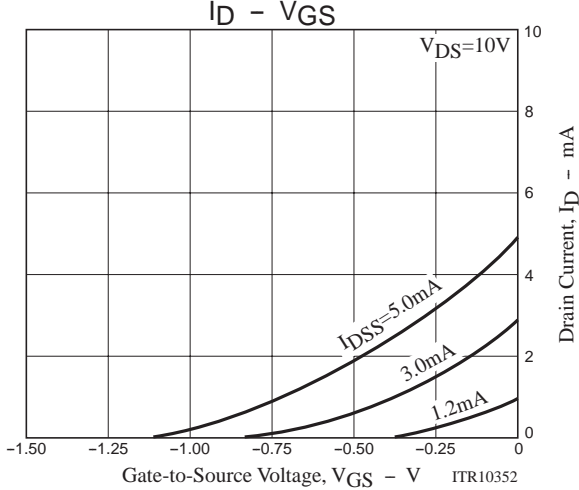
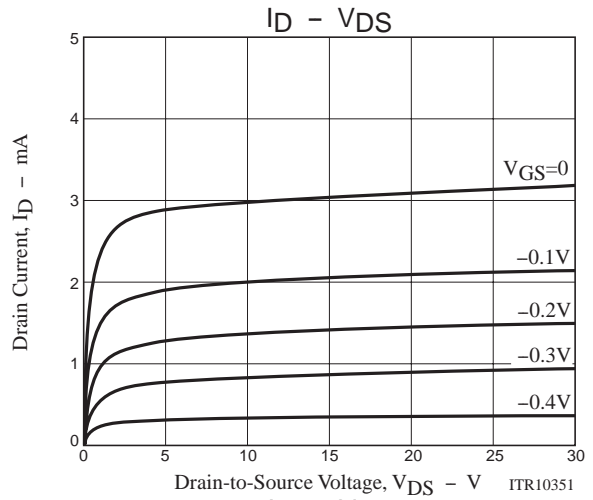
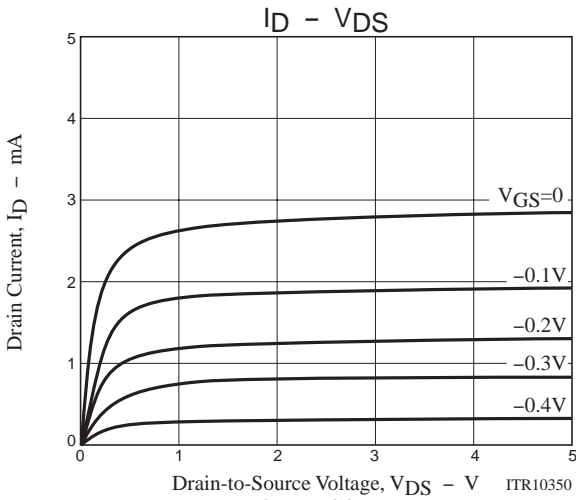


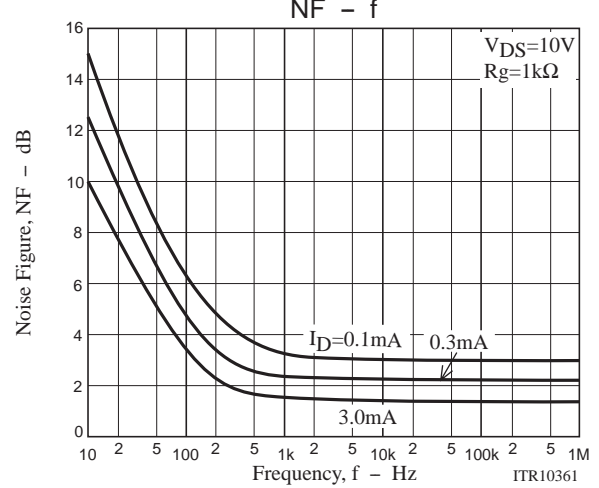
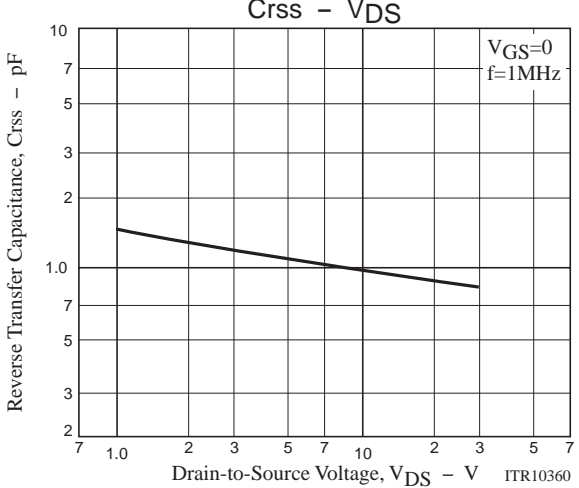
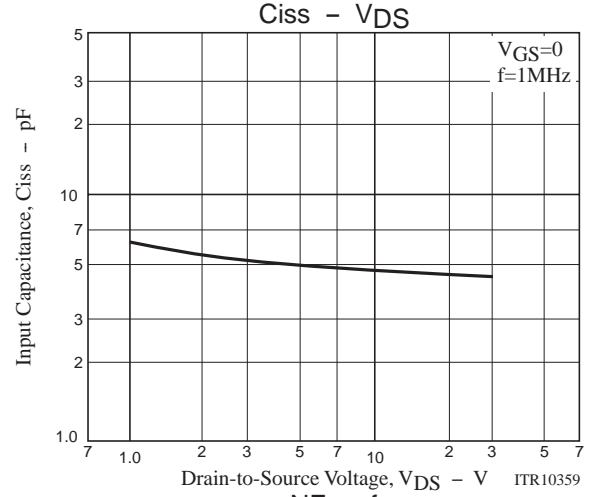
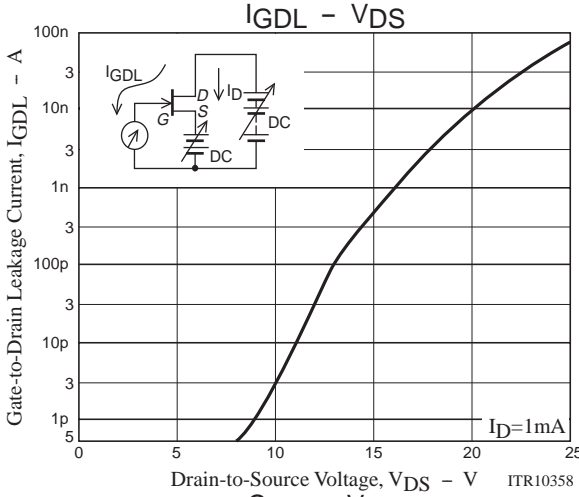
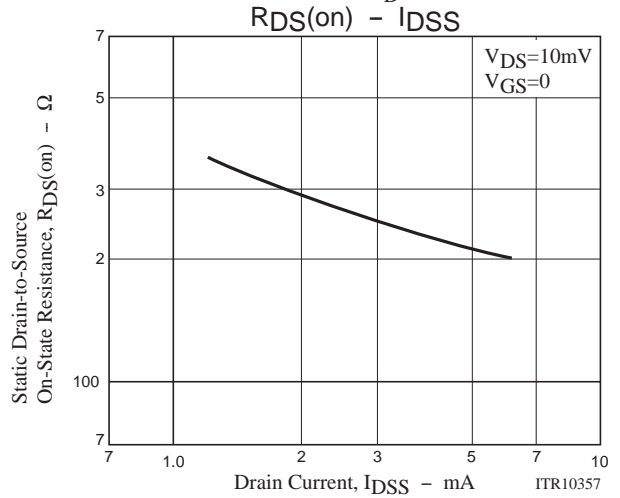
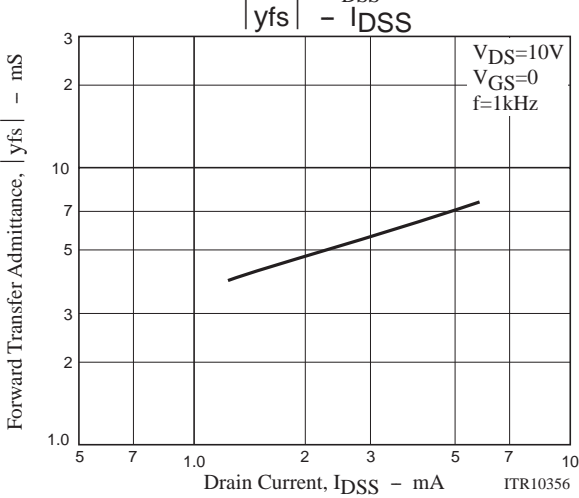
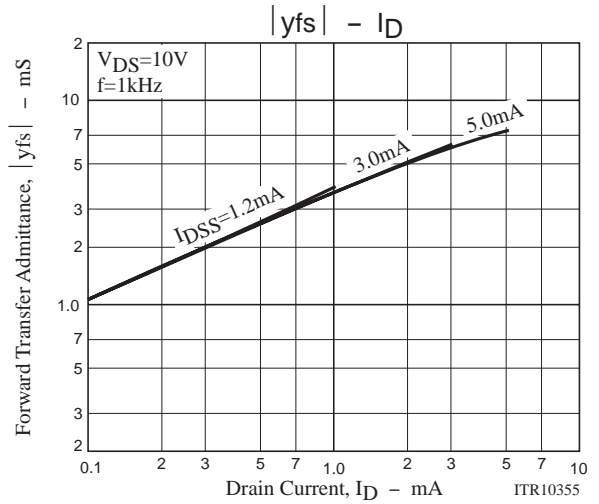
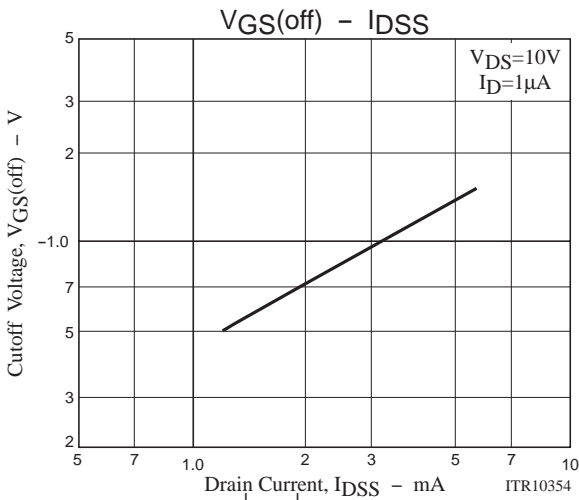
Electrical Connection

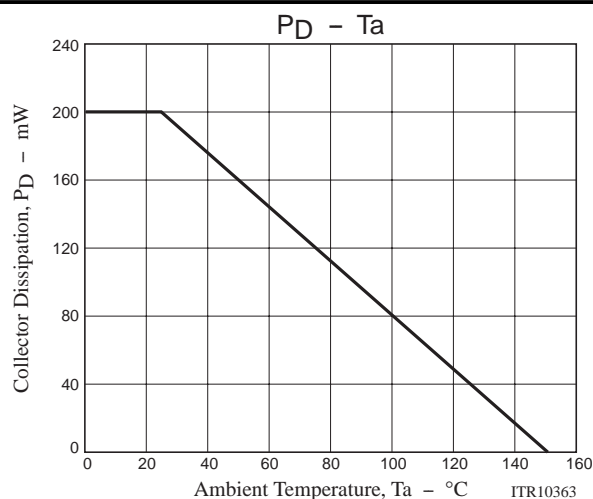
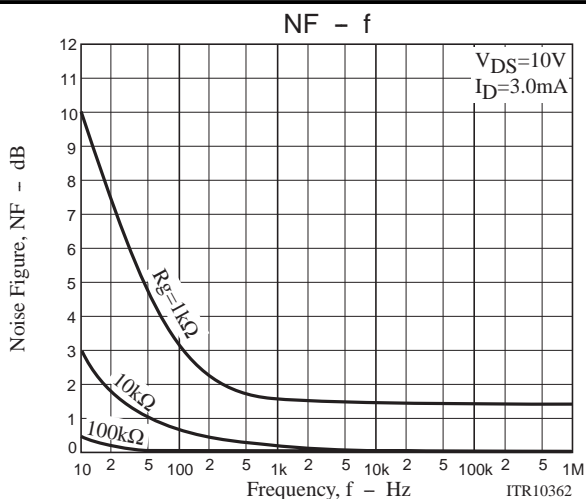


- 1 : Drain1
- 2 : Source1
- 3 : Gate2
- 4 : Source2
- 5 : Drain2
- 6 : Gate1

Top view







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