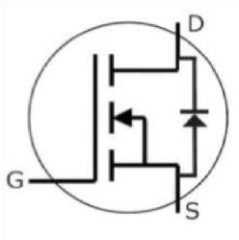
CPM2-1200-0025A

Wolfspeed SiC Gen 2 MOSFET



Package Types: Bare Die
PN's: CPM2-1200-0025A

Description

This is the Wolfspeed's 2nd generation of high performance silicon carbide MOSFET in a packageless bare die format to be implemented into any custom module design. The high blocking voltage with low on-resistance, high speed switching with low capacitance make this MOSFET ideal for high frequency switching application including solar inverters and EV chargers.

Features

- Enhanced 2nd Generation SiC MOSFET
- High blocking voltage with low on-resistance
- High speed switching with low capacitance
- Fast intrinsic diode with low reverse recovery

Applications

- EV Chargers
- SMPS
- Solar Inverters
- Motor Drives
- DC/DC Converters

Absolute Maximum Ratings

Stress beyond those listed under absolute maximum ratings may damage the device.

Parameter	Symbol		Rating	Unit
Drain-Source Voltage, across T_{vj}	$V_{DS(max)}$		1200	V
Maximum Gate-Source Voltage, Peak Transient Capability	$V_{GS(max)}$		-10/+25	V
Continuous Drain Current, V_{GS} = 15V, assumes die packaged in TO-247 package with $R_{th(j-c)}$ < 0.35 K/W	I_D	T_c = 25°C	81	A
		T_c = 100°C	60	
Pulsed Drain Current, t_p limited by $T_{vj(max)}$	$I_{D(pulse)}$		200	A
Virtual Junction and Storage Temperature	T_{VJ}, T_{stg}		-55 to +175	°C
Maximum Processing Temperature, in non-reactive ambient	T_{proc}		325	°C

Recommended Operating Conditions

Parameter	Symbol	Rating	Unit
Recommended Operating Gate - Source Voltage	$V_{GS(op)}$	-5/+20	V

Electrical Characteristics (T_{VJ} = 25˚C)

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	1200			V	V_{GS} = 0 V, I_D =100 µA
Gate Threshold Voltage	$V_{GS(th)}$	2	2.9	4	V	V_{DS} = V_{GS}, I_{DS} = 15 mA
			2.4		V	V_{DS} = V_{GS}, I_{DS} = 15 mA, T_{VJ} = 175°C
Zero Gate Voltage Drain Current	I_{DSS}		1	100	µA	V_{DS} = 1200 V, V_{GS} = 0 V
Gate-Source Leakage Current	I_{GSS}			250	nA	V_{GS} = 20 V, V_{DS} = 0 V
Drain-Source On-State Resistance	$R_{DS(on)}$		25	34	mΩ	V_{GS} = 20 V, I_D = 50 A
			43			V_{GS} = 20 V, I_D = 50 A, T_{VJ} = 175°C
Transconductance	g_{fs}		27.5		S	V_{DS} = 20 V, I_{DS} = 50 A
			24.6			V_{DS} = 20 V, I_{DS} = 50 A, T_{VJ} = 175°C
Input Capacitance	C_{iss}		3350		pF	V_{GS} = 0 V, V_{DS} = 1000 V f = 1 Mhz V_{AC} = 25 mV
Output Capacitance	C_{oss}		235			
Reverse Transfer Capacitance	C_{rss}		24			
C_{oss} Stored Energy	E_{oss}		126		µJ	V_{DS} = 1000 V, f = 1 Mhz
Internal Gate Resistance	$R_{G(int)}$		1.26		Ω	f = 1 Mhz
Gate to Source Charge	Q_{gs}		46		nC	V_{DS} = 800 V, V_{GS} = -5 V/20 V I_{DS} = 50 A
Gate to Drain Charge	Q_{gd}		50			
Total Gate Charge	Q_g		161			

Reverse Diode Characteristics (T_{VJ} = 25˚C)

Characteristics	Symbol	Typ.	Max.	Unit	Test Conditions
Diode Forward Voltage	V_{SD}	4.1		V	V_{GS} = -4 V, I_{SD} = 25 A
		3.7		V	V_{GS} = -4 V, I_{SD} = 25 A, T_{VJ} = 175 °C
Reverse Recovery Time	t_{rr}	45		ns	V_{GS} = -4 V, I_{SD} = 50 A, V_R = 800 V dif/dt = 1000 A/µs, T_{VJ} = 175 °C
Reverse Recovery Charge	Q_{rr}	406		nC	
Peak Reverse Recovery Current	I_{rrm}	13.5		A	

Typical Performance

All the graphs are based on a die placed in a TO-247-4L package.

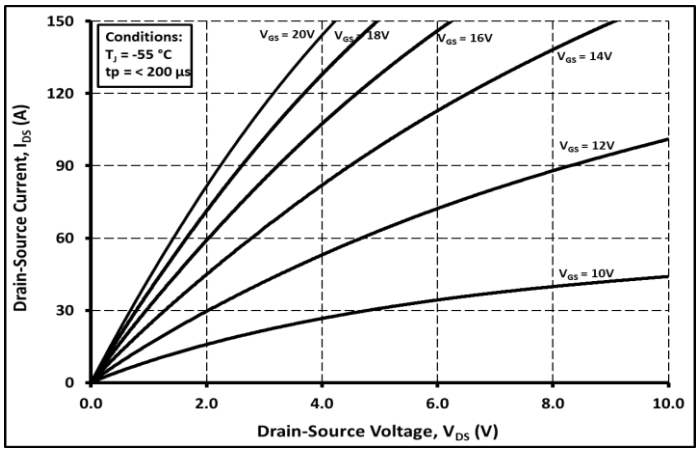


Figure 1.

Output Characteristics T_{vj} = -55 °C

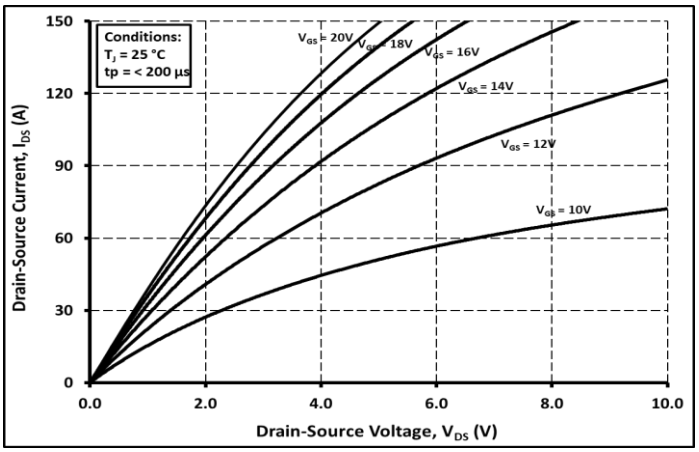


Figure 2.

Output Characteristics T_{vj} = 25 °C

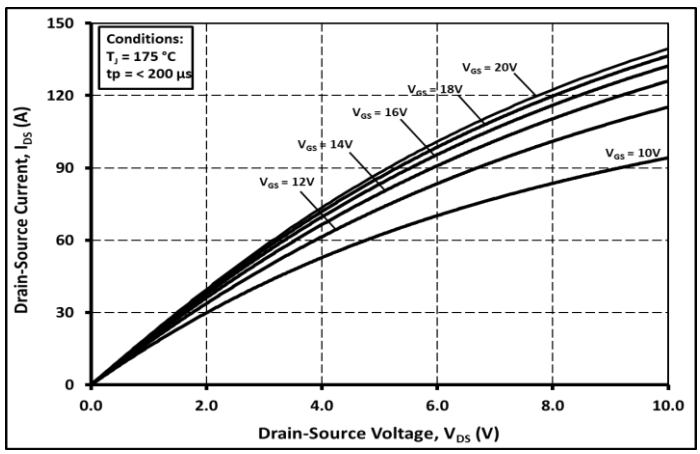


Figure 3.

Output Characteristics T_{vj} = 175 °C

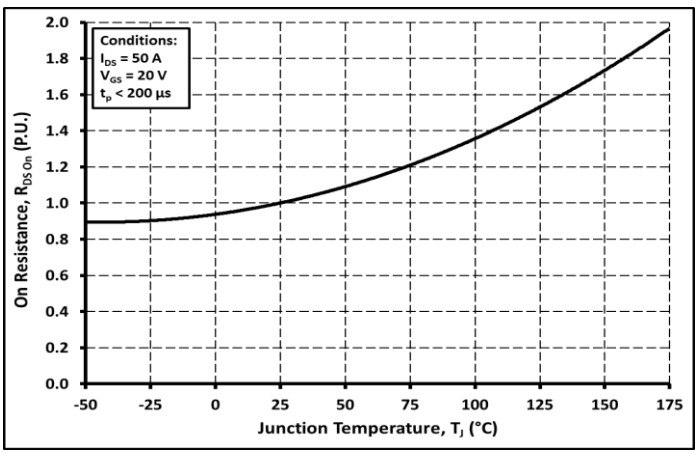


Figure 4.

Normalized On-Resistance vs. Temperature

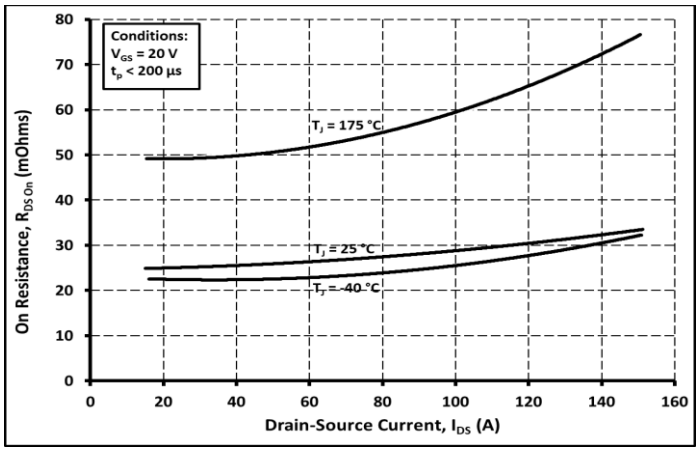


Figure 5.

On-Resistance vs. Drain Current For Various Temperatures

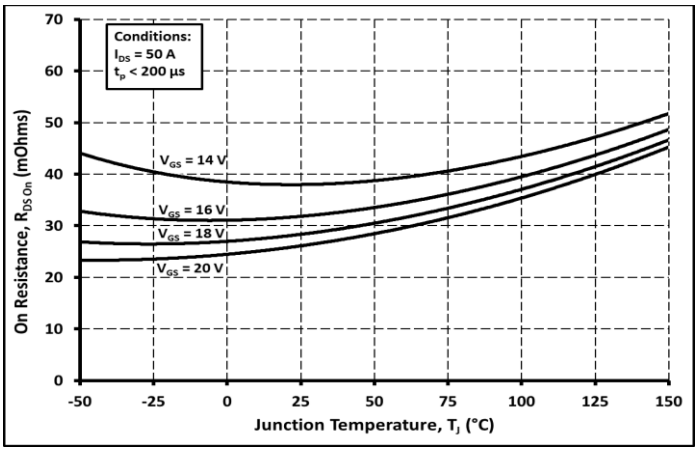


Figure 6.

On-Resistance vs. Temperature For Various Gate Voltages

Typical Performance

All the graphs are based on a die placed in a TO-247-4L package.

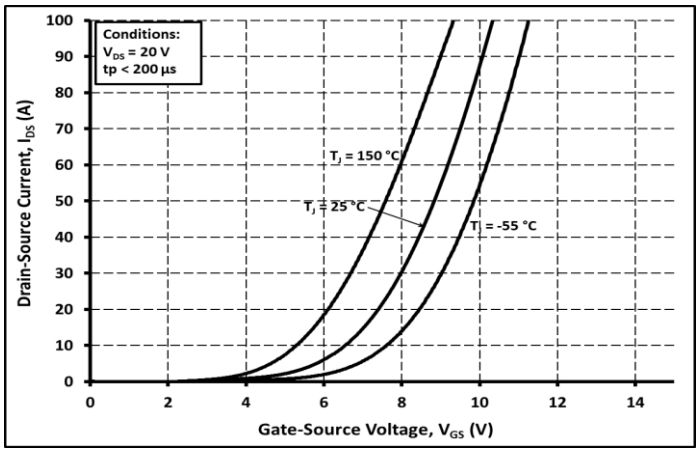


Figure 7.

Transfer Characteristic For Various Junction Temperatures

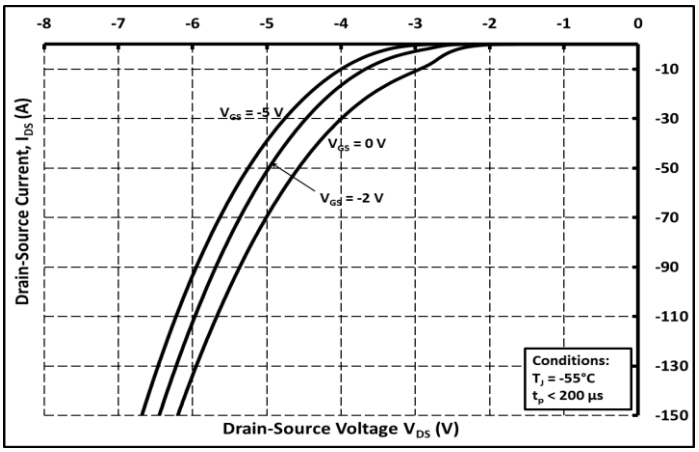


Figure 8.

Body Diode Characteristic at T_{vj} = -55 °C

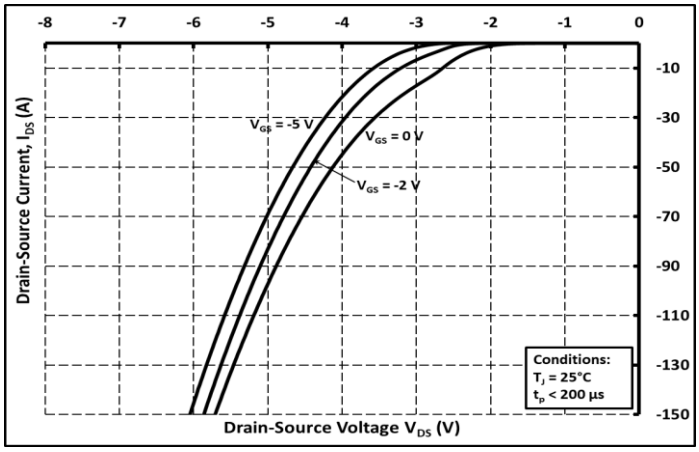


Figure 9.

Body Diode Characteristic at T_{vj} = 25 °C

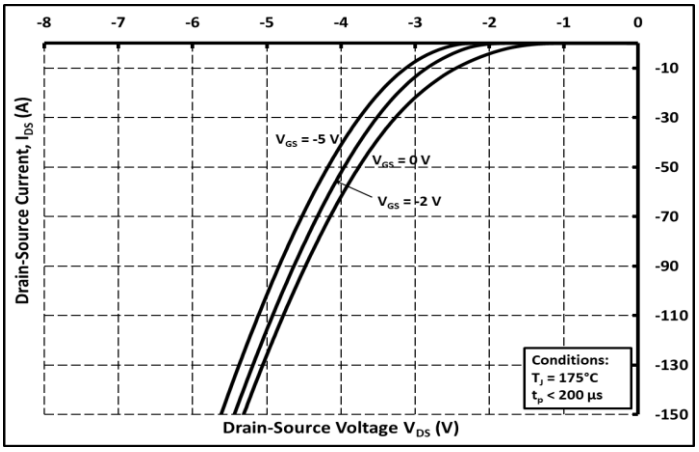


Figure 10.

Body Diode Characteristic at T_{vj} = 175 °C

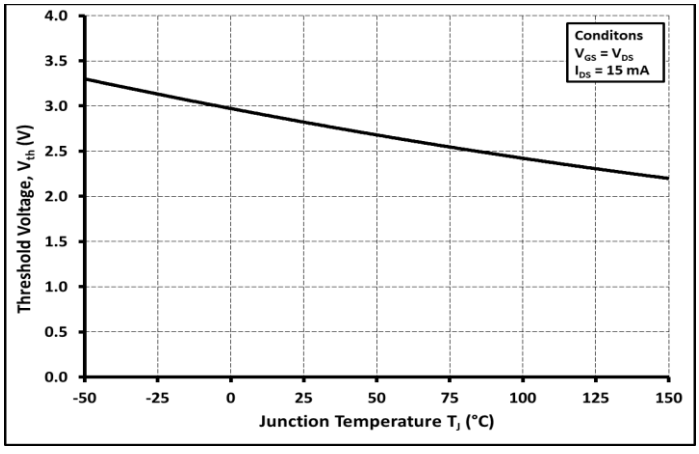


Figure 11.

Threshold Voltage vs. Temperature

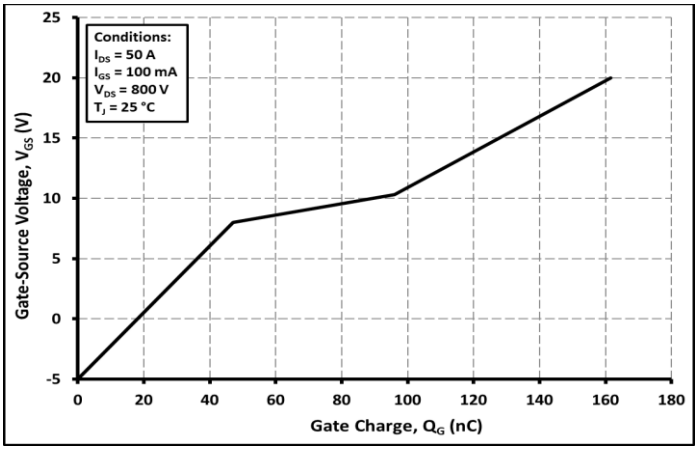


Figure 12.

Gate Charge Characteristics

Typical Performance

All the graphs are based on a die placed in a TO-247-4L package.

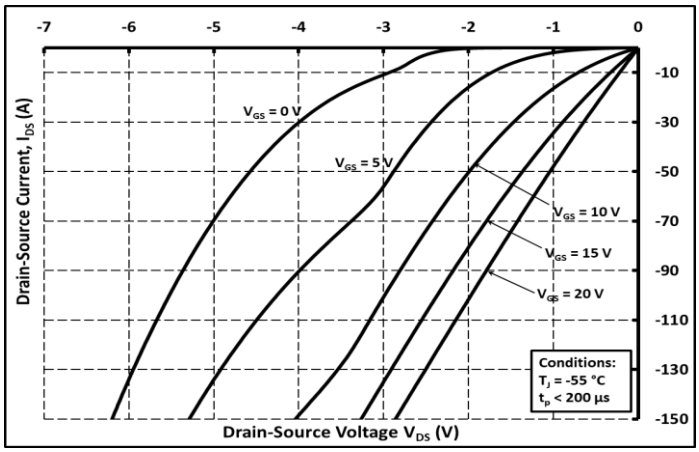


Figure 13.

3rd Quadrant Characteristic at T_{vj} = -55 °C

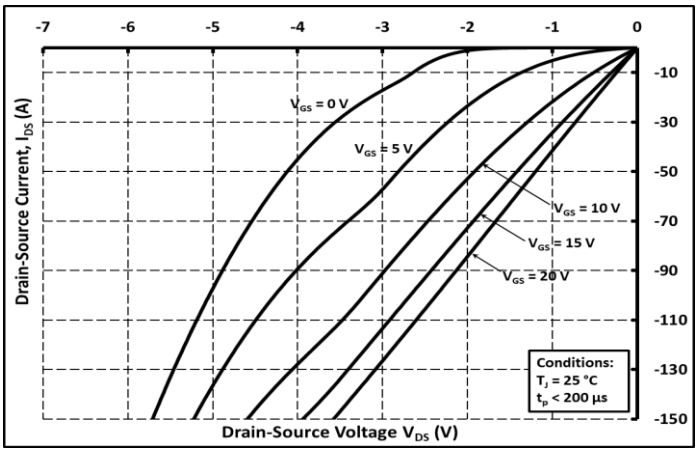


Figure 14.

3rd Quadrant Characteristic at T_{vj} = 25 °C

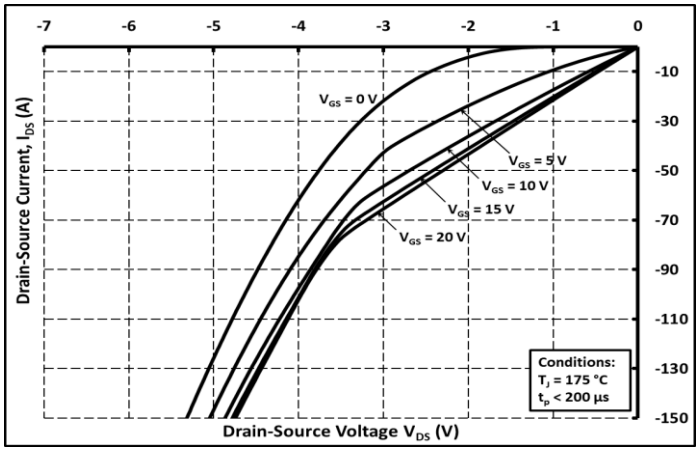


Figure 15.

3rd Quadrant Characteristic at T_{vj} = 175 °C

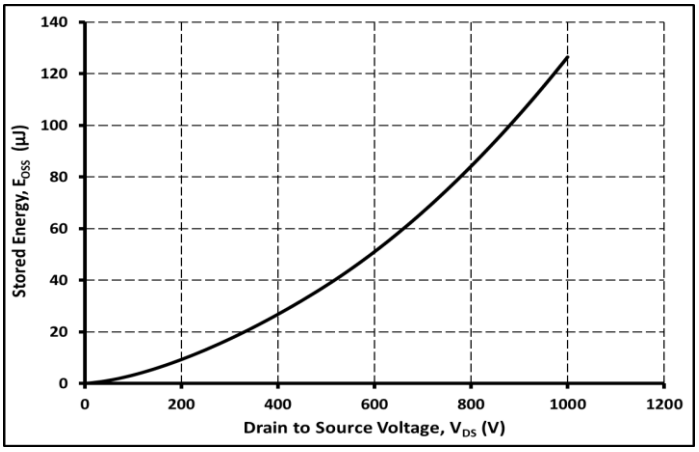


Figure 16.

Output Capacitor Stored Energy

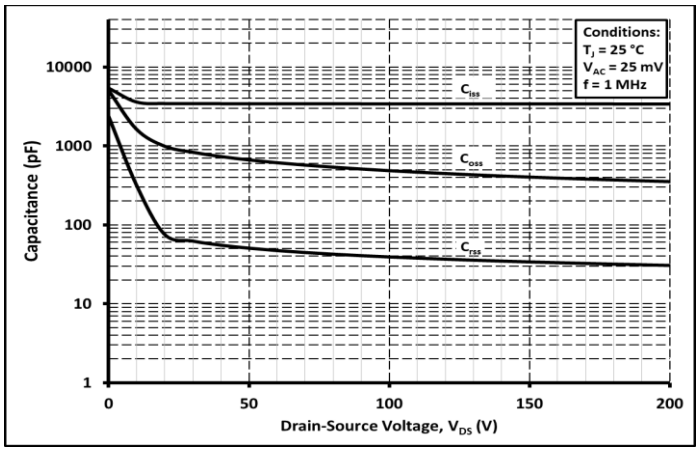


Figure 17.

Capacitances vs. Drain-Source Voltage (0-200V)

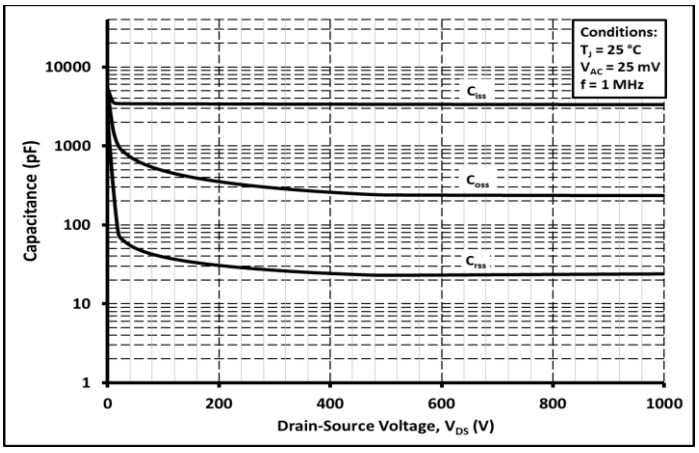
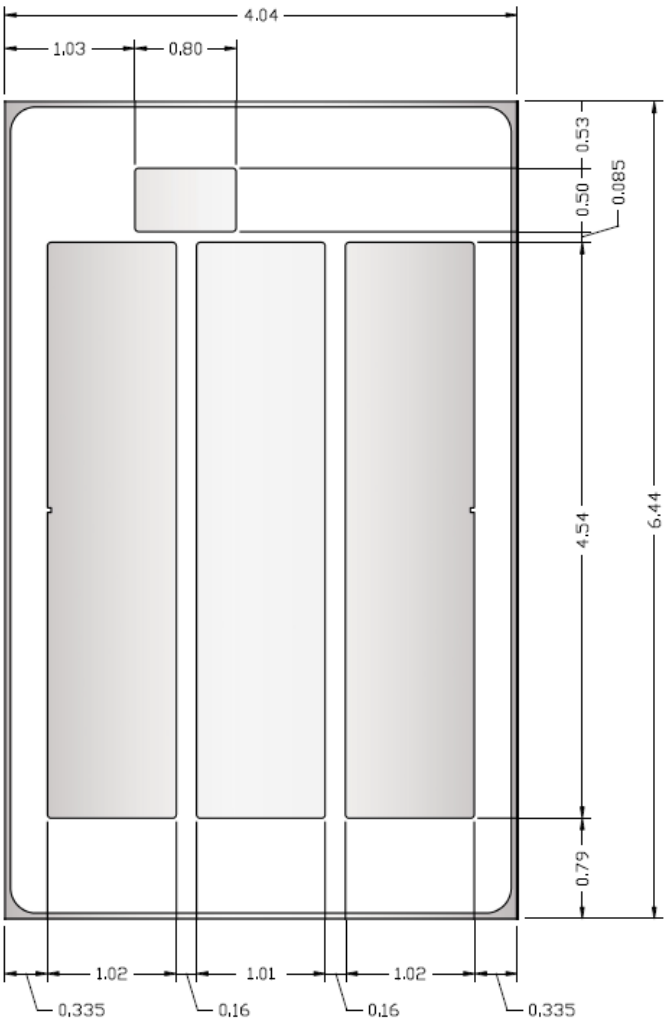


Figure 18.

Capacitances vs. Drain-Source Voltage (0-1200V)

Product Dimensions CPM2-1200-0025A



Product Dimensions CPM2-1200-0025A

Parameter	Typical	Units
Die Size (L x W)	4.04 x 6.44	mm
Exposed Source Pad Metal Dimensions	1.0 x 4.54 (x3)	mm
Gate Pad Dimensions	0.50 x 0.80	mm
Chip Thickness[1]	180 ± 40	µm
Frontside (Source) metalization (Al)	4	µm
Frontside (Gate) metalization (Al)	4	µm
Backside (Drain) metalization (Ni/Au)	0.8 / 0.1	µm

[1] SiC wafer thickness

Product Ordering Information

Order Number	Description	Package
CPM2-1200-0025A-FY6	SiC MOSFET G2 IND 1200V/25mO UV MLT	Bare Die Product

Revision History

Revision History	Date of Change	Brief Summary
0		Initial Release
1	12/21/2023	• Template updated

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