

## **GEMOSTECH**

### **Dual 2.2W Audio Amplifier Plus Stereo Headphone**

### CR4863 Series

### **General Description**

will deliver

The CR4863 is a dual bridge-connected audio power amplifier which, when connected to a 5V supply, will deliver 2.2W to a  $4\Omega$  load (Note 1) or 2.5W to a  $3\Omega$ load (Note 2) with less than 1.0% THD+N. In addition, the headphone input pin allows the amplifiers to operate in single-ended mode when driving stereo headphones.

Boomer audio power amplifiers were designed specifically to provide high quality output power from a surface mount package while requiring few external components. To simplify audio system design, the CR4863 combines dual bridge speaker amplifiers and stereo headphone amplifiers on one chip.

The CR4863 features an externally controlled, low-power consumption shutdown mode, a stereo headphone amplifier mode, and thermal shutdown protection. It also utilizes circuitry to reduce "clicks and pops" during device turn-on.

Note 1: An CR4863MTE that has been properly mounted to a circuit board will deliver 2.2W into  $4\Omega$ . The other package options for the CR4863 will deliver 1.1W into  $8\Omega$ . See the Application Information sections for further information concerning the CR4863MTE .

Note 2: An CR4863MTE that has been properly mounted to a circuit board and forced-air cooled will deliver 2.5W into  $3\Omega$ .

#### **Feature**

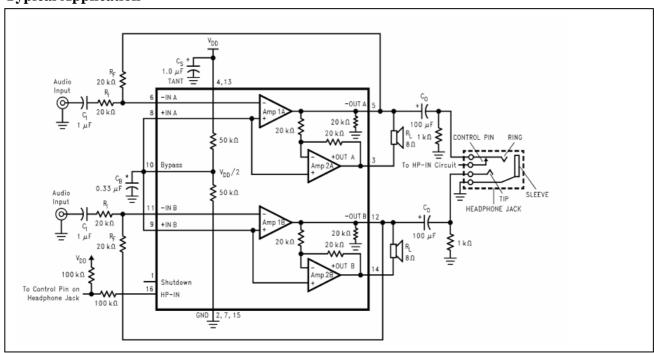
- Stereo headphone amplifier mode
- Thermal shutdown protection circuit
- "Click and pop" suppression circuit

#### **Application**

- Multimedia Monitors
- Portable DVD/VCD

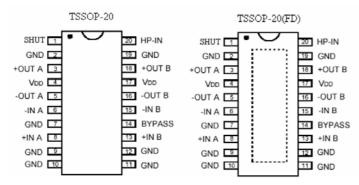
- TSSOP-20 (173mil) Pb-Free Packaging
- Unity-gain stable
- TSSOP-20 Exposed-DAP Pb-Free Packag
- Portable Televisions
- Portable and Desktop Computers

### **Typical Application**





### Pin Assignment



### **ELECTRICAL CHARACTERISTICS**

### **Absolute Maximum Ratings**

Symbol	Parameter	Min.	Max.	Units
$V_{DD}$	DC Supply Voltage		7.0	V
$V_{IN}$	Input Voltage	-0.3	VDD+0.3	V
$P_{\mathrm{D}}$	Power Dissipation	Internally Limited		
	ESD Susceptibility(HBM Model)		2000	V
$T_{J}$	Junction Temperature		150	$^{\circ}$
$T_{S}$	Storage Temperature	-55	150	$^{\circ}$

Note: Stress above those listed may cause permanent to the devices

### **Recommended Operating Conditions**(TA=25°C)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
$V_{DD}$	DC Supply Voltage		2.0		5.5	V
Ta	Temperature Range		-40		85	$^{\circ}$

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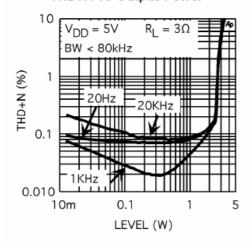
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
$I_{DD}$	Quiescent Power	$V_{IN}=0V,I_O=0A,HP-IN=0V$	6	15.7	20	A
	Supply Current	$V_{IN}=0V,I_O=0A,HP-IN=4V$		8.5		m A
$I_{SD}$	Shutdown Current	V <sub>DD</sub> applied to the Shut Pin	2	0.7		μΑ
$V_{\mathrm{IH}}$	Headphone High Input		4			V
	Voltage		4			
V <sub>IL</sub>	Headphone Low Input				0.8	V
	Voltage				0.8	v
$V_{OS}$	Output Offset Volt.	$V_{IN}=0V$		5	50	m A
$P_{O}$	Output Power (THD=1%,f=1KHZ)	RL=3Ω(TSSOP-20FD)		2.5		W
		RL=4Ω(TSSOP-20FD)		2.2		
		RL=8Ω		1.1		
$P_{\mathrm{O}}$	Output Power (THD+N=10%,f=1KHZ)	RL=3Ω(TSSOP-20FD)		3.2		W
		RL=4Ω(TSSOP-20FD)		2.7		
		RL=8Ω		1.5		
PSRR	Power Supply Rejection	$V_{DD}$ =5V,RL=8 $\Omega$ ,C <sub>B</sub> =1 $\mu$ F	66	66		d B
	Ratio	$V_{RIPPLE}=200 \text{mV}_{RMS}$		66		
SNR	Signal to Noise Ratio	$V_{DD}$ =5 $V$ , $P_{O}$ =1.1 $W$ , $RL$ =8 $\Omega$		96		d B
$X_{TALK}$	Channel Separation	f=1KHZ,CB=1μF		89		d B



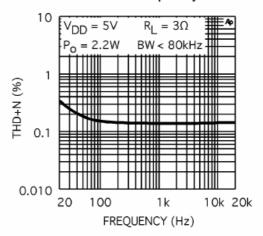


### **Typical Performance Characteristics for TSSOP-20FD**

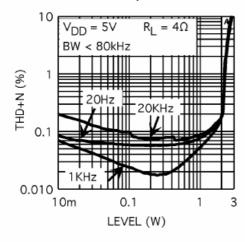
### THD+N vs Output Power



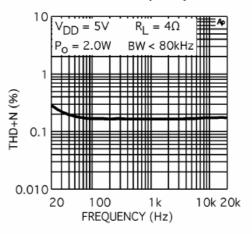
#### THD+N vs Frequency



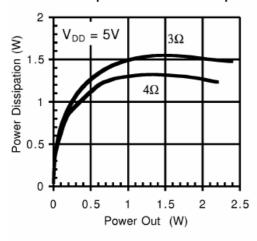
#### THD+N vs Output Power



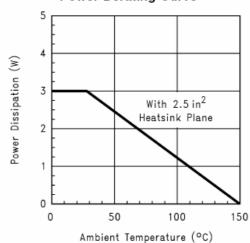
#### THD+N vs Frequency



### Power Dissipation vs Power Output



#### **Power Derating Curve**



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### **Packaging Information**

