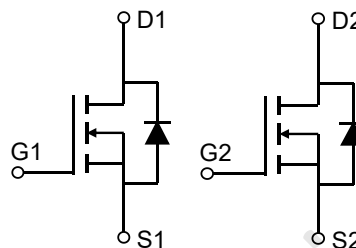


Description

Features

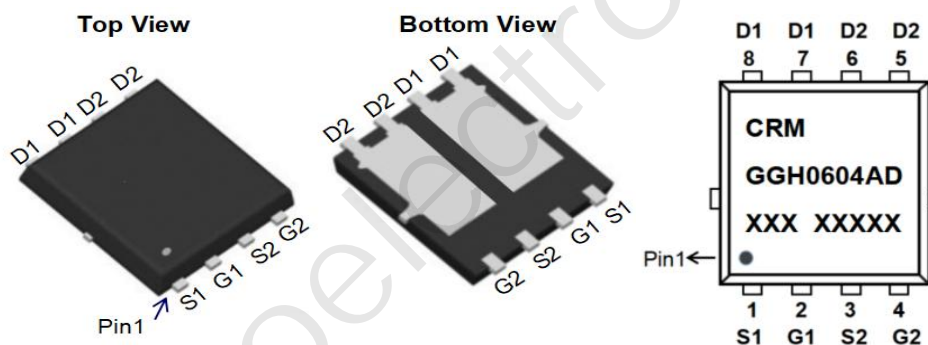
- 60V, 65A
 $R_{DS(ON)}$ Typ = 5.2mΩ @ $V_{GS} = 10V$
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_d s TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGH0604AD	CRMGGH0604AD	PDFN5x6-8L-D	TAPING	13"	5000	60000

Absolute Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	60	V
V_{GS}	Gate-to-Source Voltage	±20	V
I_D	Continuous Drain Current	$T_C = 25^\circ C$	65
		$T_C = 100^\circ C$	39
I_{DM}	Pulsed Drain Current ⁽¹⁾	260	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	121	mJ
P_D	Power Dissipation	$T_C = 25^\circ C$	51
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.45	$^\circ C/W$
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ C$

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.4	2.8	3.6	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 10V, I _D = 20A	-	5.2	6.8	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 30V, f = 1MHz	-	1563	-	pF
C _{oss}	Output Capacitance		-	580	-	pF
C _{rss}	Reverse Transfer Capacitance		-	14	-	pF
Q _g	Total Gate Charge	V _{GS} = 0 to 10V V _{DS} = 30V, I _D = 20A	-	29	-	nC
Q _{gs}	Gate Source Charge		-	8.3	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	5.8	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} = 10V, V _{DD} = 30V I _D = 20A, R _{GEN} = 4.5Ω	-	10	-	ns
t _r	Turn-On Rise Time		-	28	-	ns
t _{d(off)}	Turn-Off DelayTime		-	20	-	ns
t _f	Turn-Off Fall Time		-	95	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	65	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	260	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 20A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I _F = 20A, di/dt = 100A/us	-	31	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	19	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting T_J=25°C, V_{DD}=15V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=22A
 3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

Test Circuit

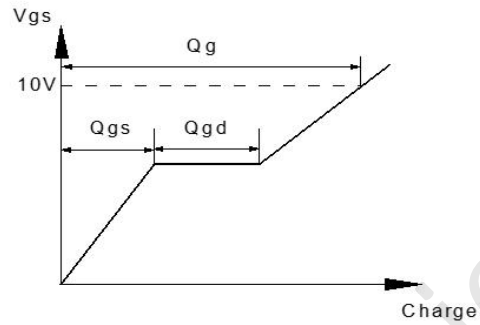
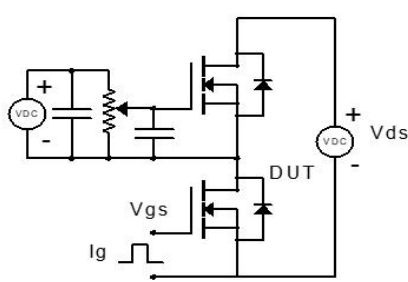


Figure 1: Gate Charge Test Circuit & Waveform

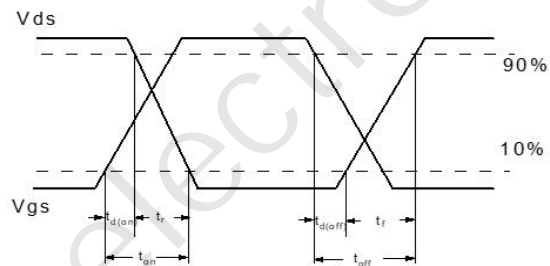
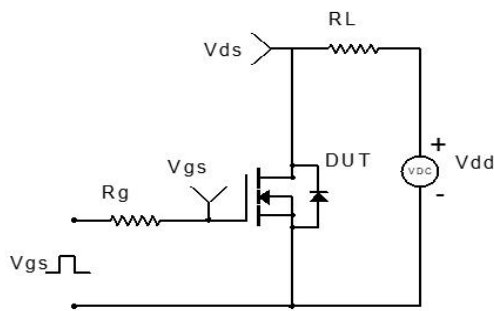


Figure 2: Resistive Switching Test Circuit & Waveform

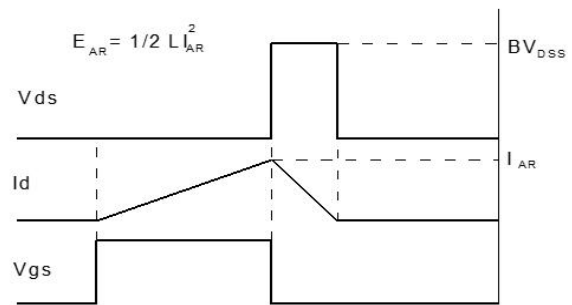
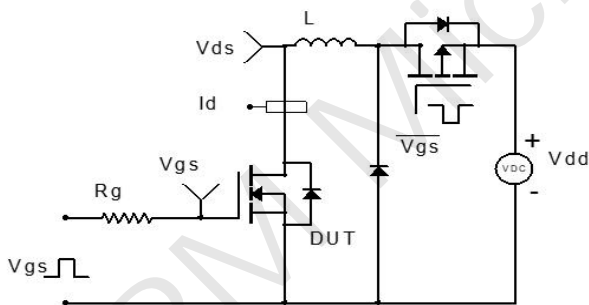


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

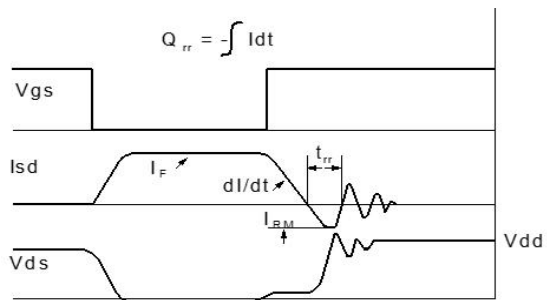
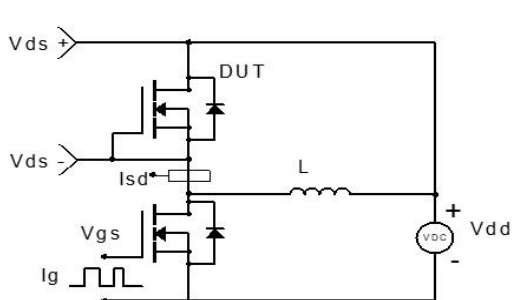
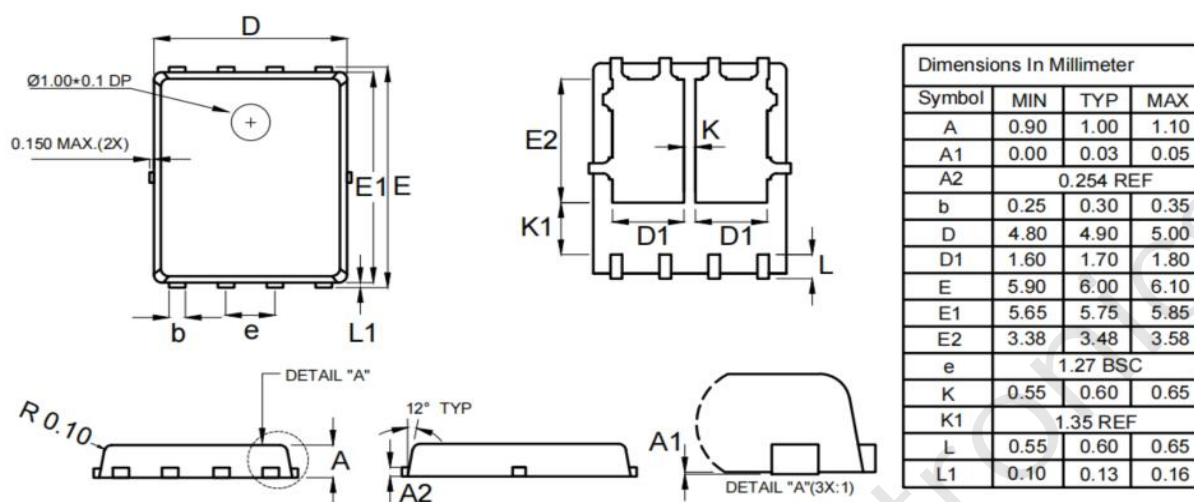


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(PDFN5x6-8L-D)




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Contact information

For more information, please visit: <http://www.crm-semi.tech>

For sales information, please send an email to: sales@crm-semi.com