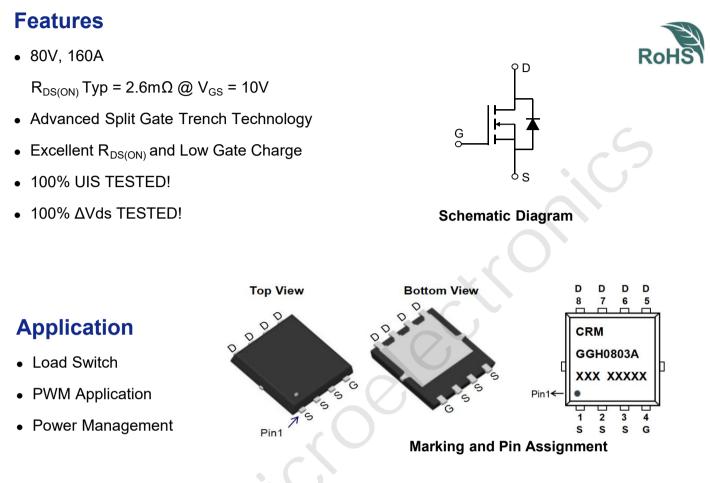


# CRMGGH0803A

N-Channel 80V, 2.6mΩ Typ. Power MOSFET

### Description



#### Package Marking and Ordering Information

| Device      | Marking     | Package    | Outline | Reel Size | Reel (pcs) | Per Carton (pcs) |
|-------------|-------------|------------|---------|-----------|------------|------------------|
| CRMGGH0803A | CRMGGH0803A | PDFN5x6-8L | TAPING  | 13"       | 5000       | 60000            |

#### Absolute Maximum Ratings (@ T<sub>J</sub> = 25°C unless otherwise specified)

| Symbol                       | Parameter                                     |                        | Value      | Units |
|------------------------------|---|------------------------|------------|-------|
| V <sub>DS</sub>              | Drain-to-Source Voltage                       |                        | 80         | V     |
| V <sub>GS</sub>              | Gate-to-Source Voltage                        |                        | ±20        | V     |
| ID                           | Continuous Drain Current                      | T <sub>C</sub> = 25°C  | 160        | А     |
|                              |   | T <sub>C</sub> = 100°C | 96         | А     |
| I <sub>DM</sub>              | Pulsed Drain Current <sup>(1)</sup>           |                        | 640        | А     |
| E <sub>AS</sub>              | Single Pulsed Avalanche Energy <sup>(2)</sup> |                        | 462        | mJ    |
| P <sub>D</sub>               | Power Dissipation                             | T <sub>C</sub> = 25°C  | 156        | W     |
| $R_{	extsf{	heta}JC}$        | Thermal Resistance, Junction to Case          |                        | 0.8        | °C/W  |
| <b>Τ</b> J, T <sub>STG</sub> | Junction & Storage Temperature Range          |                        | -55 to 150 | °C    |



#### **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

| Symbol               | Parameter  | Conditions   | Min.     | Тур. | Max. | Unit |
|----------------------|--|--|----------|------|------|------|
| Off Char             | acteristics  |  |          |      |      |      |
| V <sub>(BR)DSS</sub> | Drain-Source Breakdown Voltage                           | $I_{\rm D}$ = 250 $\mu$ A, $V_{\rm GS}$ = 0V             | 80       | -    | -    | V    |
| I <sub>DSS</sub>     | Zero Gate Voltage Drain Current                          | V <sub>DS</sub> = 80V, V <sub>GS</sub> = 0V              | -        | -    | 1.0  | μΑ   |
| I <sub>GSS</sub>     | Gate-Body Leakage Current                                | $V_{DS} = 0V, V_{GS} = \pm 20V$                          | -        | -    | ±100 | nA   |
| On Char              | acteristics  |  |          |      | 6    |      |
| V <sub>GS(th)</sub>  | Gate Threshold Voltage                                   | $V_{DS}$ = $V_{GS}$ , $I_D$ = 250 $\mu$ A                | 2        | 3    | 4    | V    |
| R <sub>DS(ON)</sub>  | Static Drain-Source ON-Resistance <sup>(3)</sup>         | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A              | -        | 2.6  | 3.4  | mΩ   |
| Dynamic              | Characteristics  |  |          |      |      |      |
| C <sub>iss</sub>     | Input Capacitance  |  | -        | 6059 | -    | pF   |
| C <sub>oss</sub>     | Output Capacitance                                       | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 40V,<br>f = 1MHz | -        | 1696 | -    | pF   |
| C <sub>rss</sub>     | Reverse Transfer Capacitance                             |  | Χ-       | 46   | -    | pF   |
| Qg                   | Total Gate Charge  | (  | -        | 88   | -    | nC   |
| $Q_gs$               | Gate Source Charge                                       | $V_{GS} = 0$ to 10V<br>$V_{DS} = 40V$ , $I_{D} = 20A$    | <u> </u> | 38   | -    | nC   |
| $Q_{gd}$             | Gate Drain("Miller") Charge                              | $v_{\rm DS} = 40 v, I_{\rm D} = 20 A$                    | -        | 22   | -    | nC   |
| Switchin             | g Characteristics  |  |          |      |      |      |
| t <sub>d(on)</sub>   | Turn-On DelayTime  |  | -        | 28   | -    | ns   |
| t <sub>r</sub>       | Turn-On Rise Time  | V <sub>GS</sub> = 10V, V <sub>DD</sub> = 40V             | -        | 32   | -    | ns   |
| $t_{d(off)}$         | Turn-Off DelayTime                                       | $I_D$ = 20A, $R_{GEN}$ = 6 $\Omega$                      | -        | 65   | -    | ns   |
| t <sub>f</sub>       | Turn-Off Fall Time                                       |  | -        | 40   | -    | ns   |
| Drain-So             | urce Diode Characteristics and M                         | lax Ratings  |          |      |      |      |
| I <sub>S</sub>       | Maximum Continuous Drain to Source Diode Forward Current |  |          | -    | 160  | А    |
| I <sub>SM</sub>      | Maximum Pulsed Drain to Source Diode Forward Current     |  | -        | -    | 640  | А    |
| $V_{SD}$             | Drain to Source Diode Forward Voltage                    | V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A               | -        | -    | 1.2  | V    |
| trr                  | Body Diode Reverse Recovery Time                         |  | -        | 70   | -    | ns   |
| Qrr                  | Body Diode Reverse Recovery Charge                       | I <sub>F</sub> = 20A, di/dt = 100A/us                    | _        | 142  | _    | nC   |

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E<sub>AS</sub> condition: Starting T<sub>J</sub>=25°C, V<sub>DD</sub>=40V, V<sub>G</sub>=10V, R<sub>G</sub>=25ohm, L=0.5mH, I<sub>AS</sub>=43A

3. Pulse Test: Pulse Width $\leqslant$ 300µs, Duty Cycle $\leqslant$ 0.5%.



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### **Test Circuit**

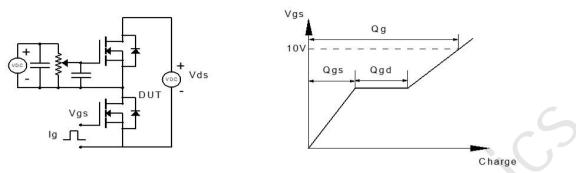
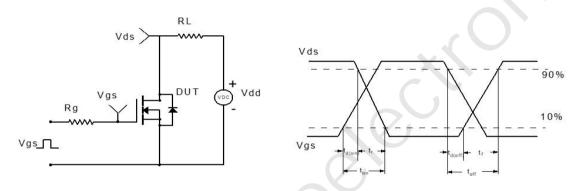
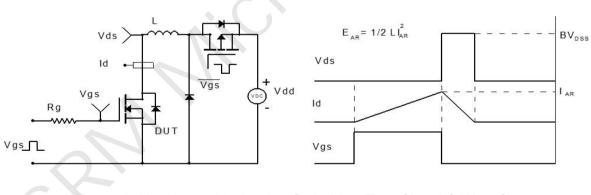


Figure 1: Gate Charge Test Circuit & Waveform



#### Figure 2: Resistive Switching Test Circuit & Waveform





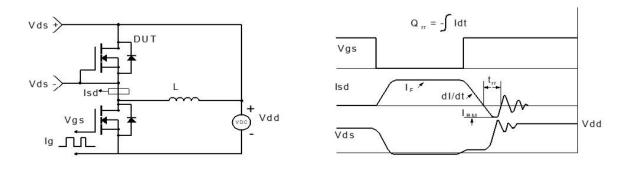
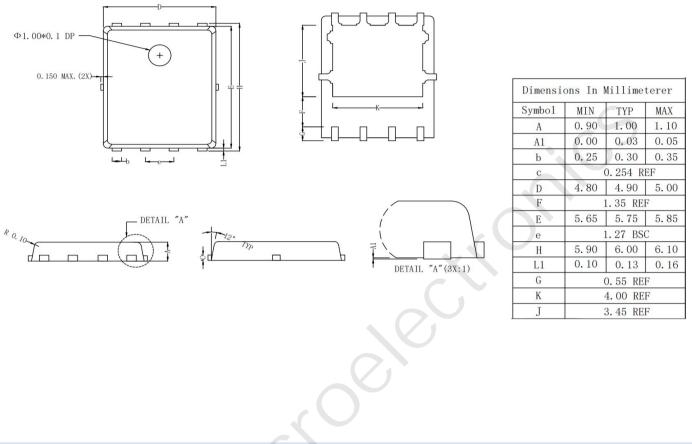


Figure 4: Diode Recovery Test Circuit & Waveform



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## Package Mechanical Data(PDFN5x6-8L)



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## **Contact information**

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