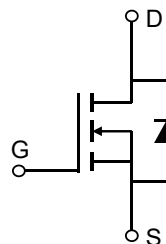


### Description

### Features

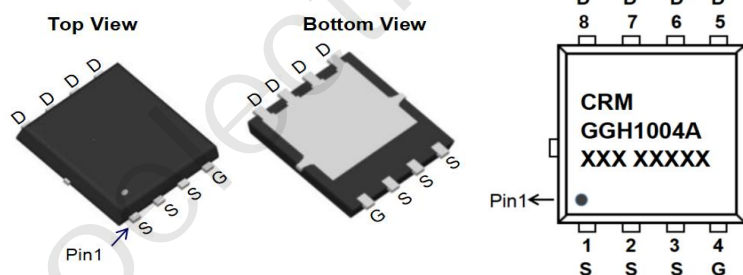
- 100V, 125A  
 $R_{DS(ON)}$  Typ = 3.6mΩ @  $V_{GS} = 10V$
- Advanced Split Gate Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS TESTED!
- 100%  $\Delta V_{ds}$  TESTED!



Schematic Diagram

### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGH1004A	CRMGGH1004A	PDFN5x6-8L	TAPING	13"	5000	60000

### Absolute Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	100	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$	125
		$T_C = 100^\circ C$	75
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	500	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	306	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$	133
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.94	$^\circ C/W$
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ C$

### Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	100	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.5	2.9	3.5	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 30A	-	3.6	4.7	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 50V, f = 1MHz	-	2767	-	pF
C <sub>oss</sub>	Output Capacitance		-	1383	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	23	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 50V, I <sub>D</sub> = 30A	-	74	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	28	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	20	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 50V I <sub>D</sub> = 30A, R <sub>GEN</sub> = 3Ω	-	16	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	35	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	50	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	30	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	125	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	500	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I <sub>F</sub> = 15A, di/dt = 100A/us	-	31	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	48	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. E<sub>AS</sub> condition: Starting T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, R<sub>G</sub>=25ohm, L=0.5mH, I<sub>AS</sub>=35A
  3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

**Typical Performance Characteristics**

Figure 1: Output Characteristics

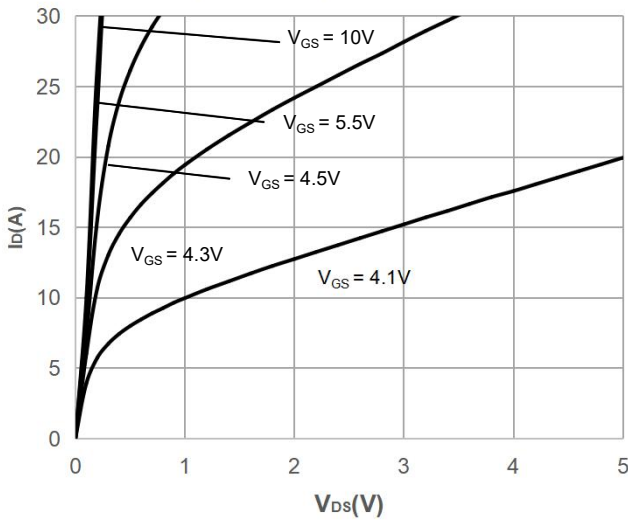


Figure 2: Typical Transfer Characteristics

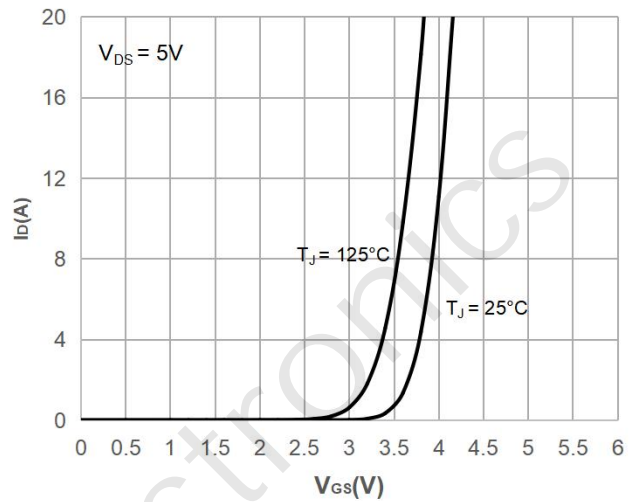


Figure 3: On-resistance vs. Drain Current

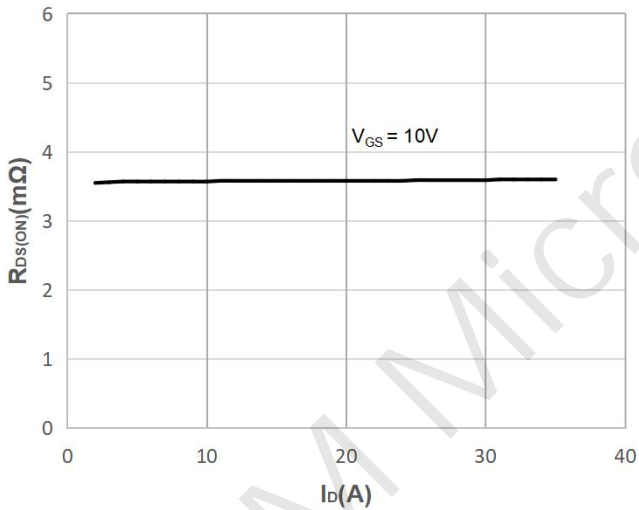


Figure 4: Body Diode Characteristics

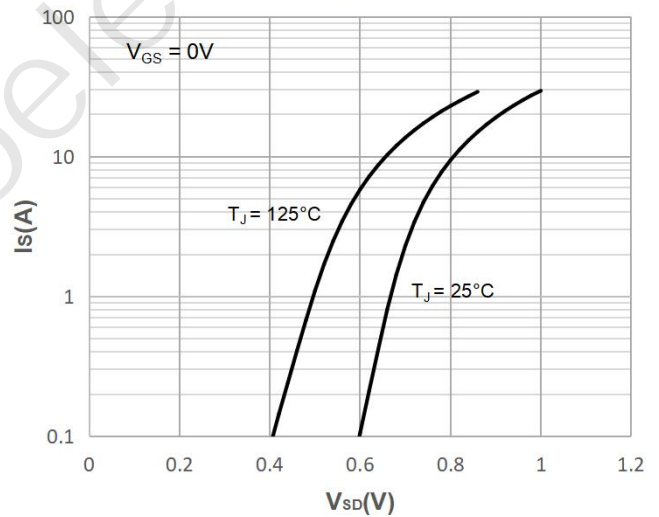


Figure 5: Gate Charge Characteristics

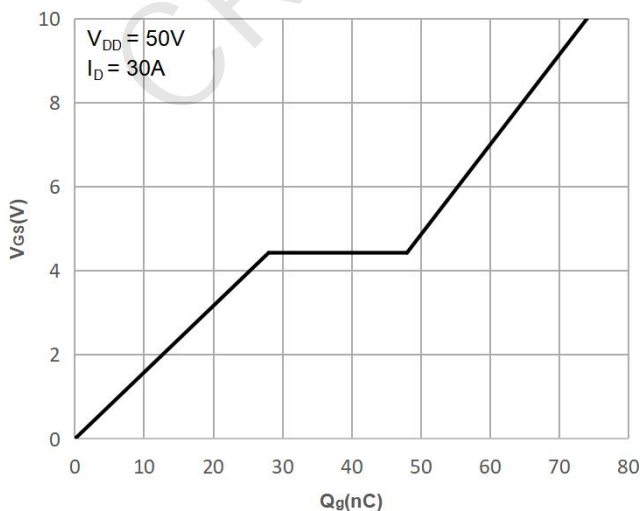
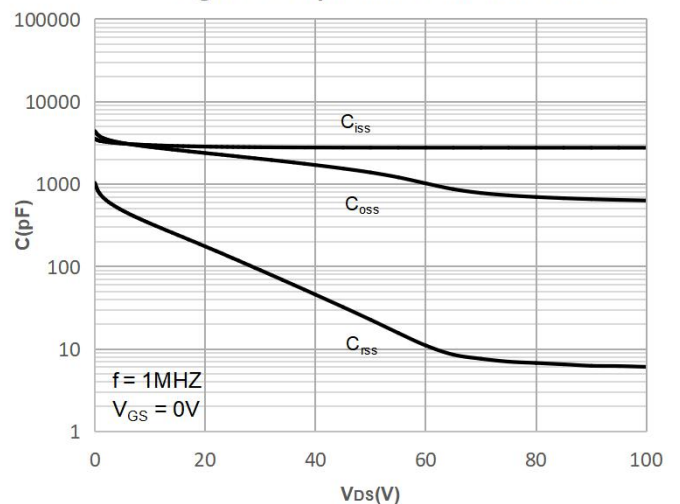


Figure 6: Capacitance Characteristics



### Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

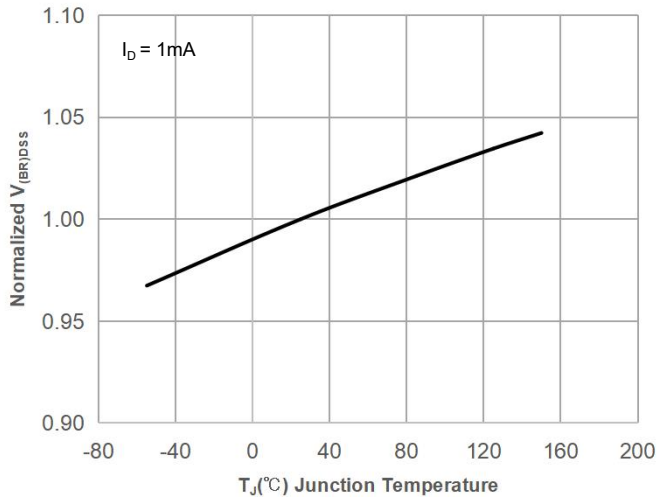


Figure 8: Normalized on Resistance vs. Junction Temperature

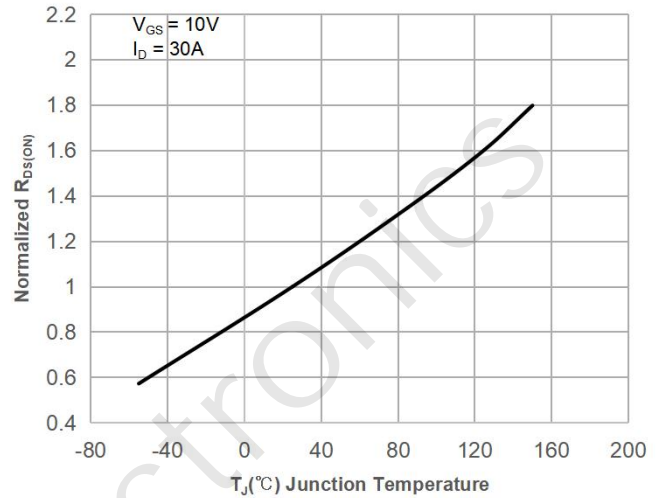


Figure 9: Maximum Safe Operating Area

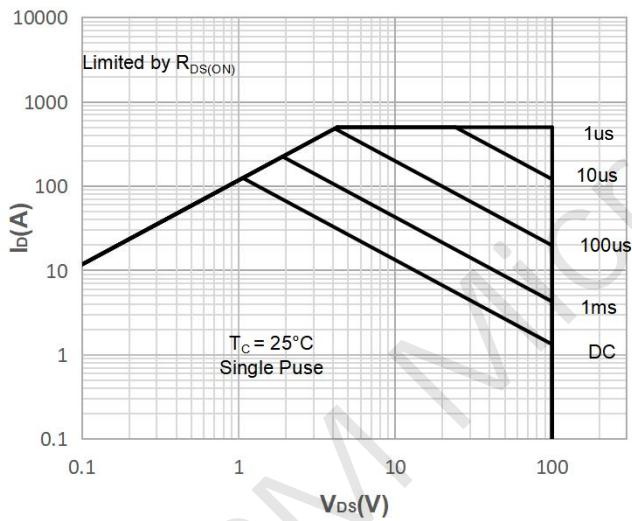


Figure 10: Maximum Continuous Driian Current vs. Case Temperature

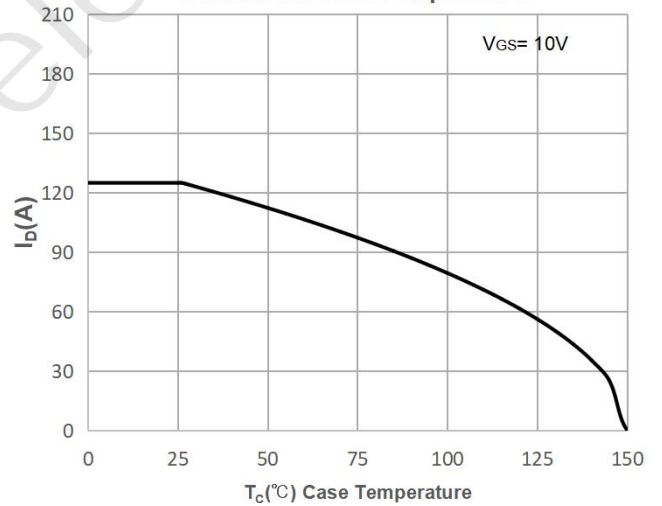


Figure 11: Normalized Maximum Transient Thermal Impedance

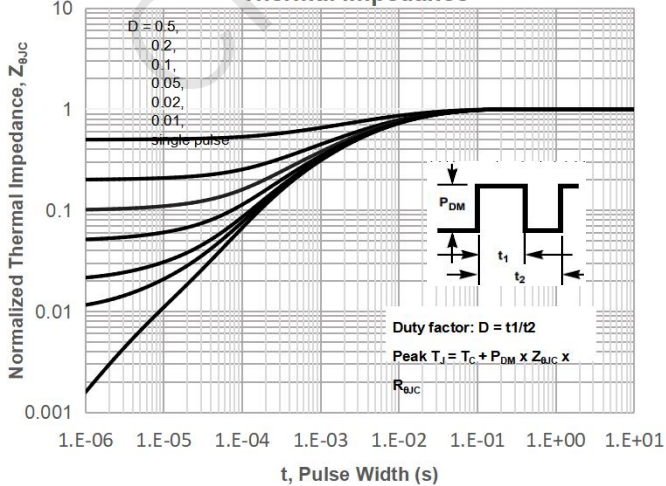
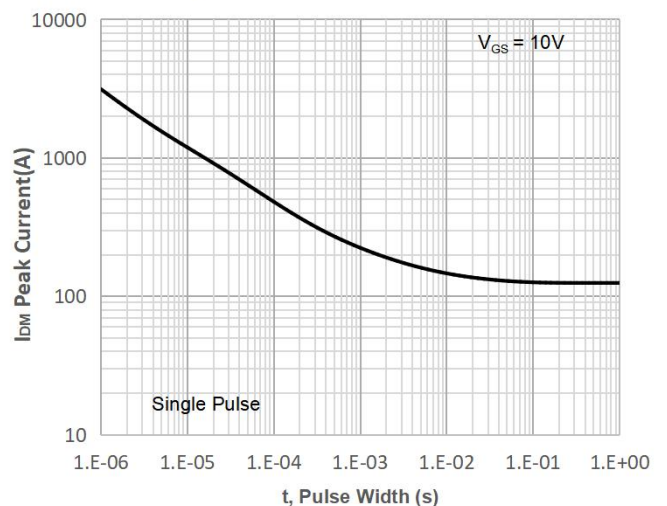
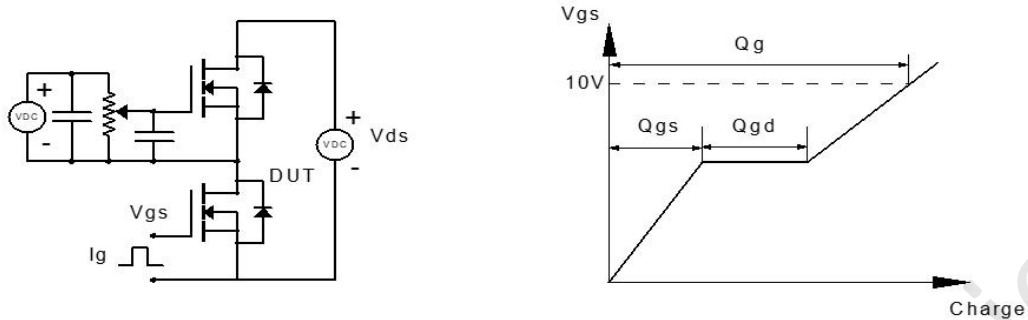


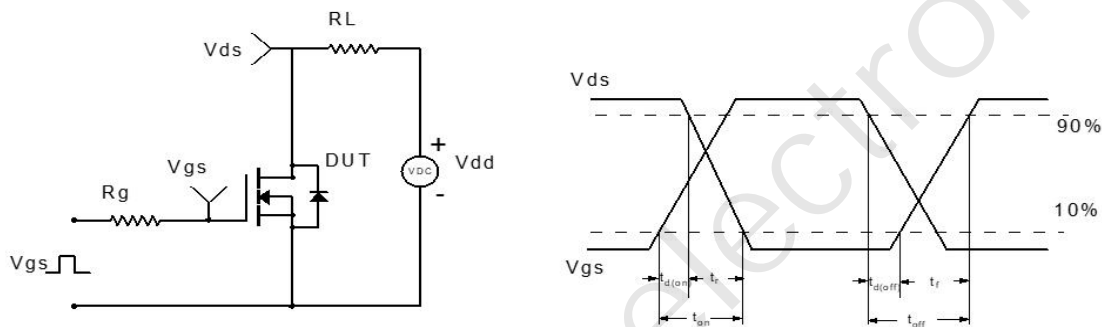
Figure 12: Peak Current Capacity



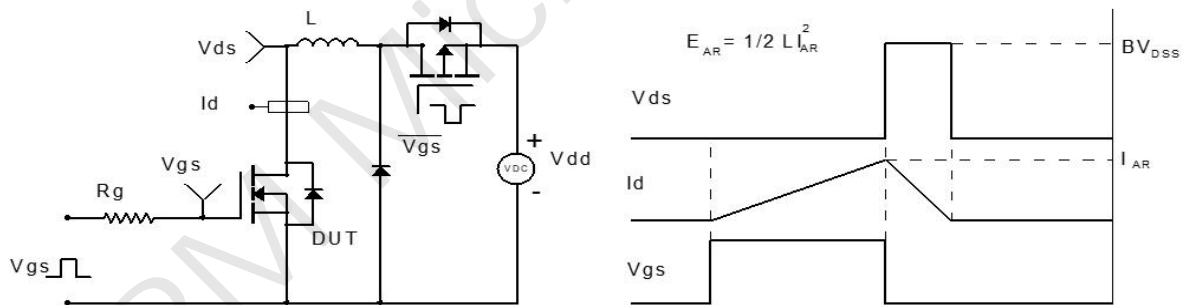
**Test Circuit**



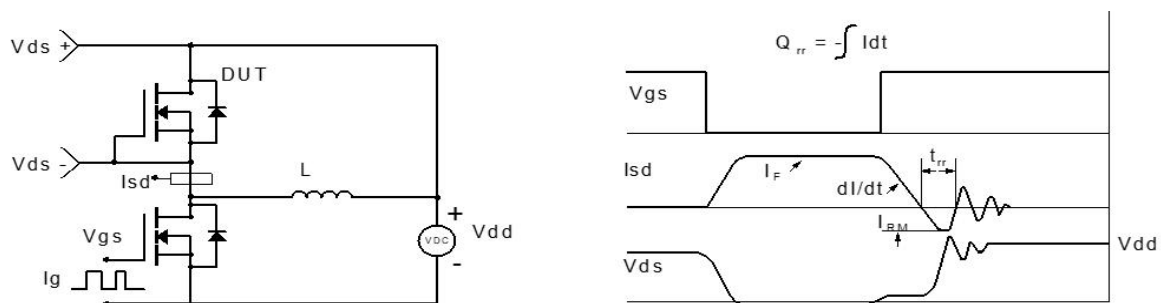
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**

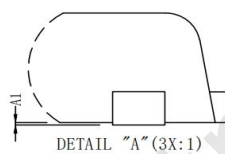
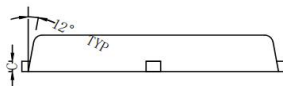
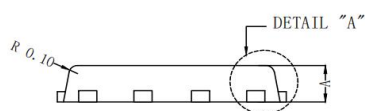
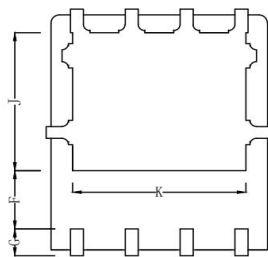
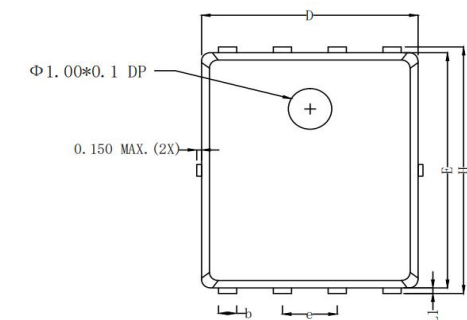


**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



**Figure 4: Diode Recovery Test Circuit & Waveform**

### Package Mechanical Data(PDFN5x6-8L)



Dimensions In Millimeterer			
Symbol	MIN	TYP	MAX
A	0.90	1.00	1.10
A1	0.00	0.03	0.05
b	0.25	0.30	0.35
c	0.254 REF		
D	4.80	4.90	5.00
F	1.35 REF		
E	5.65	5.75	5.85
e	1.27 BSC		
H	5.90	6.00	6.10
L1	0.10	0.13	0.16
G	0.55 REF		
K	4.00 REF		
J	3.45 REF		

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