CRMJBU2301G

P-Channel -20V, 59mΩ Typ. Power MOSFET

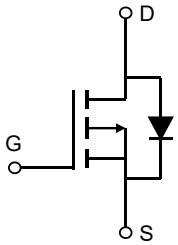
Description

Features

- -20V, -3A

  $R_{DS(ON)}$ Typ = 59mΩ @ V_{GS} = -4.5V

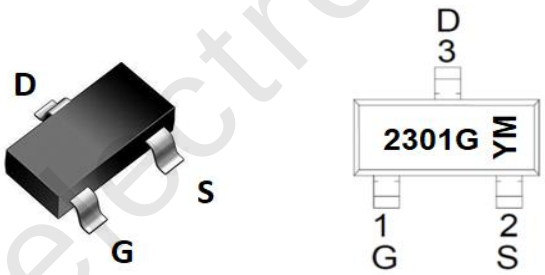
  $R_{DS(ON)}$ Typ = 80mΩ @ V_{GS} = -2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMJBU2301G	2301G	SOT-23-3L	TAPING	7"	3000	120000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		-20	V
V_{GS}	Gate-to-Source Voltage		±12	V
I_D	Continuous Drain Current	T_A = 25°C	-3	A
		T_A = 100°C	-1.8	A
I_{DM}	Pulsed Drain Current [1]		-12	A
P_D	Power Dissipation	T_A = 25°C	1	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient[2]		125	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = -250μA, V_{GS} = 0V	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = -20V, V_{GS} = 0V	-	-	-1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±12V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = -250μA	-0.4	-0.65	-1	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(3)	V_{GS} = -4.5V, I_D = -2A	-	59	76	mΩ
		V_{GS} = -2.5V, I_D = -1A	-	80	104	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = -10V, f = 1MHz	-	258	-	pF
C_{oss}	Output Capacitance		-	37	-	pF
C_{rss}	Reverse Transfer Capacitance		-	31	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to -4.5V V_{DS} = -10V, I_D = -2.5A	-	2.9	-	nC
Q_{gs}	Gate Source Charge		-	0.45	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	0.75	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = -4.5V, V_{DD} = -10V I_D= -2.5A, R_{GEN} = 3Ω	-	9.8	-	ns
t_r	Turn-On Rise Time		-	4.9	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	20.5	-	ns
t_f	Turn-Off Fall Time		-	7	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-3	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-12	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = -2A	-	-	-1.2	V
trr	Body Diode Reverse Recovery Time	I_F = -2A, di/dt = 100A/us	-	4.3	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	0.6	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Test Circuit

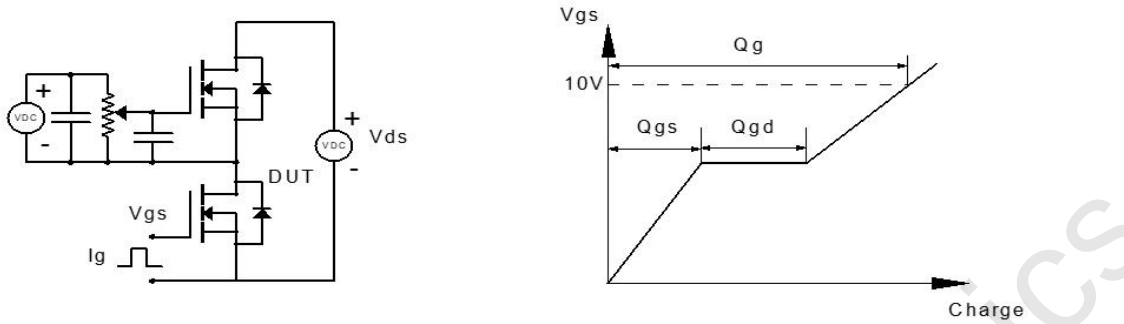


Figure 1: Gate Charge Test Circuit & Waveform

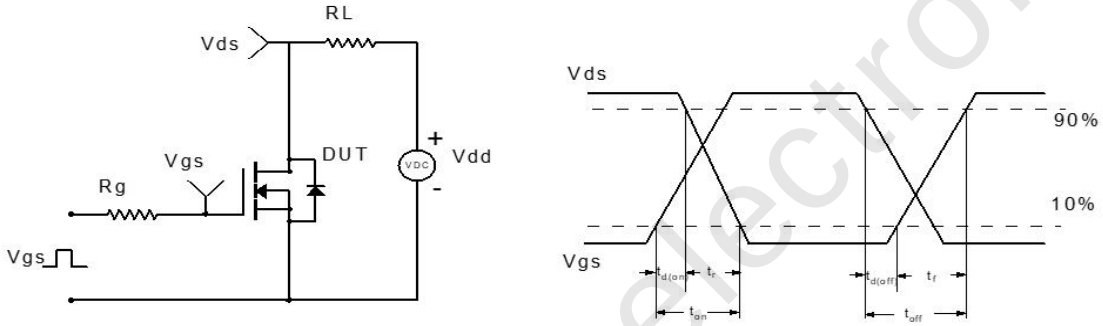


Figure 2: Resistive Switching Test Circuit & Waveform

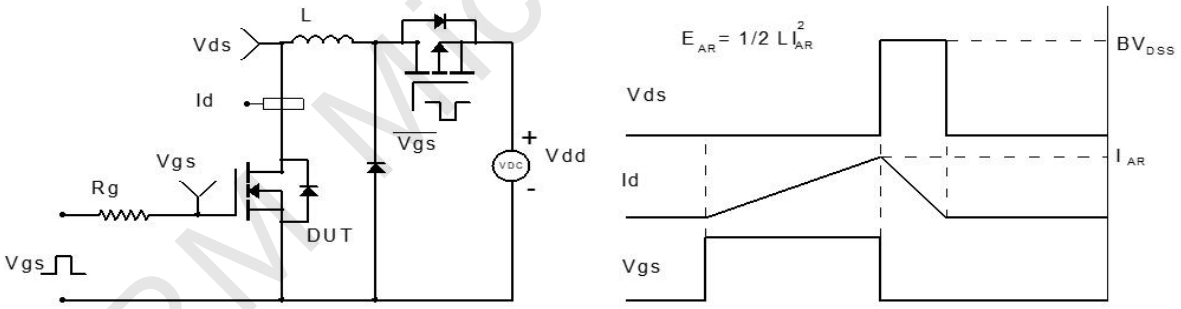


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

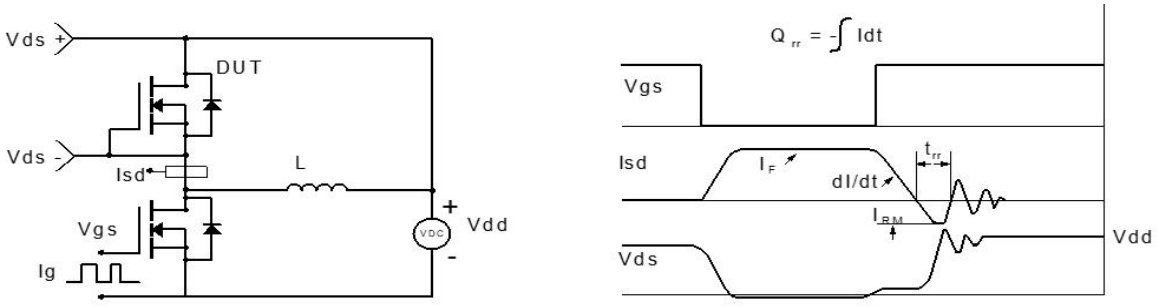
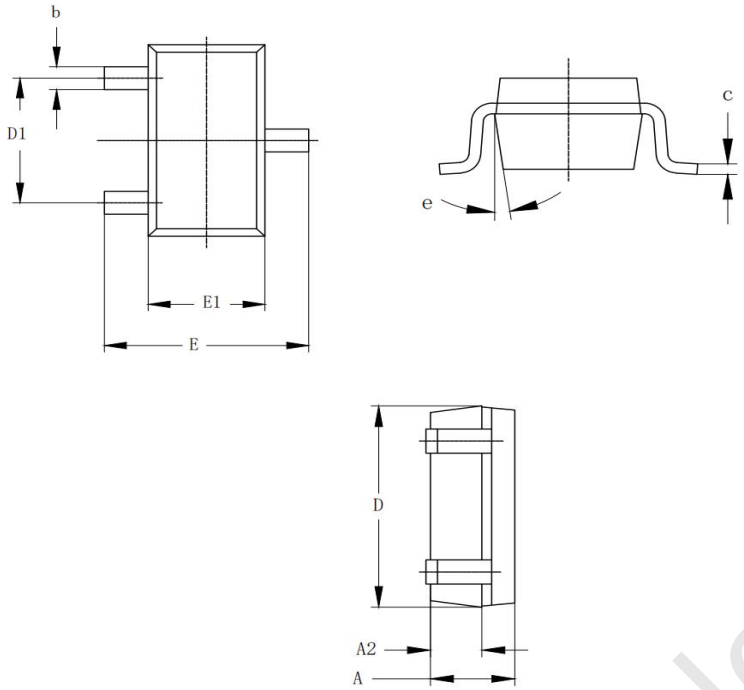


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOT-23-3L)



COMMON DIMENSION(MM)			
PKG	SOT-23-3L		
SYMBOL	MIN	TYP	MAX
A	1.080	1.100	1.120
A1	0.010	0.060	0.150
A2	0.640	0.670	0.700
b	0.325	0.350	0.375
c	0.125	0.135	0.150
D	2.920	2.930	2.980
D1	1.875	1.900	1.925
E	2.650	2.800	2.950
E1	1.580	1.600	1.670
L	0.300	0.450	0.600
e	8°		
Q	0°	4°	8°

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