CRMKBL0303A

P-Channel -30V, 3.6mΩ Typ. Power MOSFET

Description

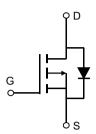
Features

• -30V, -100A

 $R_{DS(ON)}$ Typ = 3.6m Ω @ V_{GS} = -10V

 $R_{DS(ON)}$ Typ = $6m\Omega$ @ V_{GS} = -4.5V

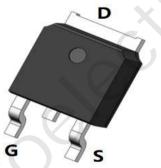
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

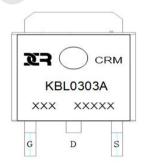


Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management





Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKBL0303A	CRMKBL0303A	TO-252-3L	TAPING	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		-30	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Oction Desire Comment	T _C = 25°C	-100	Α
I _D	Continuous Drain Current	T _C = 100°C	-60	А
I _{DM}	Pulsed Drain Current ⁽¹⁾		-400	Α
E _{AS}	Single Pulsed Avalanche Energy ⁽²⁾		248	mJ
P_{D}	Power Dissipation	T _C = 25°C	78	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		1.6	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = -250 \mu A, V_{GS} = 0 V$	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -30V, V_{GS} = 0V$	-	-	-1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.2	-1.6	-2.2	V
В	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = -10V, I_D = -20A$	-	3.6	4.7	mΩ
$R_{DS(ON)}$		$V_{GS} = -4.5V, I_{D} = -10A$	-	6	7.8	mΩ
Dynamic	Characteristics					
C_{iss}	Input Capacitance		-(3910	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = -15V,$ f = 1MHz	X - \	692	-	pF
C_{rss}	Reverse Transfer Capacitance	1 - 1101112		561	-	pF
Q_g	Total Gate Charge		U -	78	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } -10V$ $V_{DS} = -15V, I_{D} = -30A$	-	13	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	18	-	nC
Switchin	g Characteristics					
$t_{d(on)}$	Turn-On DelayTime	.()	-	17.5	-	ns
t _r	Turn-On Rise Time	$V_{GS} = -10V, V_{DD} = -15V$	-	50	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_D = -30A, R_{GEN} = 3Ω	-	108	-	ns
$t_{\rm f}$	Turn-Off Fall Time		-	70	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
Is	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	-100	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	-400	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = -20A$	-	-	-1.2	V
trr	Body Diode Reverse Recovery Time	I = 204 di/dt = 4004/:	-	33	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = -30A$, di/dt = 100A/us	-	40	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =-15V, V_G =-10V, R_G =25ohm, L=0.5mH, I_{AS} =-31.5A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

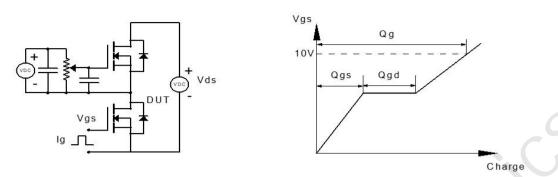


Figure 1: Gate Charge Test Circuit & Waveform

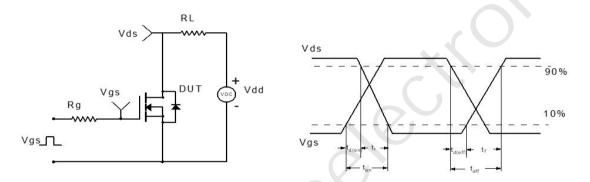


Figure 2: Resistive Switching Test Circuit & Waveform

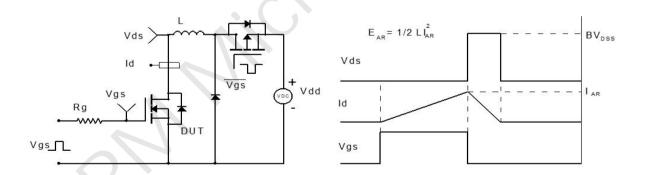


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

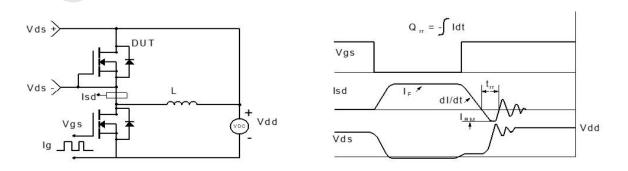
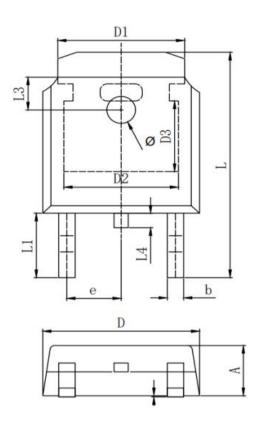


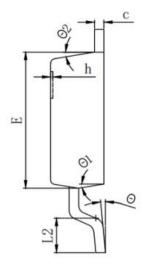
Figure 4: Diode Recovery Test Circuit & Waveform

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Package Mechanical Data(TO-252-3L)





CVMDOL	MILLIMETER				
SYMBOL	MIN	Typ.	MAX		
A	2. 200	2. 300	2. 400		
A1	0.000		0. 127		
b	0.640	0.690	0.740		
c(电镀后)	0.460	0.520	0. 580		
D	6.500	6.600	6. 700		
D1	5. 334 REF				
D2	4. 826 REF				
D3	3. 166 REF				
E	6.000	6. 100	6. 200		
e	2. 286 TYP				
h	0.000	0. 100	0. 200		
L	9, 900	10.100	10.300		
L1	2.888 REF				
L2	1.400	1.550	1.700		
L3	1.600 REF				
L4	0.600	0.800	1.000		
ф	1.100	1.200	1. 300		
θ	0°		8°		
θ 1	9° TYP				
θ2	9° TYP				

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