CRMKBL10140A

P-Channel -100V, 150mΩ Typ. Power MOSFET

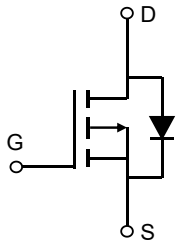
Description

Features

- -100V, -10A

  $R_{DS(ON)}$ Typ = 150mΩ @ V_{GS} = -10V

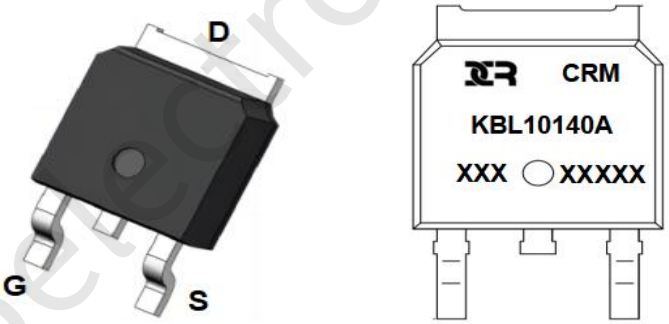
  $R_{DS(ON)}$ Typ = 170mΩ @ V_{GS} = -4.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKBL10140A	CRMKBL10140A	TO-252-3L	TAPING	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		-100	V
V_{GS}	Gate-to-Source Voltage		±20	V
I_D	Continuous Drain Current	T_C = 25°C	-10	A
		T_C = 100°C	-6	A
I_{DM}	Pulsed Drain Current [1]		-40	A
E_{AS}	Single Pulsed Avalanche Energy [2]		46	mJ
P_D	Power Dissipation	T_C = 25°C	34	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.7	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = -250μA, V_{GS} = 0V	-100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = -100V, V_{GS} = 0V	-	-	-1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = -250μA	-1.1	-1.6	-2.2	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(3)	V_{GS} = -10V, I_D = -5A	-	150	195	mΩ
		V_{GS} = -4.5V, I_D = -4A	-	170	221	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = -25V, f = 1MHz	-	2577	-	pF
C_{oss}	Output Capacitance		-	66	-	pF
C_{rss}	Reverse Transfer Capacitance		-	52	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to -10V, V_{DS} = -50V, I_D = -10A	-	45	-	nC
Q_{gs}	Gate Source Charge		-	4.5	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	5.7	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = -10V, V_{DD} = -50V, I_D= -6.5A, R_{GEN} = 10Ω	-	22	-	ns
t_r	Turn-On Rise Time		-	30	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	58	-	ns
t_f	Turn-Off Fall Time		-	48	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-10	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-40	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = -5A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25°C, V_{DD}=-50V, V_G=-10V, R_G=25ohm, L=0.5mH, I_{AS}=-13.5A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Test Circuit

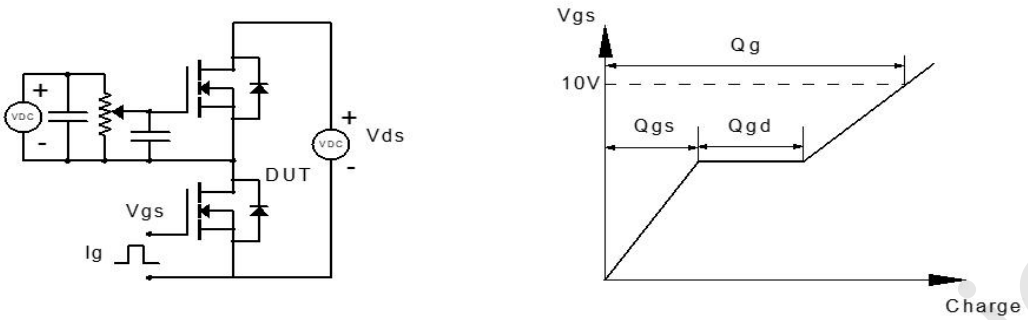


Figure 1: Gate Charge Test Circuit & Waveform

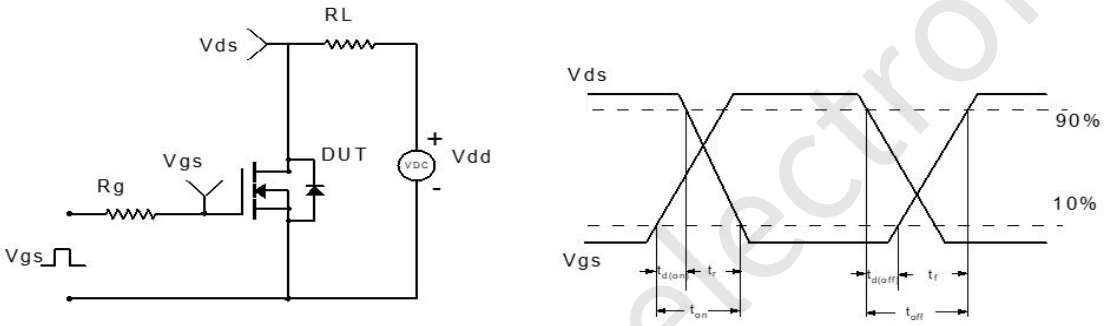


Figure 2: Resistive Switching Test Circuit & Waveform

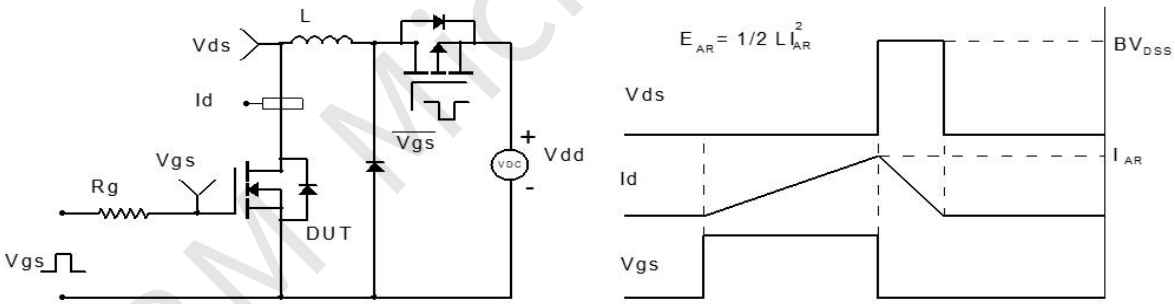


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

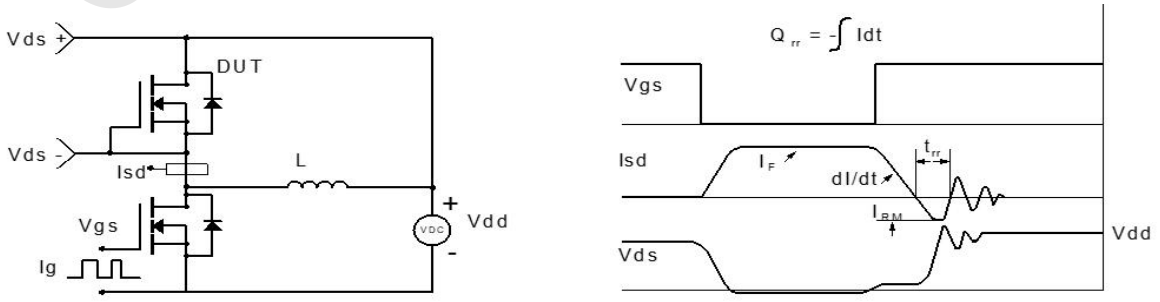
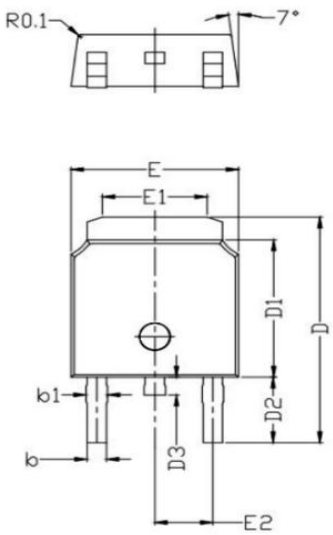
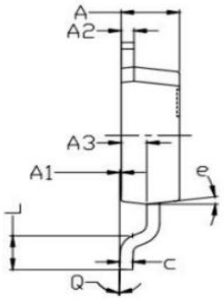


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TO-252-3L)





COMMON DIMENSION (MM)			
PKG	TO-252-3L		
Symbol	MIN	NOM	MAX
A	2.250	2.300	2.400
A1	0.010	0.060	0.150
A2	0.500	0.508	0.550
A3	0.960	1.010	1.060
b	0.740	0.760	0.800
b1	0.880	0.900	0.950
c	0.500	0.508	0.550
D	9.800	10.025	10.350
D1	6.050	6.100	6.180
D2	2.850	2.900	2.950
D3	0.700	0.800	2.900
E	6.550	6.600	6.700
E1	4.050	4.130	4.200
E2	2.250	2.286	2.300
L	1.400	1.500	1.600
e	7		
Q	0°	2°	5°

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