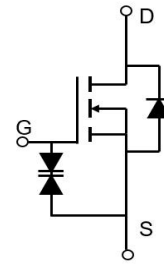


### Description

#### Features

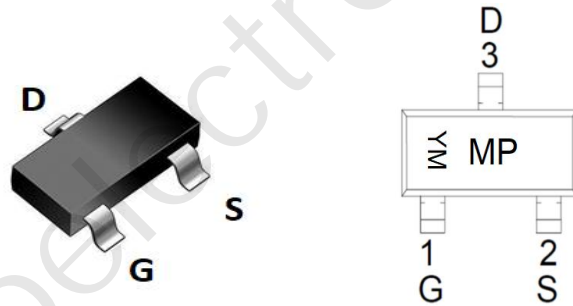
- 60V, 0.3A  
 $R_{DS(ON)}$  Typ = 1.7Ω @  $V_{GS} = 10V$   
 $R_{DS(ON)}$  Typ = 2 Ω @  $V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free
- ESD Protected: 2KV



Schematic Diagram

#### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMLCTL2N7002K	MP	SOT-523-3L	TAPING	7"	3000	120000

#### Absolute Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	60	V
$V_{GS}$	Gate-to-Source Voltage	±20	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ C$	0.3
		$T_A = 100^\circ C$	0.18
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	1.2	A
$P_D$	Power Dissipation	$T_A = 25^\circ C$	0.17
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(2)</sup>	735	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C

### Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	60	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±10	μA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1	1.6	2	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.3A	-	1.7	2.1	Ω
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 0.2A	-	2.0	2.4	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz	-	28	-	pF
C <sub>oss</sub>	Output Capacitance		-	11	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	4	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 4.5V V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.3A	-	1.7	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	0.3	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	0.6	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 10V I <sub>D</sub> = 0.2A, R <sub>GEN</sub> = 10Ω	-	2	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	15	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	7	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	20	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	0.3	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	1.2	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 0.3A	-	-	1.2	V

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. R<sub>θJA</sub> is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
  3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

**Typical Performance Characteristics**

Figure 1: Output Characteristics

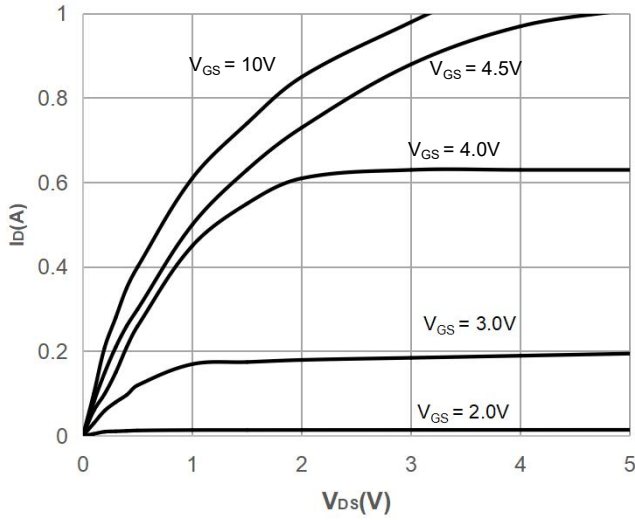


Figure 2: Typical Transfer Characteristics

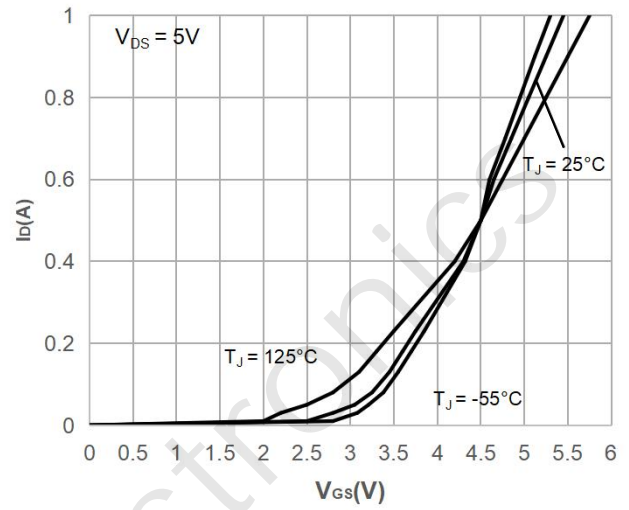


Figure 3: On-resistance vs. Drain Current

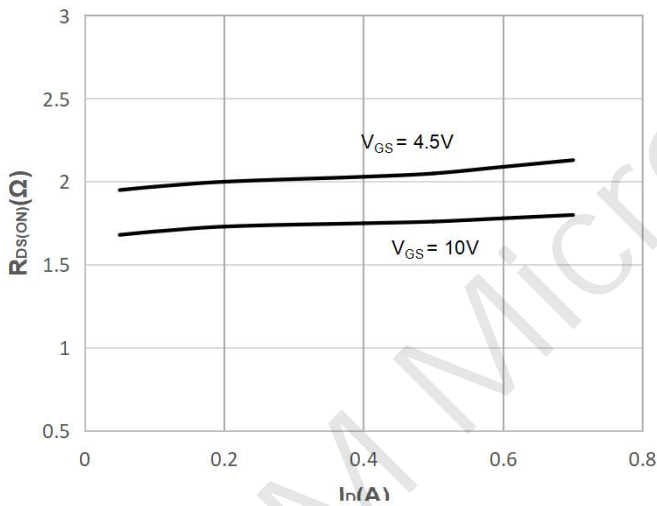


Figure 4: Body Diode Characteristics

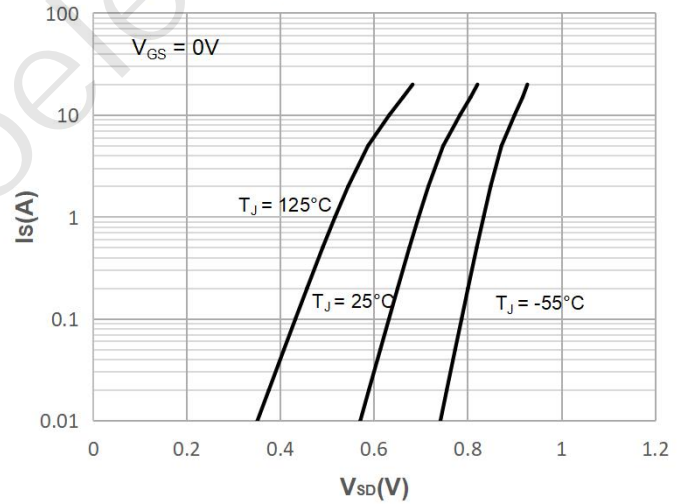


Figure 5: Gate Charge Characteristics

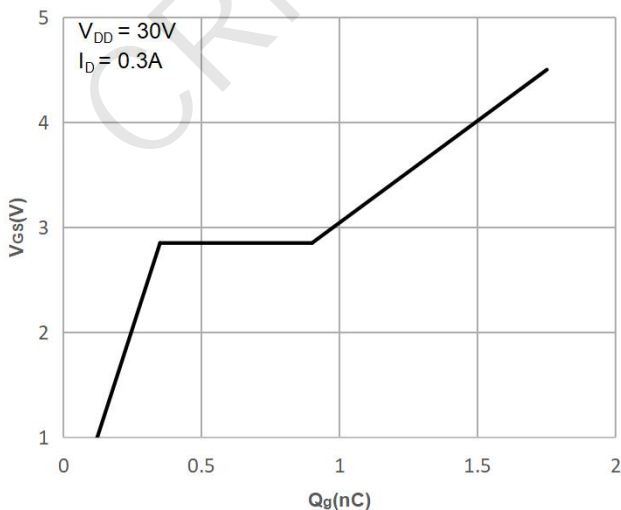
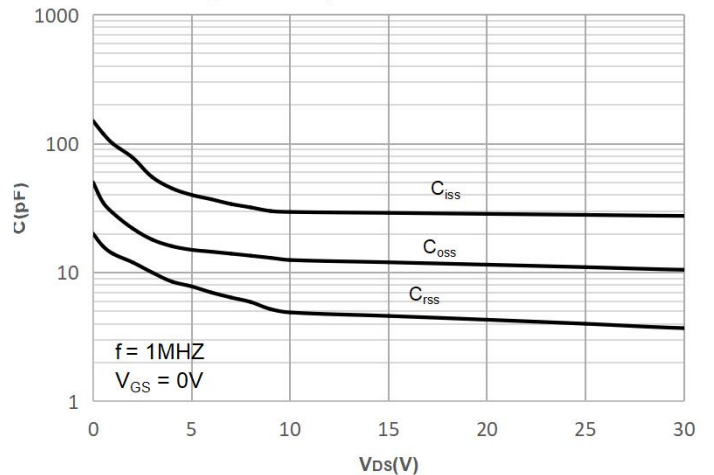
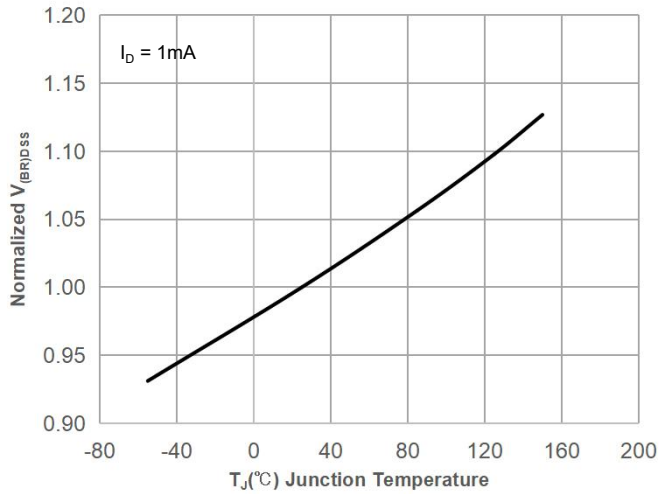


Figure 6: Capacitance Characteristics

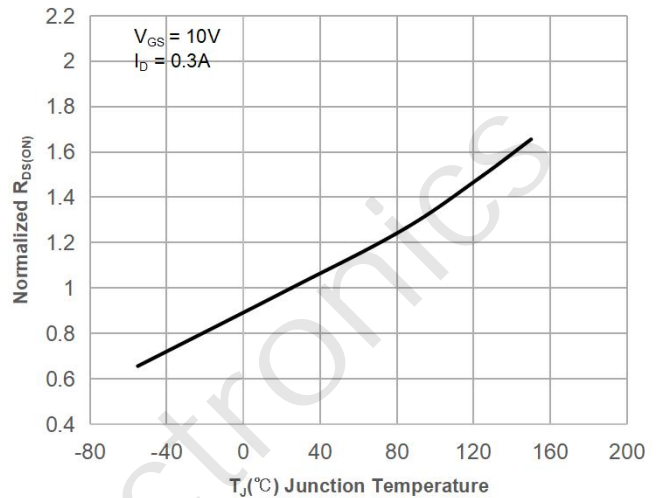


**Typical Performance Characteristics**

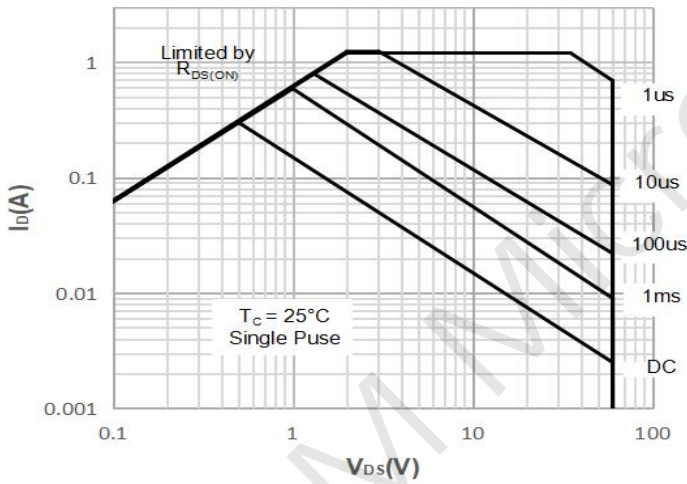
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



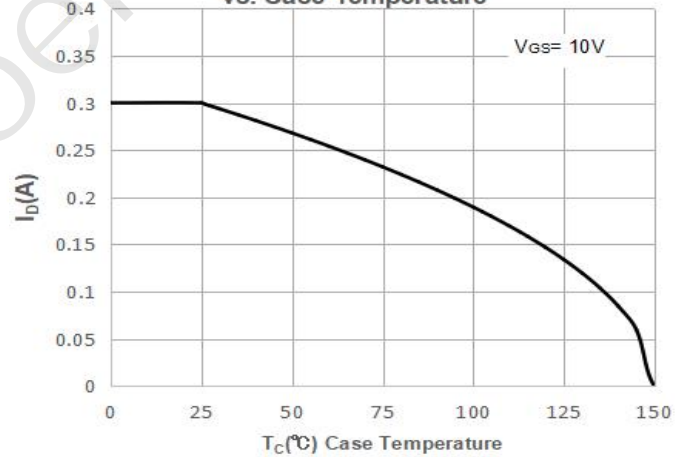
**Figure 8: Normalized on Resistance vs. Junction Temperature**



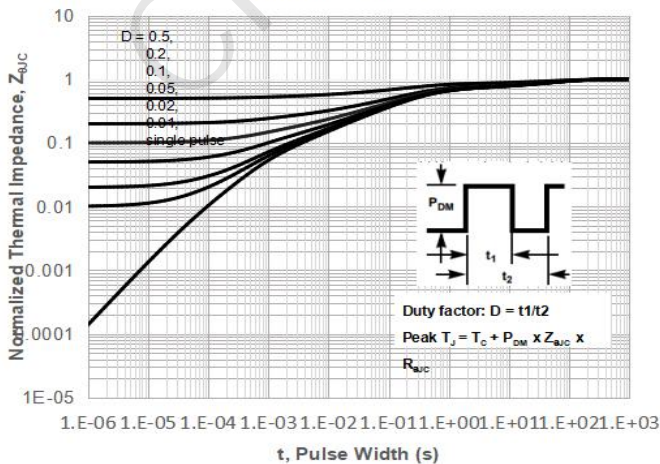
**Figure 9: Maximum Safe Operating Area**



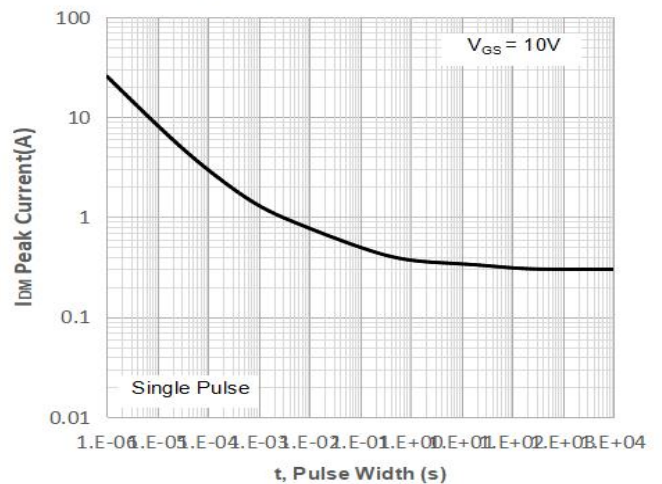
**Figure 10: Maximum Continuous Driant Current vs. Case Temperature**



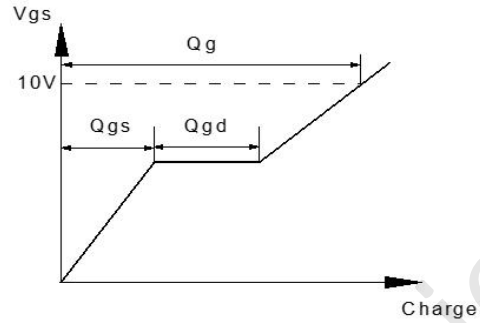
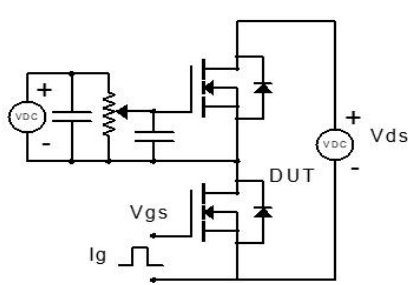
**Figure 11: Normalized Maximum Transient Thermal Impedance**



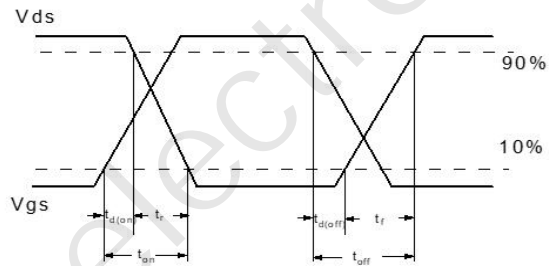
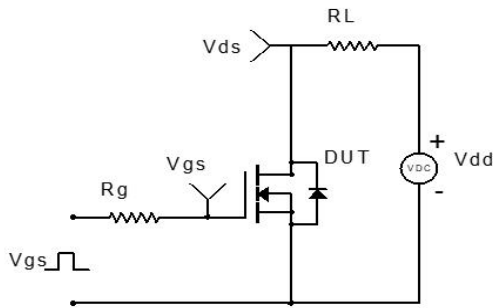
**Figure 12: Peak Current Capacity**



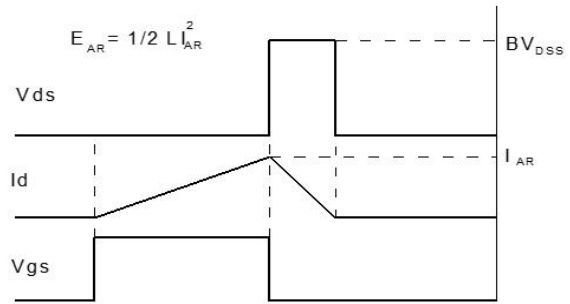
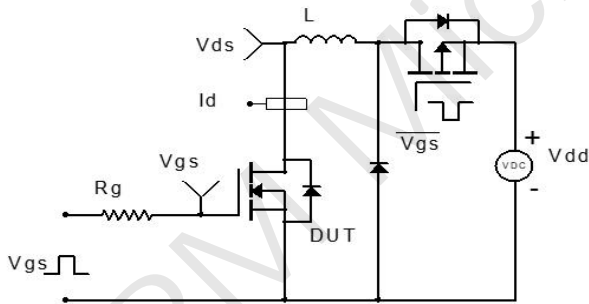
**Test Circuit**



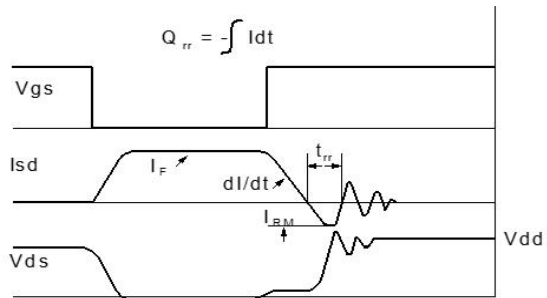
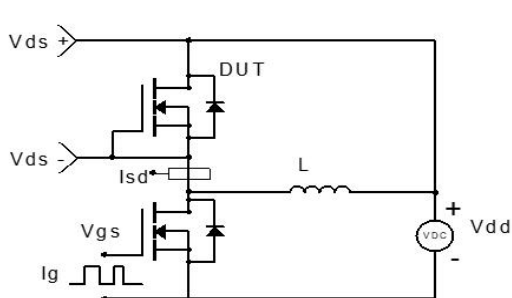
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**

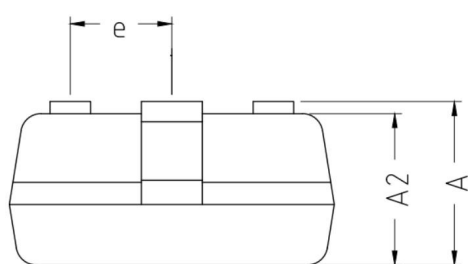
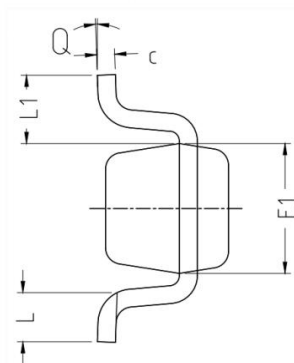
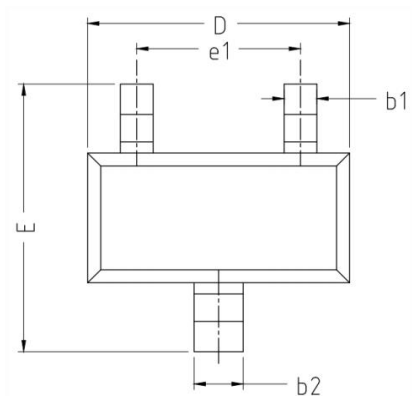


**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



**Figure 4: Diode Recovery Test Circuit & Waveform**

### Package Mechanical Data(SOT-523-3L)




COMMON IN DIMENSION (MM)			
PKG	SOT-523-3L		
Symbol	MIN	NOM	MAX
A	0.700	0.800	0.900
A2	0.700	0.750	0.800
b1	0.150	0.200	0.250
b2	0.250	0.300	0.350
c	0.100	0.130	0.200
D	1.550	1.600	1.700
E	1.450	1.600	1.750
E1	0.700	0.800	0.900
e	0.500 TYP		
e1	0.900	1.000	1.100
L	0.260	0.360	0.460
L1	0.400REF		
Q	0°	4°	8°

### Important Notice

The information presented in datasheets is for reference only. CRM reserves the right to make changes at any time to any products or information herein, without notice.

Customers are responsible for the design and applications, including compliance with all laws, regulations and safety requirements or standards.

“Typical” parameters which provided in datasheets can vary in different applications and actual performance may vary over time. Customers are responsible for doing all necessary testing to minimize the risks associated with their applications and products.

 is a registered trademark of Wuxi CRM Microelectronics Co. , Ltd.  
 Copyright ©2023 CRM Microelectronics Co. , Ltd. All rights reserved.

### Contact information

For more information, please visit: <http://www.crm-semi.tech>

For sales information, please send an email to: [sales@crm-semi.com](mailto:sales@crm-semi.com)