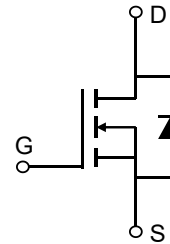


### Description

#### Features

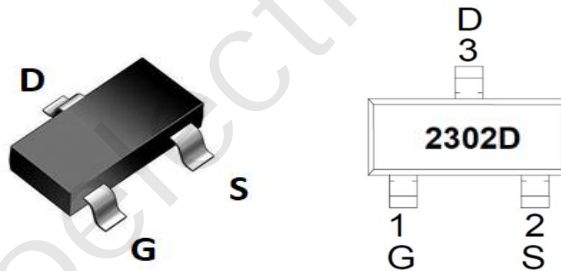
- 20V, 4A  
 $R_{DS(ON)}$  Typ = 25mΩ @  $V_{GS} = 4.5V$   
 $R_{DS(ON)}$  Typ = 29mΩ @  $V_{GS} = 2.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free



Schematic Diagram

#### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMLTU2302D	2302D	SOT-23	TAPING	7"	3000	120000

#### Absolute Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	20	V
$V_{GS}$	Gate-to-Source Voltage	±12	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ C$	4
		$T_A = 100^\circ C$	2.4
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	16	A
$P_D$	Power Dissipation	$T_A = 25^\circ C$	1.2
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(2)</sup>	106	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C

### Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	20	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±12V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	0.5	0.75	1	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4A	-	25	32	mΩ
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3A	-	29	38	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 10V, f = 1MHz	-	470	-	pF
C <sub>oss</sub>	Output Capacitance		-	55	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	50	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 4.5V V <sub>DS</sub> = 10V, I <sub>D</sub> = 2A	-	6	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	1	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	1.5	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 4.5V, V <sub>DD</sub> = 10V I <sub>D</sub> = 2A, R <sub>GEN</sub> = 3Ω	-	4	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	13	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	65	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	33	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	4	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	16	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 4A	-	-	1.2	V

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. R<sub>θJA</sub> is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
  3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

**Typical Performance Characteristics**

Figure 1: Output Characteristics

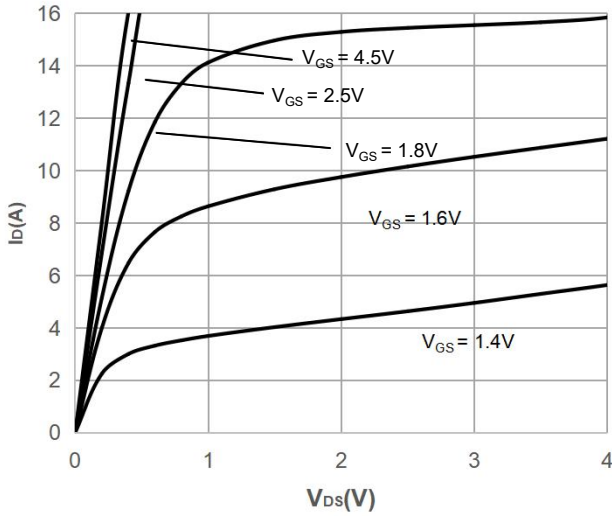


Figure 2: Typical Transfer Characteristics

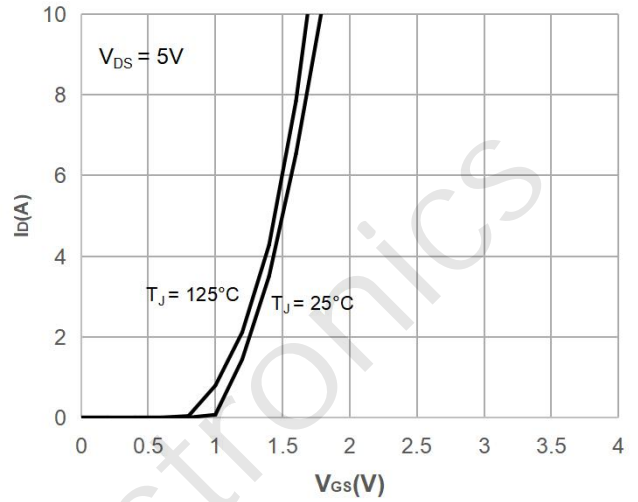


Figure 3: On-resistance vs. Drain Current

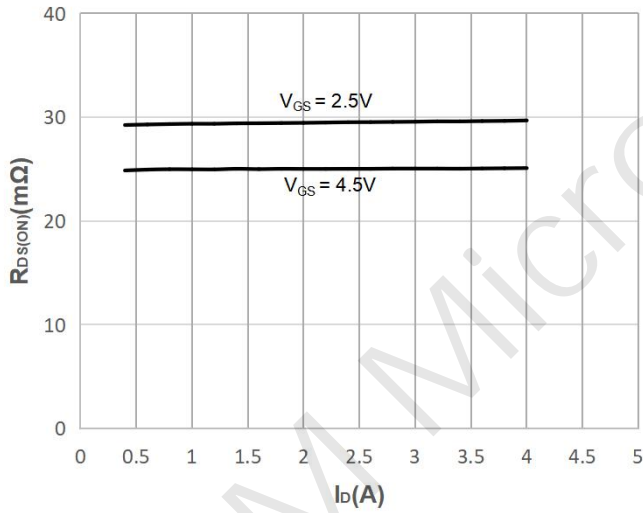


Figure 4: Body Diode Characteristics

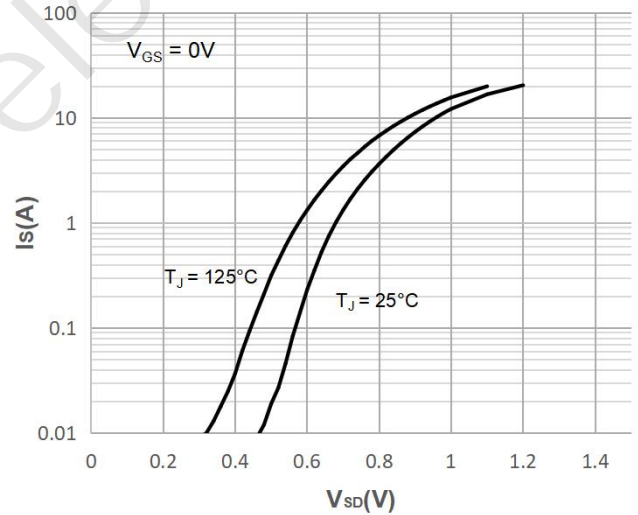


Figure 5: Gate Charge Characteristics

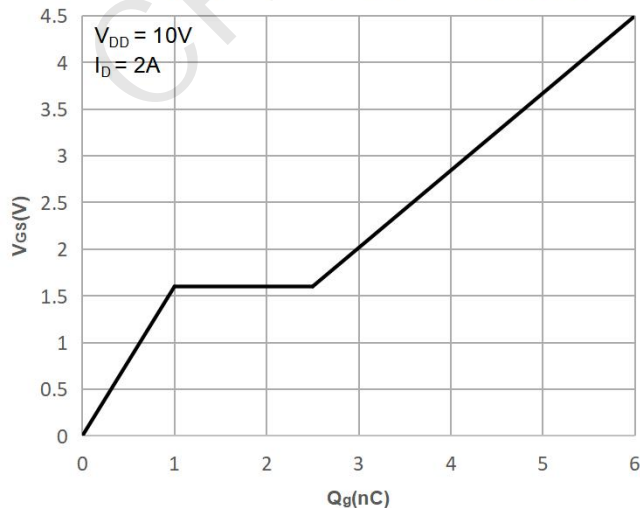
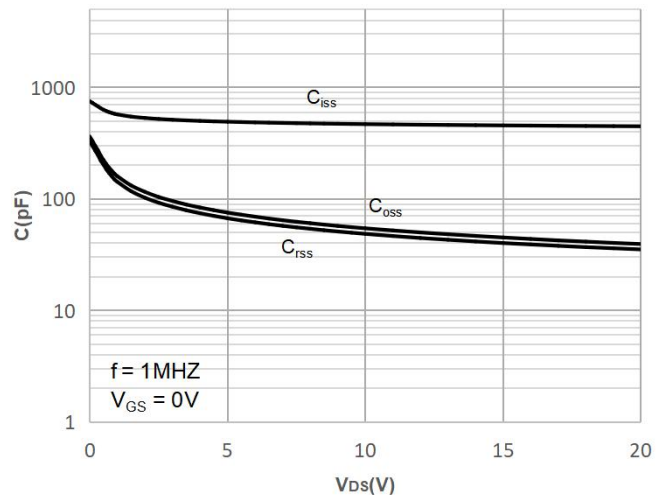
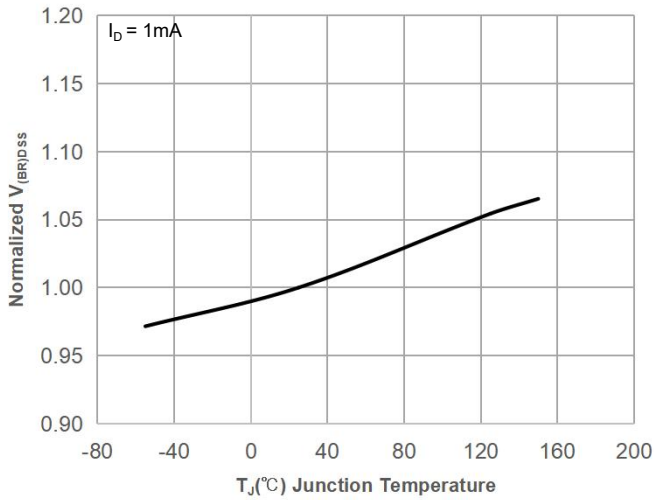


Figure 6: Capacitance Characteristics

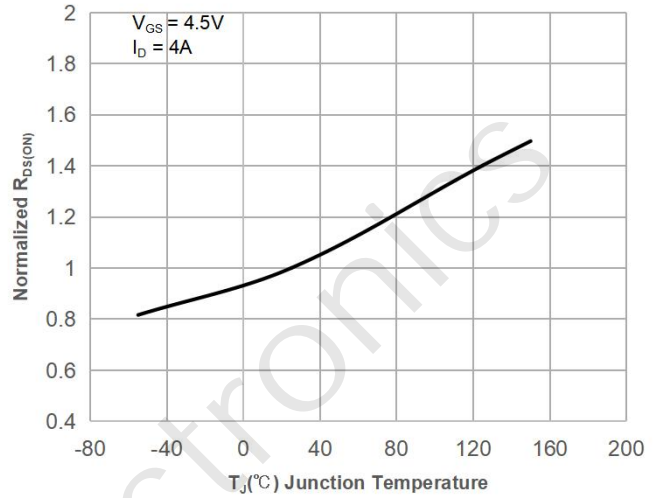


**Typical Performance Characteristics**

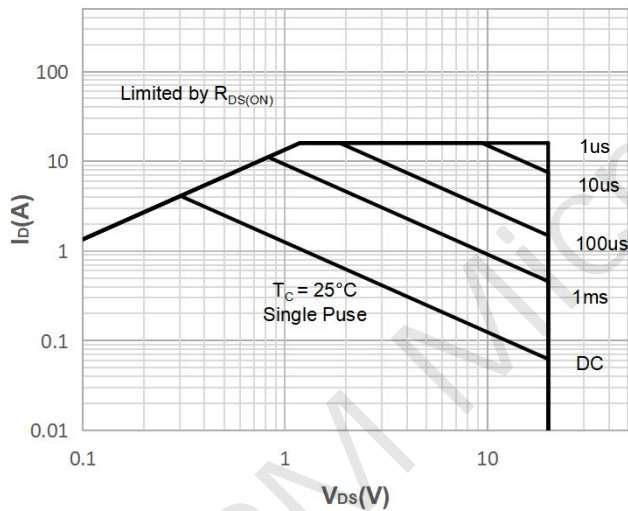
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



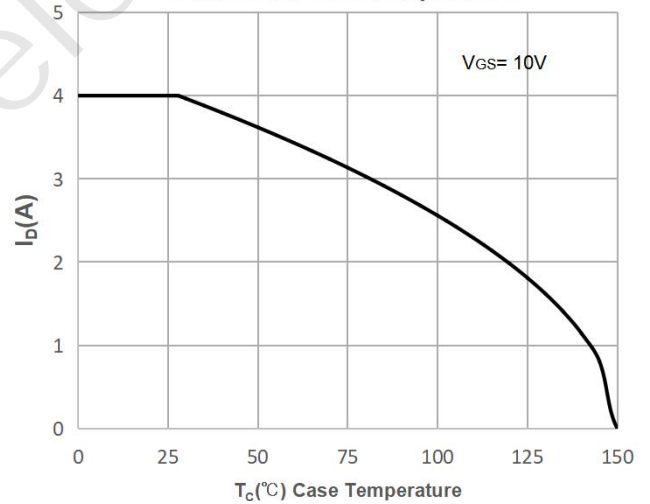
**Figure 8: Normalized on Resistance vs. Junction Temperature**



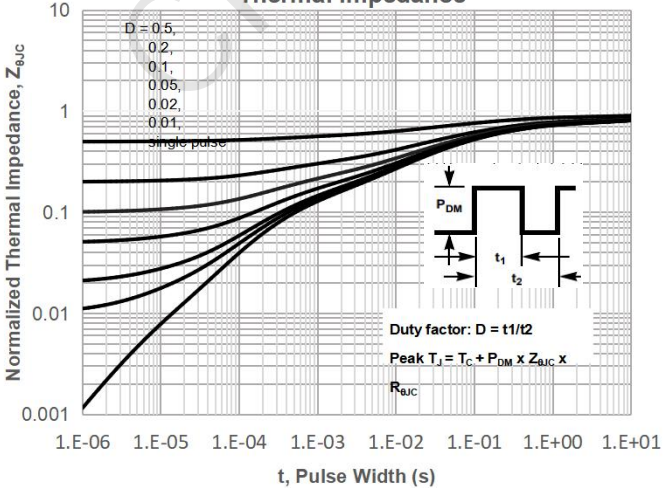
**Figure 9: Maximum Safe Operating Area**



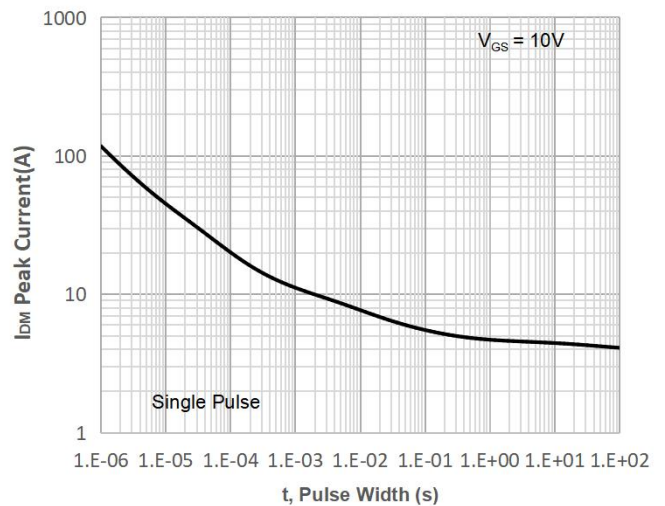
**Figure 10: Maximum Continuous Driand Current vs. Case Temperature**



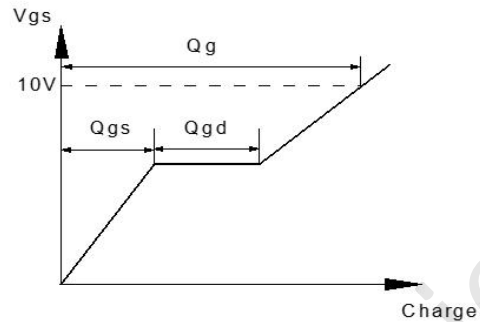
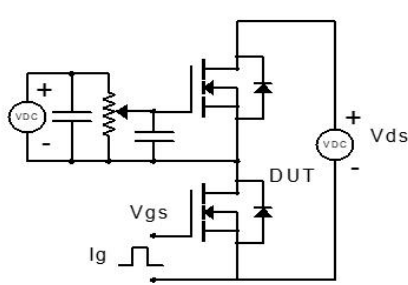
**Figure 11: Normalized Maximum Transient Thermal Impedance**



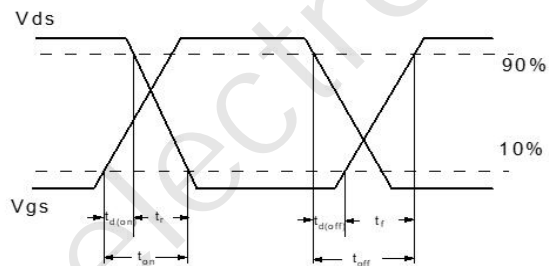
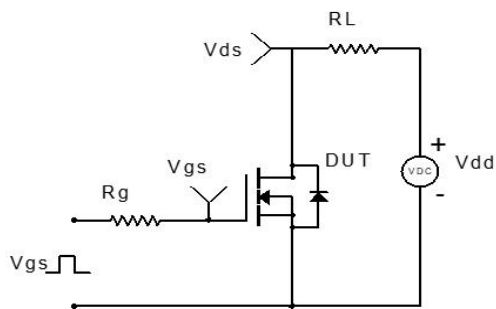
**Figure 12: Peak Current Capacity**



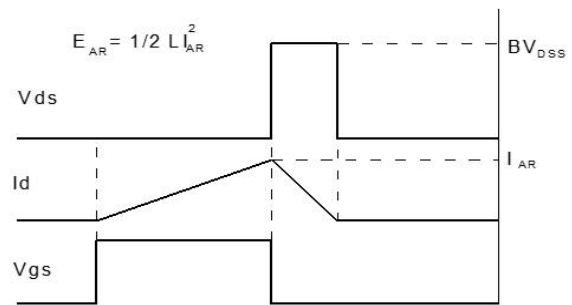
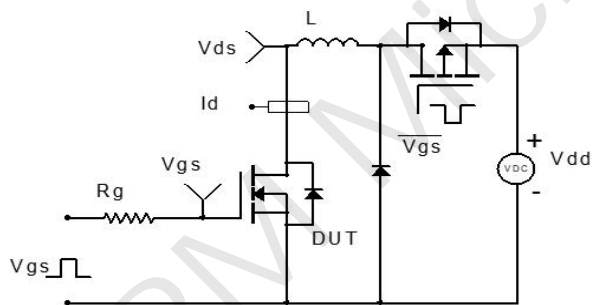
**Test Circuit**



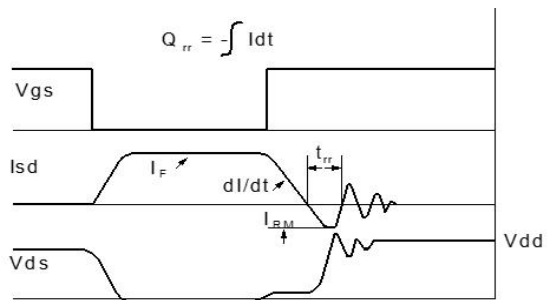
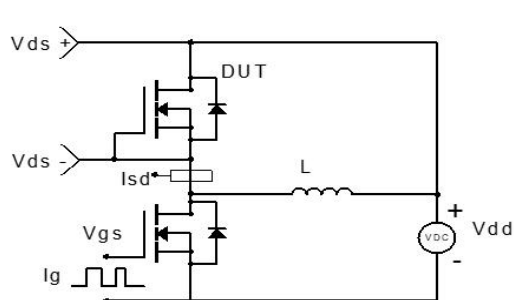
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**

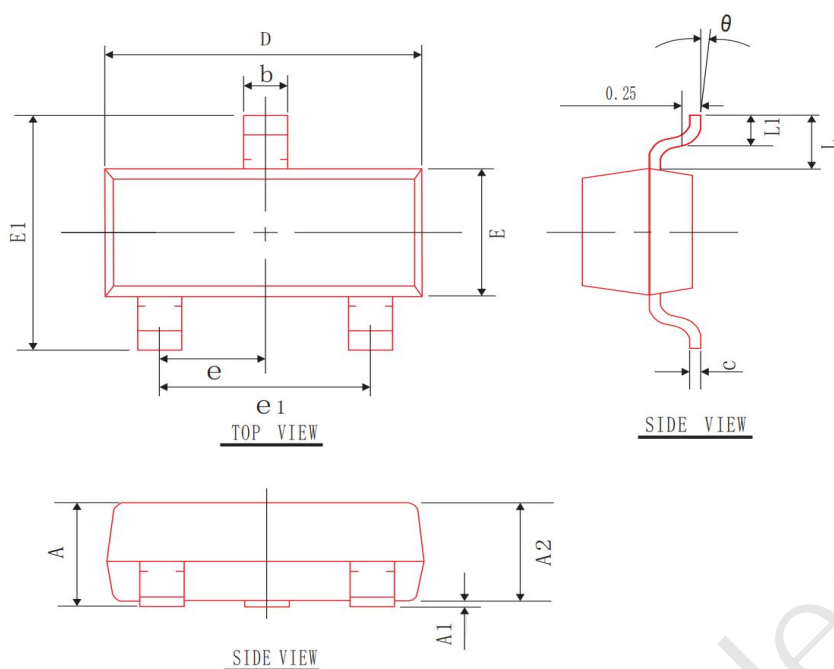


**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



**Figure 4: Diode Recovery Test Circuit & Waveform**

### Package Mechanical Data(SOT-23)




Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
C	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950 TYP.	
e1	1.800	2.000
L	0.550 REF.	
L1	0.300	0.500
θ	0	8°

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