CRMQBU0208B

P-Channel -20V, 5.8mΩ Typ. Power MOSFET

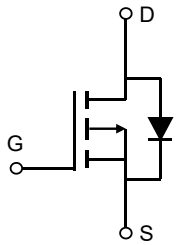
Description

Features

- -20V, -55A

  $R_{DS(ON)}$ Typ = 5.8mΩ @ V_{GS} = -4.5V

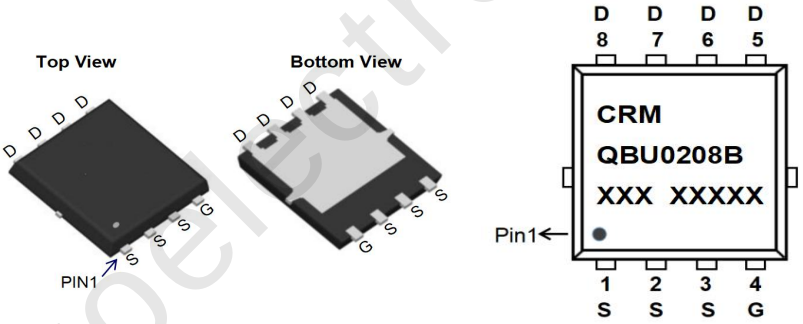
  $R_{DS(ON)}$ Typ = 7.8mΩ @ V_{GS} = -2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free
- 100% UIS TESTED!
- 100% ΔVds TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMQBU0208B	CRMQBU0208B	PDFN3.3x3.3-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		-20	V
V_{GS}	Gate-to-Source Voltage		±12	V
I_D	Continuous Drain Current	T_C = 25°C	-55	A
		T_C = 100°C	-35	A
I_{DM}	Pulsed Drain Current (1)		-220	A
E_{AS}	Single Pulsed Avalanche Energy (2)		43	mJ
P_D	Power Dissipation	T_C = 25°C	42	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		3	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = -250μA, V_{GS} = 0V	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = -20V, V_{GS} = 0V	-	-	-1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±12V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = -250μA	-0.4	-0.65	-1	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(3)	V_{GS} = -4.5V, I_D = -15A	-	5.8	7.6	mΩ
		V_{GS} = -2.5V, I_D = -10A	-	7.8	10	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = -10V, f = 1MHz	-	2839	-	pF
C_{oss}	Output Capacitance		-	372	-	pF
C_{rss}	Reverse Transfer Capacitance		-	311	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to -4.5V, V_{DS} = -10V, I_D = -15A	-	54	-	nC
Q_{gs}	Gate Source Charge		-	7	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	14	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = -10V, V_{DD} = -10V, I_D= -13A, R_{GEN} = 3Ω	-	13	-	ns
t_r	Turn-On Rise Time		-	105	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	145	-	ns
t_f	Turn-Off Fall Time		-	150	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-55	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-220	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = -10A	-	-	-1.2	V
trr	Body Diode Reverse Recovery Time	I_F = -15A, di/dt = 100A/us	-	26	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	15	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25°C, V_{DD}=-10V, V_G=-10V, R_G=25ohm, L=0.5mH, I_{AS}=-13A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Typical Performance Characteristics

Figure 1: Output Characteristics

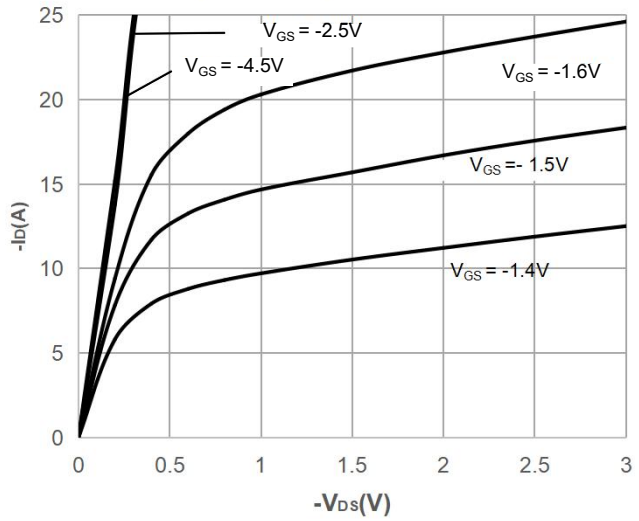


Figure 2: Typical Transfer Characteristics

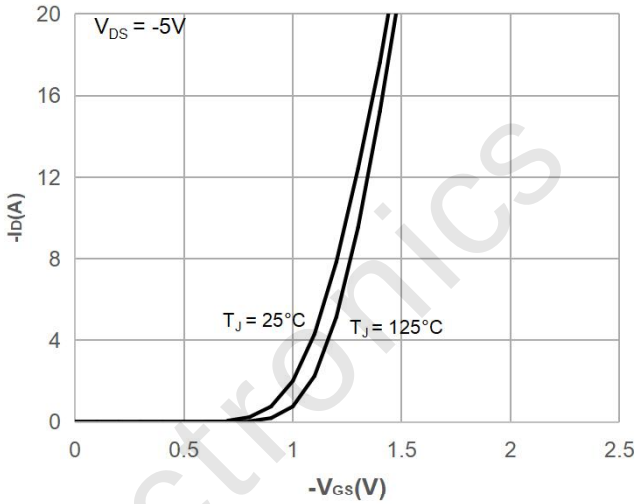


Figure 3: On-resistance vs. Drain Current

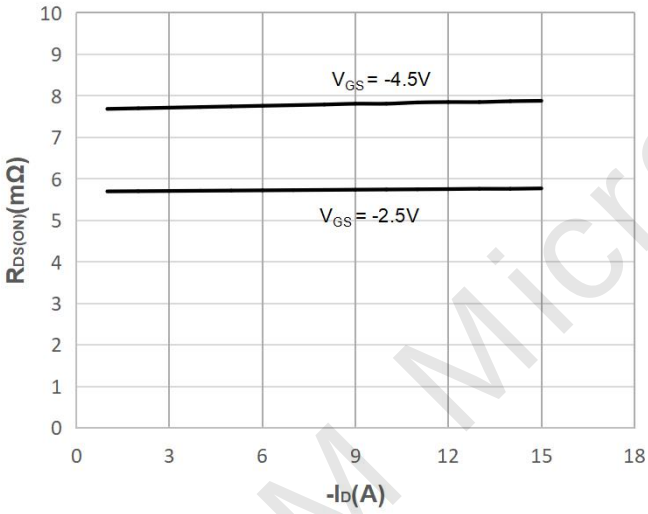


Figure 4: Body Diode Characteristics

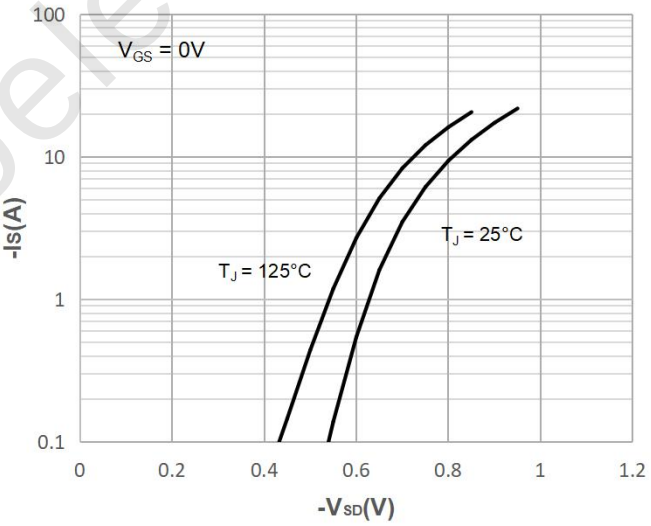


Figure 5: Gate Charge Characteristics

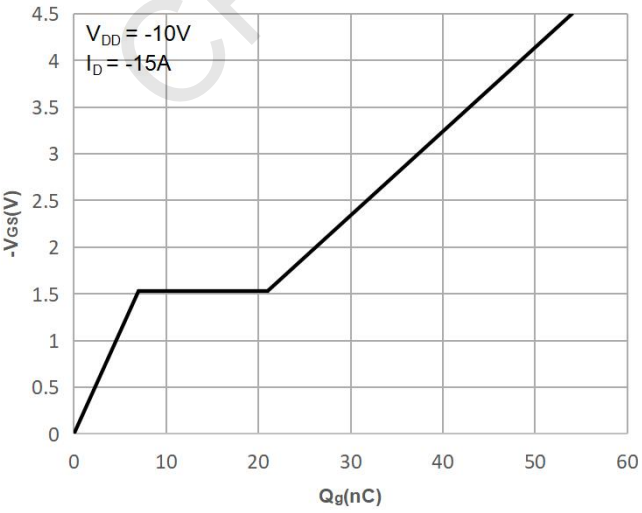
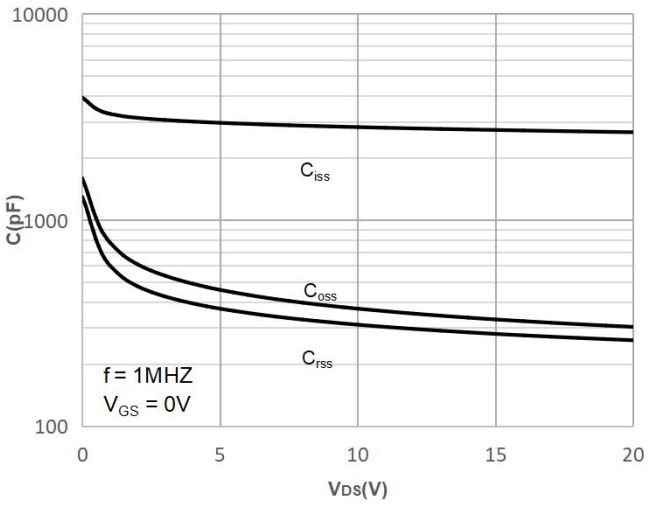


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

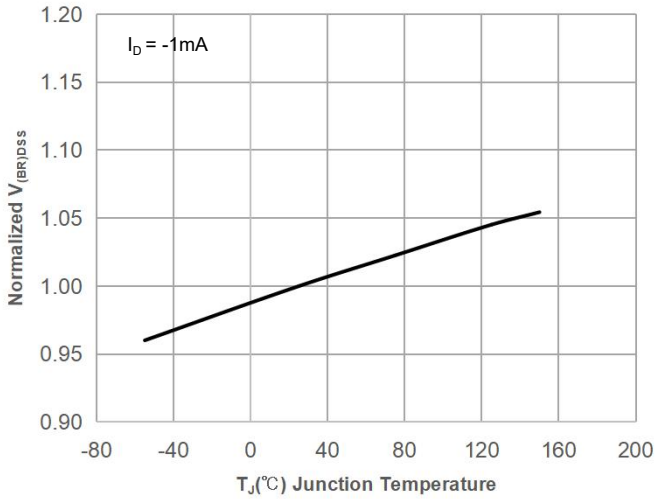


Figure 8: Normalized on Resistance vs. Junction Temperature

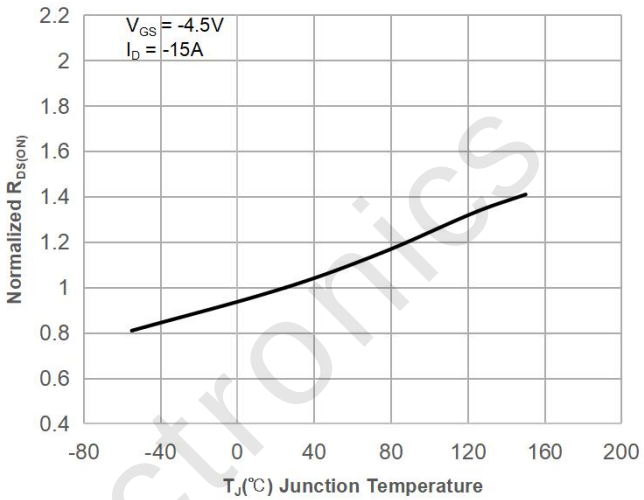


Figure 9: Maximum Safe Operating Area

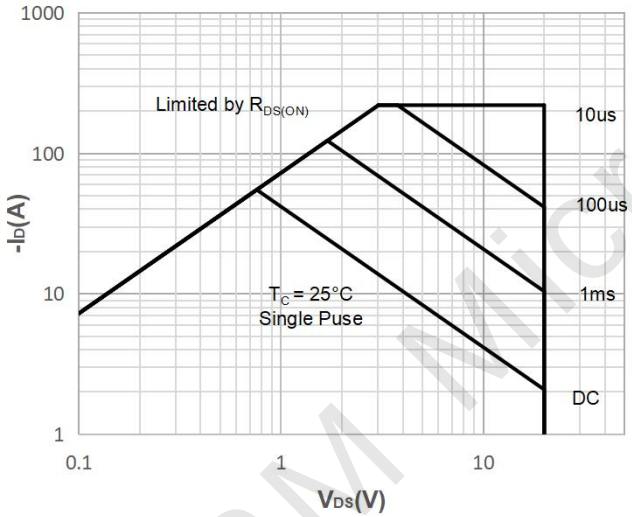


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

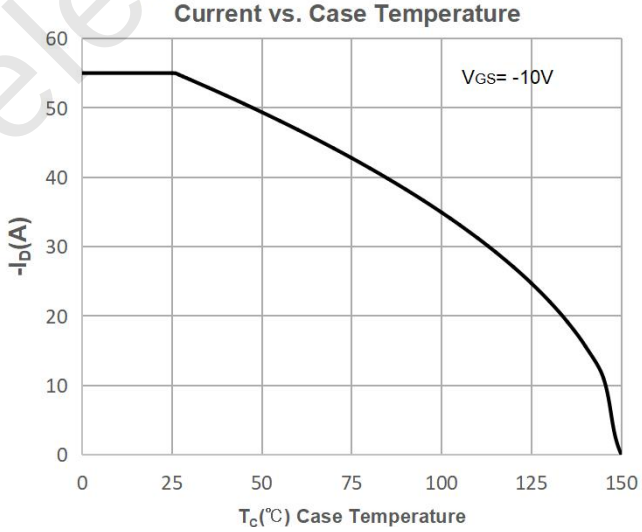


Figure 11: Normalized Maximum Transient Thermal Impedance

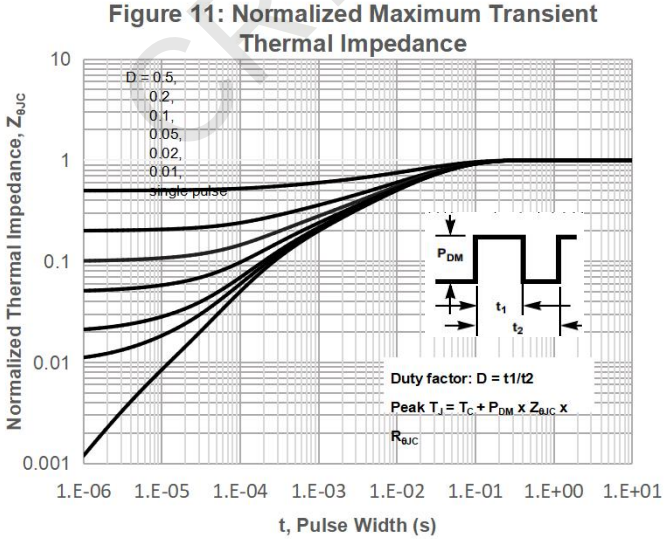
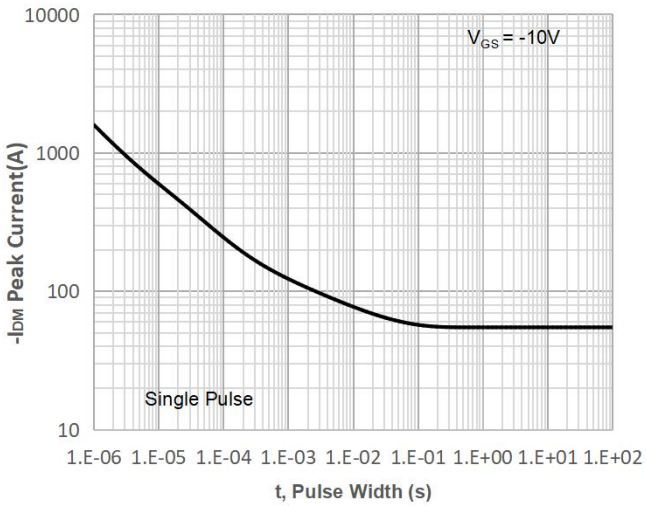


Figure 12: Peak Current Capacity



Test Circuit

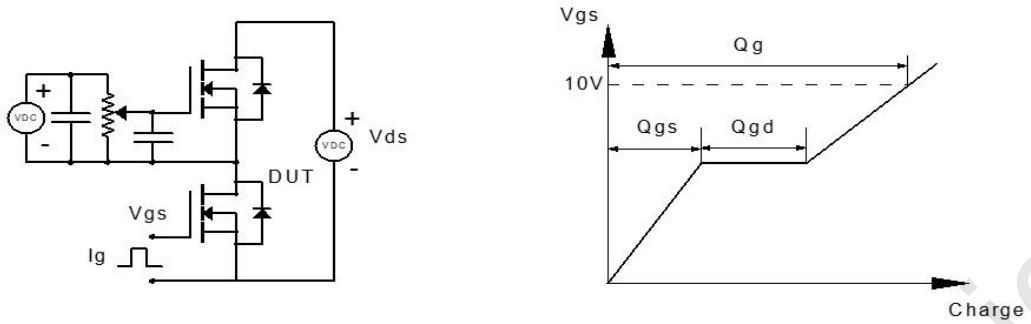


Figure 1: Gate Charge Test Circuit & Waveform

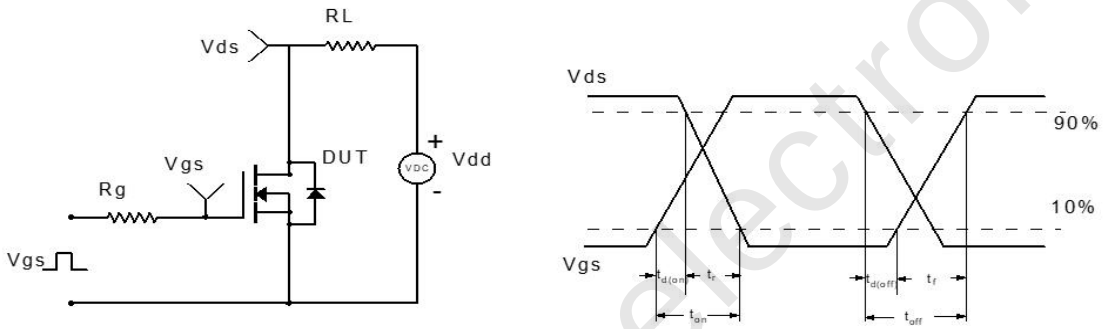


Figure 2: Resistive Switching Test Circuit & Waveform

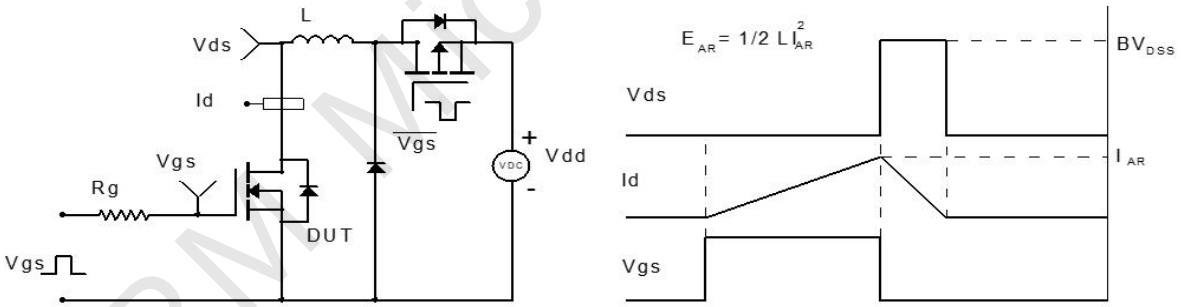


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

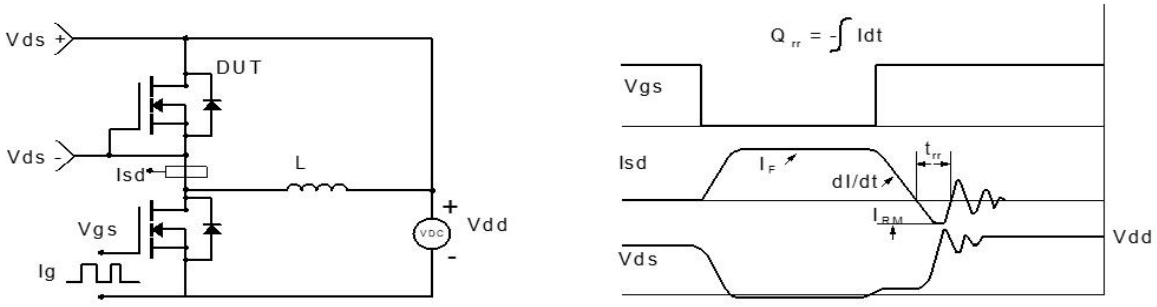
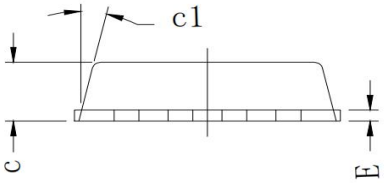
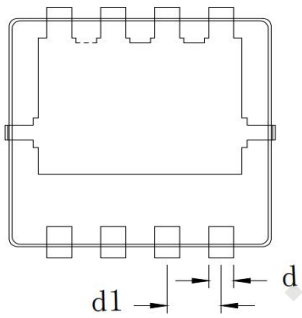
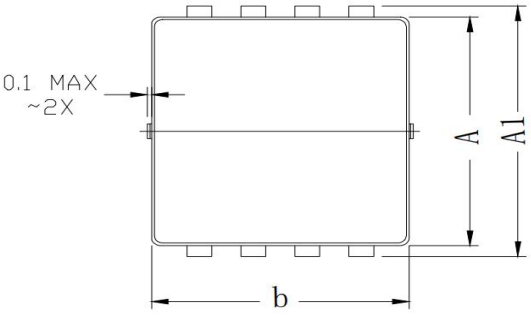


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(PDFN3.3x3.3-8L)







COMMON DIMENSION (MM)			
PKG	PDFN 3×3		
Symbol	MIN	MON	MAX
A	3.070	3.100	3.130
A1	3.300	3.400	3.500
b	3.070	3.100	3.130
c	0.770	0.800	0.830
c1	13°		
d	0.300		
d1	0.650		
E	0.152		

Important Notice

The information presented in datasheets is for reference only. CRM reserves the right to make changes at any time to any products or information herein, without notice.

Customers are responsible for the design and applications, including compliance with all laws, regulations and safety requirements or standards.

"Typical" parameters which provided in datasheets can vary in different applications and actual performance may vary over time. Customers are responsible for doing all necessary testing to minimize the risks associated with their applications and products.

is a registered trademark of Wuxi CRM Microelectronics Co. , Ltd.

Contact information

For more information, please visit: http://www.crm-semi.tech
For sales information, please send an email to: sales@crm-semi.com