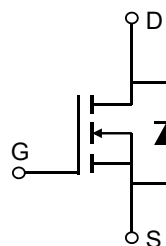


### Description

#### Features

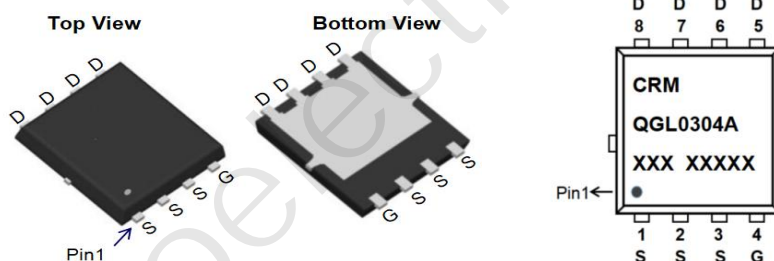
- 30V, 60A  
 $R_{DS(ON)}$  Typ = 3.4mΩ @  $V_{GS} = 10V$   
 $R_{DS(ON)}$  Typ = 5.0mΩ @  $V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free
- 100% UIS TESTED!
- 100%  $\Delta V_d$ s TESTED!



Schematic Diagram

#### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMQGL0304A	CRMQGL0304A	PDFN3.3x3.3-8L	TAPING	13"	5000	50000

#### Absolute Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	30	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$	60
		$T_C = 100^\circ C$	36
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	240	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	49	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$	27
$R_{\theta JC}$	Thermal Resistance, Junction to Case	4.6	$^\circ C/W$
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ C$

### Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1	1.5	2.2	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	3.4	4.4	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A	-	5	6.5	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz	-	953	-	pF
C <sub>oss</sub>	Output Capacitance		-	446	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	34	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 20V, I <sub>D</sub> = 50A	-	17	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	4.5	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	2	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 20V I <sub>D</sub> = 50A, R <sub>GEN</sub> = 3Ω	-	6.5	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	2.7	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	26	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	3.6	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	60	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	240	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I <sub>F</sub> = 50A, di/dt = 100A/us	-	22	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	40	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. E<sub>AS</sub> condition: Starting T<sub>J</sub>=25°C, V<sub>DD</sub>=15V, V<sub>G</sub>=10V, R<sub>G</sub>=25ohm, L=0.5mH, I<sub>AS</sub>=14A
  3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

### Typical Performance Characteristics

Figure 1: Output Characteristics

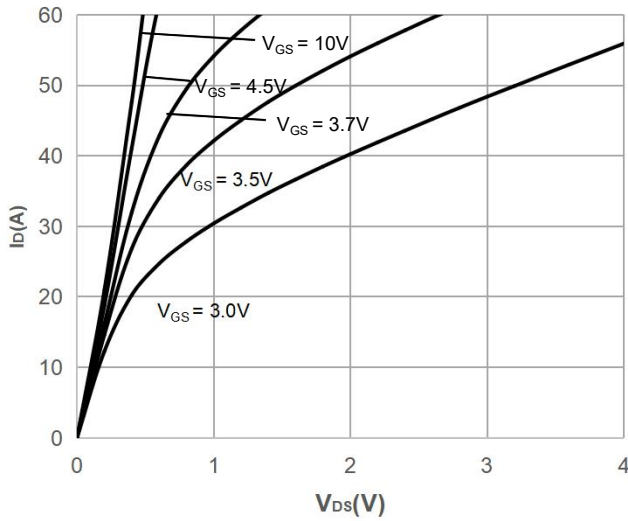


Figure 2: Typical Transfer Characteristics

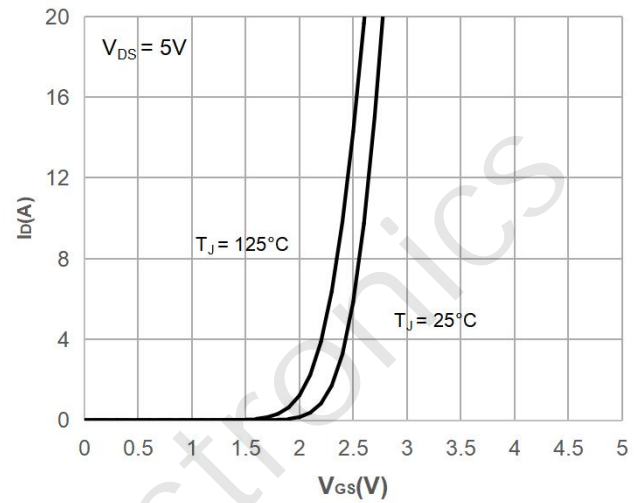


Figure 3: On-resistance vs. Drain Current

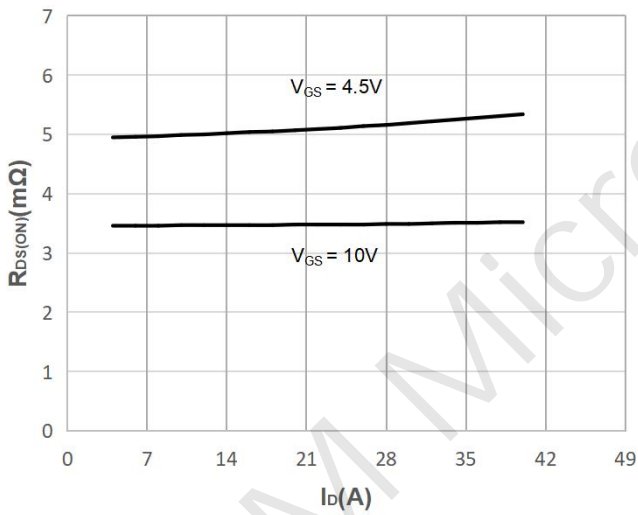


Figure 4: Body Diode Characteristics

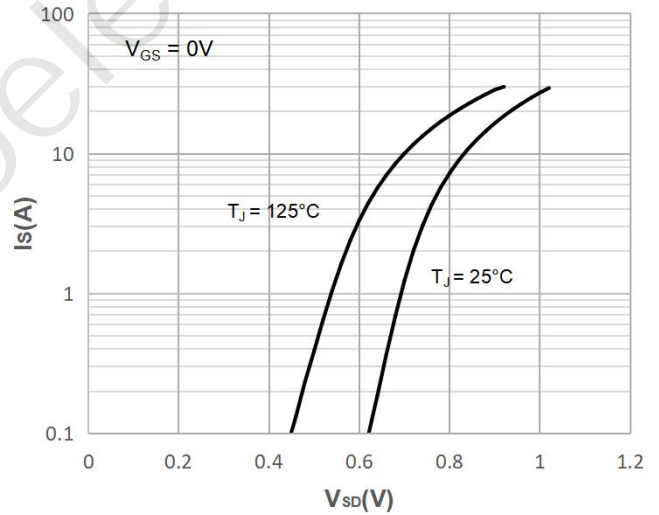


Figure 5: Gate Charge Characteristics

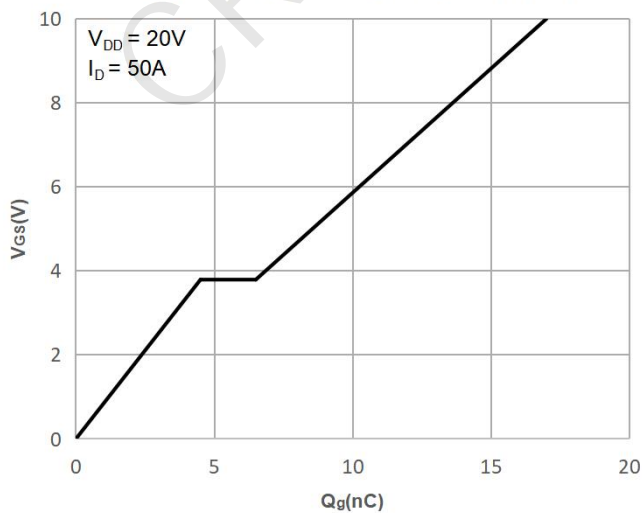
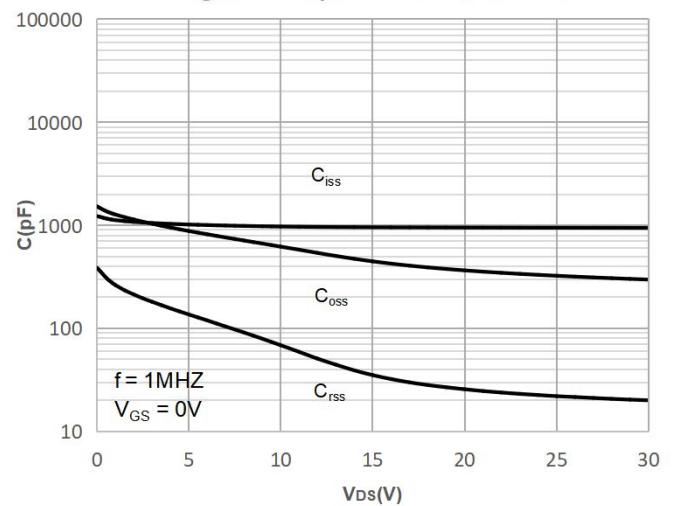
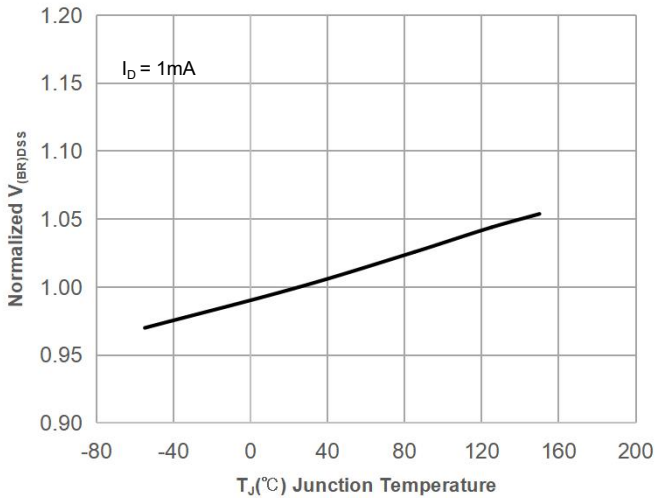


Figure 6: Capacitance Characteristics

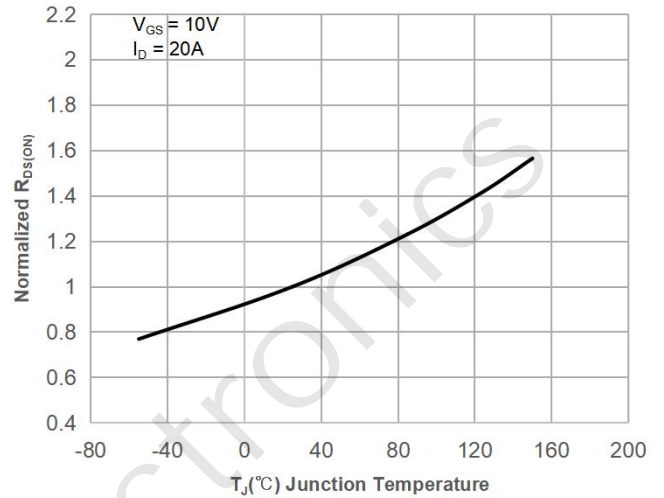


**Typical Performance Characteristics**

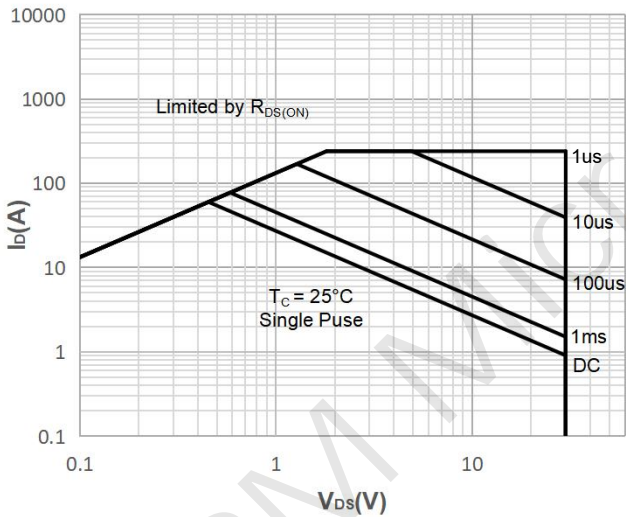
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



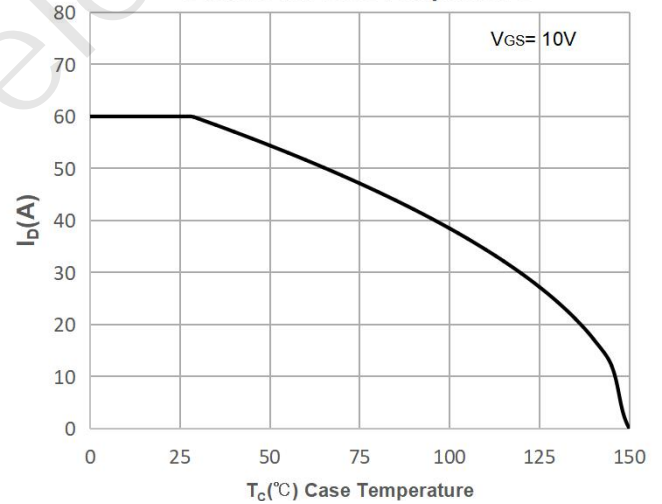
**Figure 8: Normalized on Resistance vs. Junction Temperature**



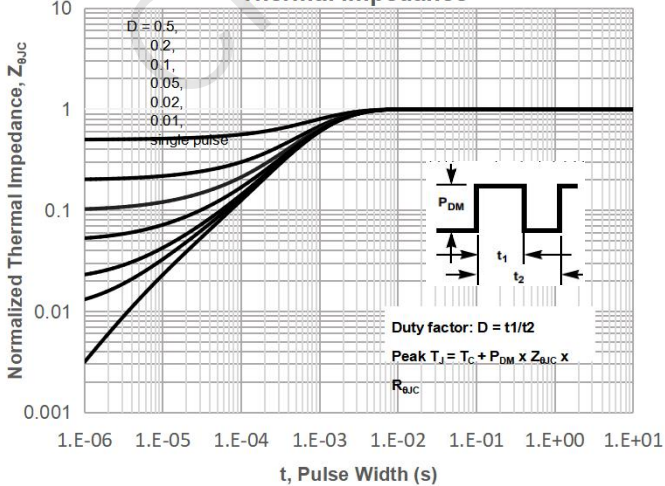
**Figure 9: Maximum Safe Operating Area**



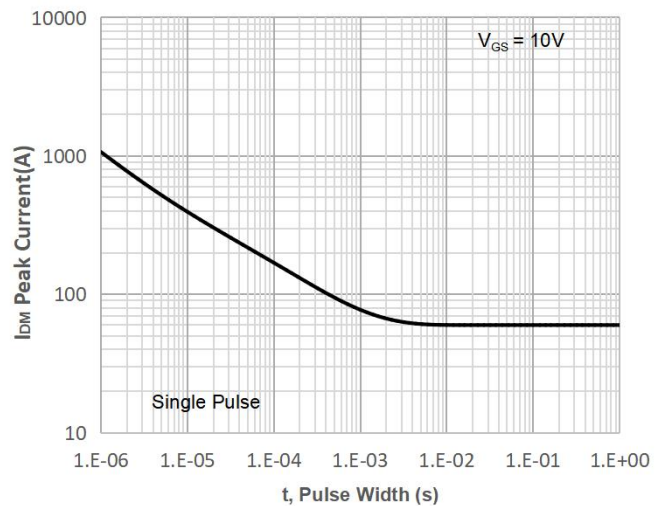
**Figure 10: Maximum Continuous Driant Current vs. Case Temperature**



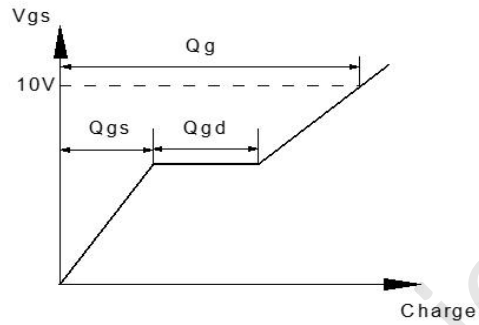
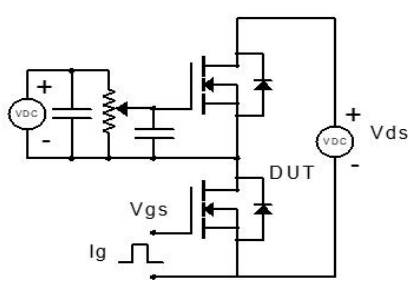
**Figure 11: Normalized Maximum Transient Thermal Impedance**



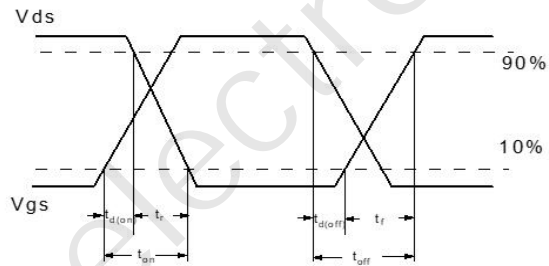
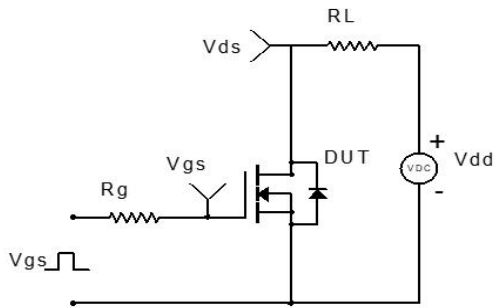
**Figure 12: Peak Current Capacity**



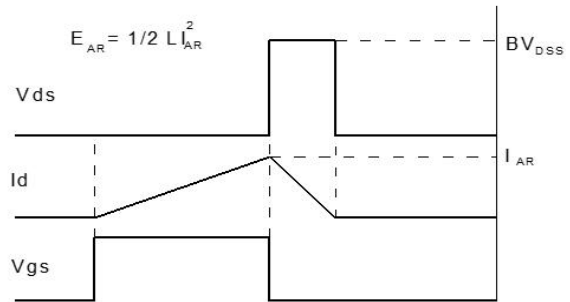
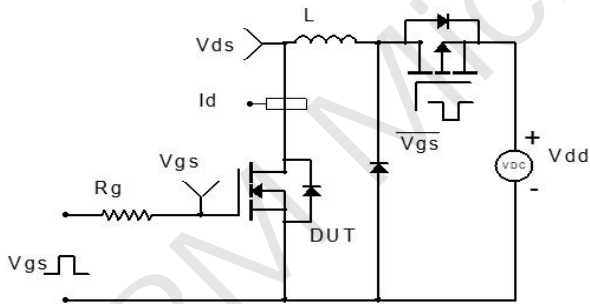
**Test Circuit**



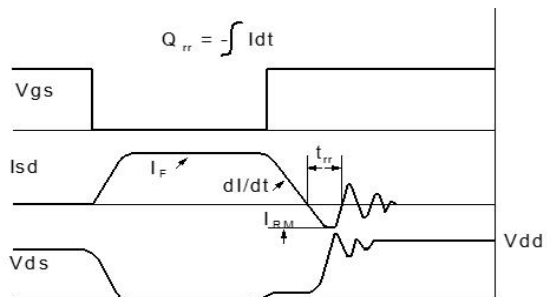
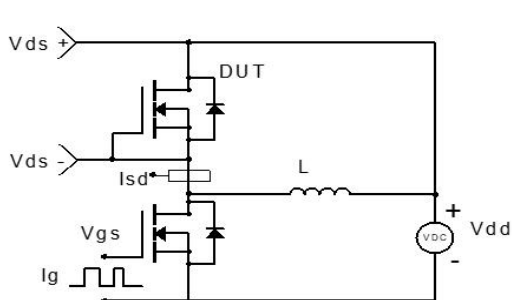
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**

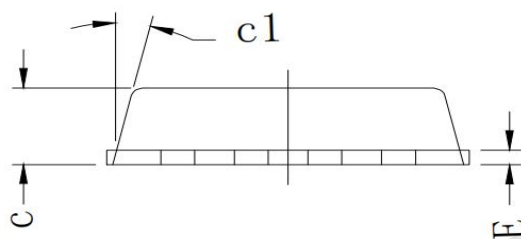
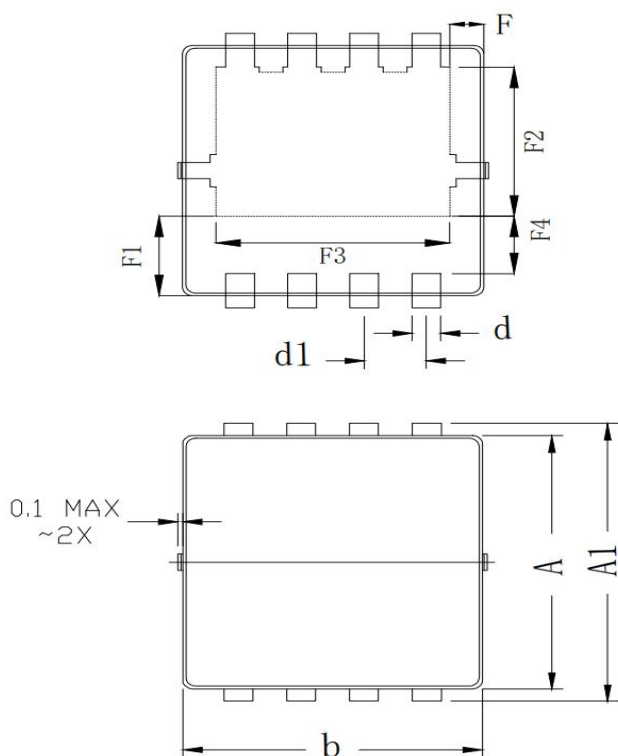


**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



**Figure 4: Diode Recovery Test Circuit & Waveform**

### Package Mechanical Data(PDFN3.3x3.3-8L)




COMMON DIMENSION (MM)			
PKG	PDFN 3.3×3.3-8L		
SYMBOL	MIN	TYP	MAX
A	3.070	3.100	3.130
A1	3.300	3.400	3.500
b	3.070	3.100	3.130
c	0.770	0.800	0.830
c1	-	13°	-
d	0.275	0.300	0.325
d1	0.625	0.650	0.675
E	0.144	0.152	0.160
F	0.300	0.325	0.350
F1	0.960	0.985	1.010
F2	1.775	1.800	1.825
F3	2.425	2.450	2.475
F4	0.660	0.685	0.710

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