

Features

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness
- Fast Reverse Recovery
- Halogen Free, RoHS Compliant

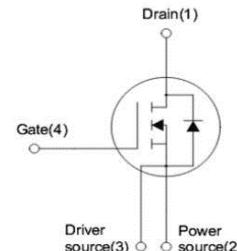
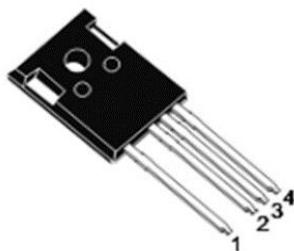
Product Summary

VDS	650V
R _{DS(on)} _typ	25mΩ
I _D	97A

Applications

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- On Board Charger

100% Avalanche Tested



Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRXQF25M065G1	-	TO-247-4L	Tube	N/A	N/A	25pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DSmax}	650	V
Continuous drain current V _{GS} =20V, T _C = 25°C V _{GS} =20V, T _C = 100°C	I _D	97 69	A
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _{D(pulse)}	243	A
Avalanche energy, single pulse (L=10mH, R _g =25Ω)	E _{AS}	2000	mJ
Gate-Source voltage (dynamic)	V _{GSmax}	-10/+25	V
Gate-Source voltage (static)	V _{GSop}	-5/+20	V
Power dissipation (T _C =25°C, T _J =175°C)	P _D	429	W
Operating junction temperature	T _j	-55...175	°C
Storage temperature	T _{stg}	-55...150	°C

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R _{thJC}	0.35	°C/W
Thermal resistance, junction – ambient. Max	R _{thJA}	40	

Electrical Characteristic (at T_j = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	V _{(BR)DSS}	650	-	-	V	V _{GS} =0V, I _D =250μA
Gate threshold voltage	V _{GS(th)}	2	-	4	V	V _{DS} =V _{GS} , I _D =15mA
Zero gate voltage drain current	I _{DSS}	-	1	100	μA	V _{DS} =650V, V _{GS} =0V T _j =25°C T _j =175°C
Gate-source leakage current	I _{GSS}	-	-	250	nA	V _{GS} =20V, V _{DS} =0V
Drain-source on-state resistance	R _{DS(on)}	-	25	-	mΩ	V _{GS} =18V, ID=33.5A
		-	20	28	mΩ	V _{GS} =20V, ID=33.5A, T _j =25°C T _j =175°C
		-	29	-	mΩ	
Transconductance	g _f	-	20	-	S	V _{DS} =20V, I _{DS} =33.5A

Dynamic Characteristic

Internal Gate resistance	R _{G(int)}	-	1.1	-	Ω	f=1MHz
Input Capacitance	C _{iss}	-	3277	-	pF	V _{GS} =0V, V _{DS} =650V, f=1MHz
Output Capacitance	C _{oss}	-	356	-		
Reverse Transfer Capacitance	C _{rss}	-	32	-		
Cross Stored Energy	E _{oss}	-	79	-	uJ	V _{DS} =650V
Gate Total Charge	Q _g	-	173	-	nC	V _{GS} =-5/20V V _{DS} =400V ID=33.5A
Gate-Source charge	Q _{gs}	-	42	-		
Gate-Drain charge	Q _{gd}	-	39	-		
Turn-on delay time	t _{d(on)}	-	32	-	ns	VDD=400V, ID=33.5A VGS=-5V/20V, RG=10Ω, L=100uH
Rise time	t _r	-	43	-		
Turn-off delay time	t _{d(off)}	-	83	-		
Fall time	t _f	-	22	-		
Turn-On Switching Energy	E _(on)	-	477	-	uJ	
Turn Off Switching Energy	E _(off)	-	114	-		

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	3.2	-	V	$V_{GS}=0V, I_F=16.8A$
		-	2.6	-	V	$V_{GS}=0V, I_F=16.8A, T_j=175^\circ C$
Body Diode Reverse Recovery Time	t_{rr}	-	52	-	ns	$di/dt=1000A/\mu s$ $I_F=33.5A$ $Vdd=400V$
Body Diode Reverse Recovery Charge	Q_{rr}	-	240	-	nC	
Body Diode Peak Reverse Recovery Current	I_{rrm}	-	11	-	A	

Typical Performance Characteristics

Fig 1. Output Characteristics ($T_j=55^\circ\text{C}$)

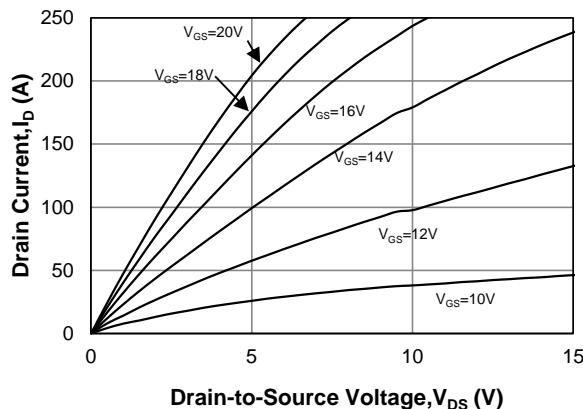


Fig 2. Output Characteristics ($T_j=25^\circ\text{C}$)

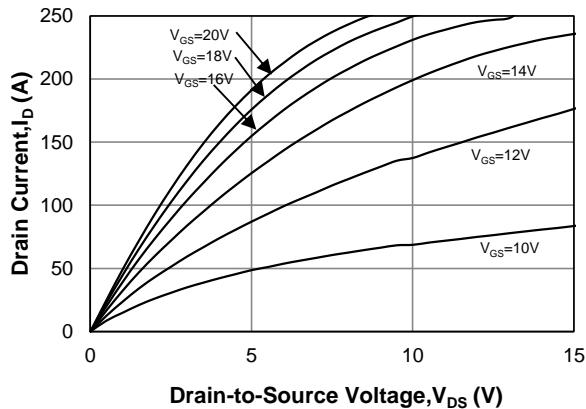


Fig 3. Output Characteristics ($T_j=175^\circ\text{C}$)

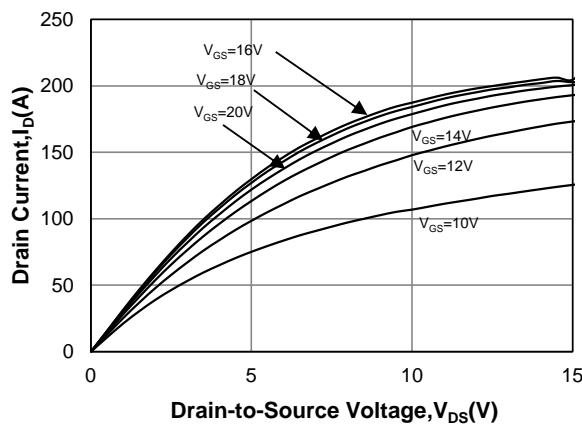


Fig 4: $R_{DS(on)}$ vs. Temperature

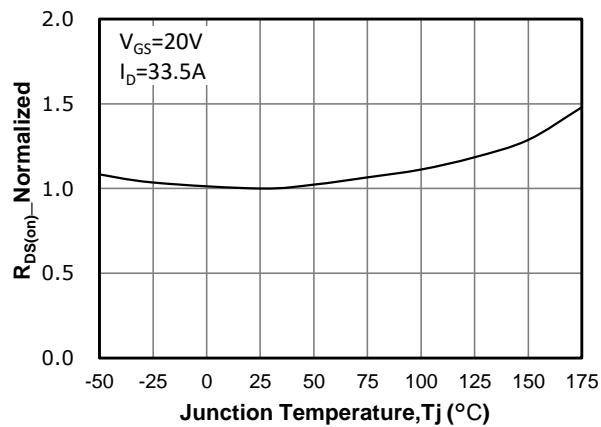


Fig 5: On-Resistance vs. Drain Current For Various Temperatures

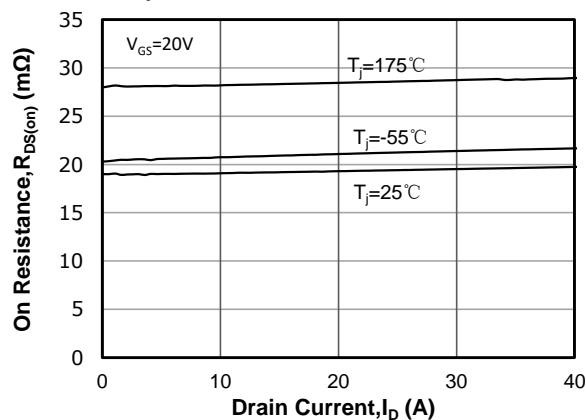


Fig 6: $R_{DS(on)}$ vs. Temperature For Various Gate Voltage

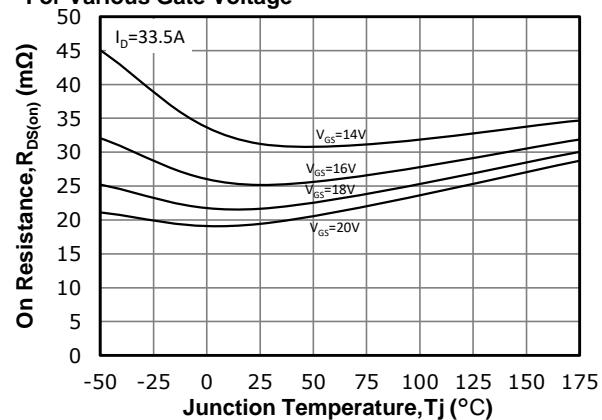


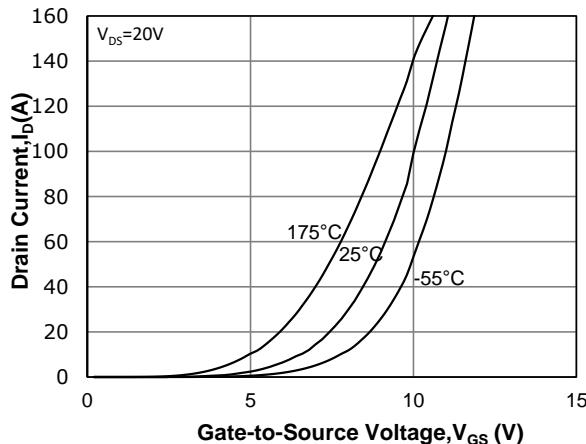
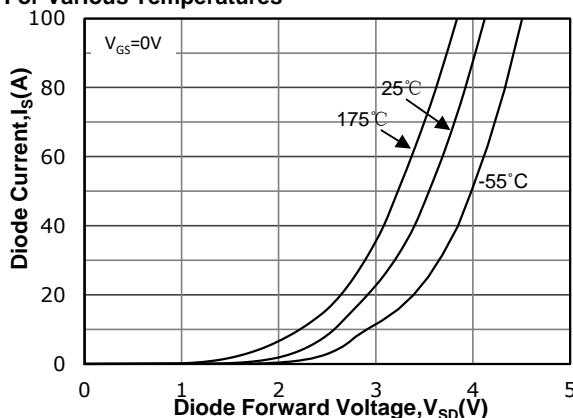
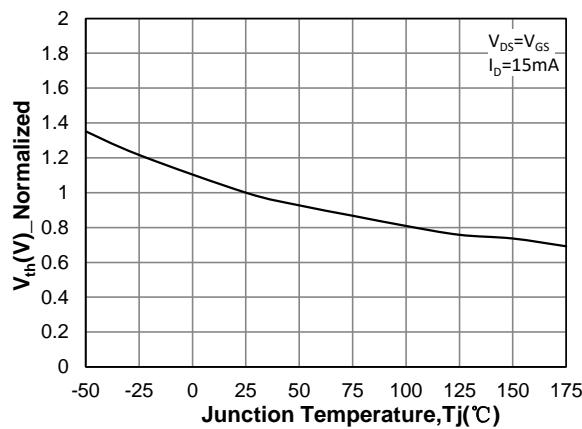
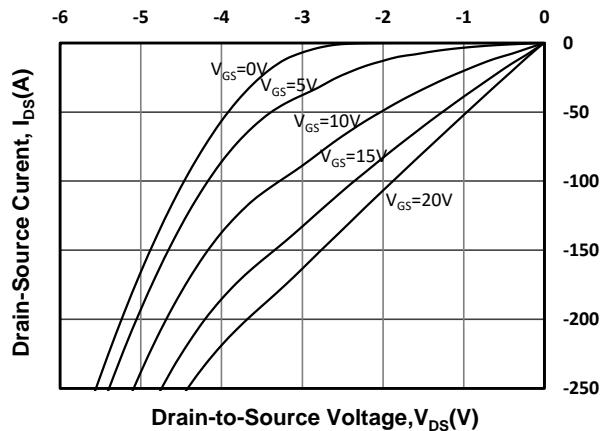
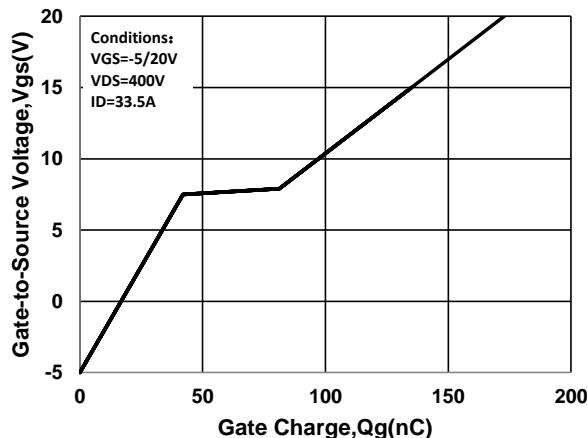
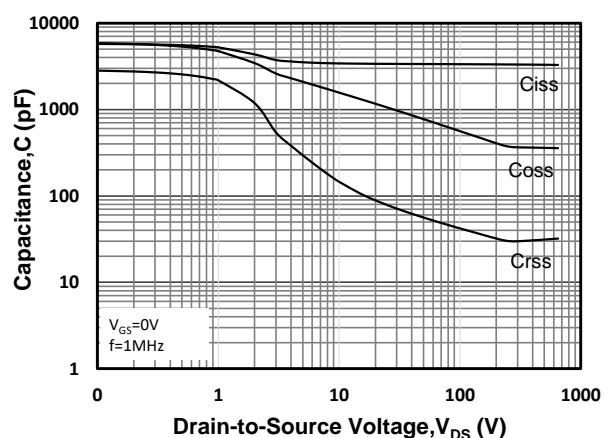
Fig 7: Transfer Characteristics

Fig 8: Body-diode Forward Characteristics For Various Temperatures

Fig 9: $V_{GS(th)}$ Vs T_j Characteristics

Fig 10: 3rd Quadrant Characteristic at 25°C

Fig 11: Gate Charge Characteristics

Fig 12: Capacitance Characteristics


Fig 13: Continuous Drain Current vs. Case Temperature

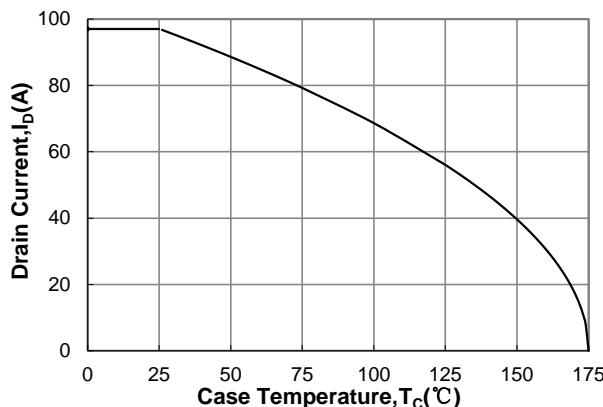


Fig 14: Maximum Power Dissipation vs. Case Temperature

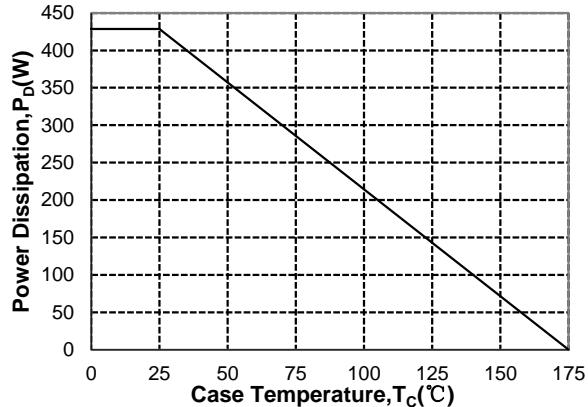


Fig 15: Safe Operating Area

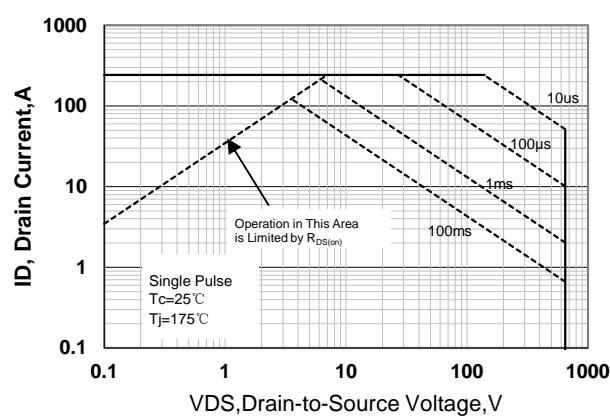


Fig 16: Output Capacitor Stored Energy

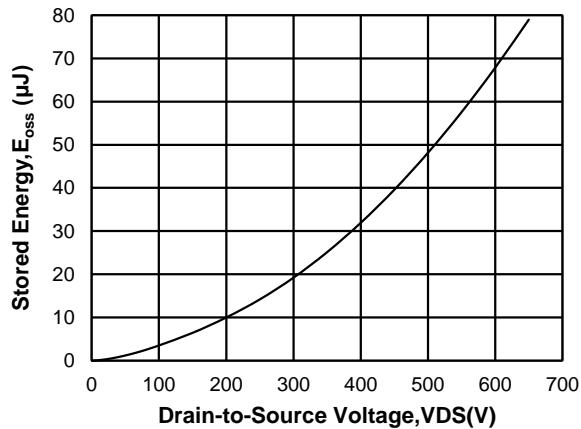
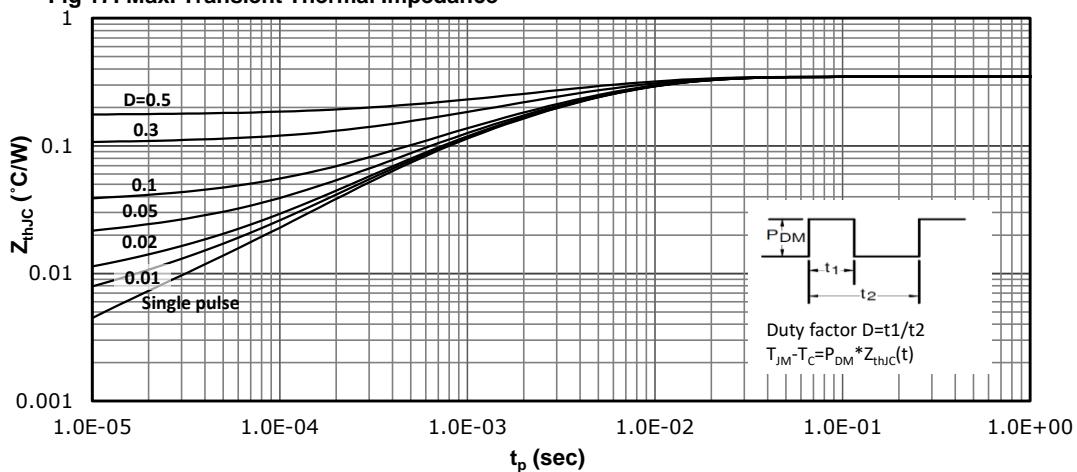
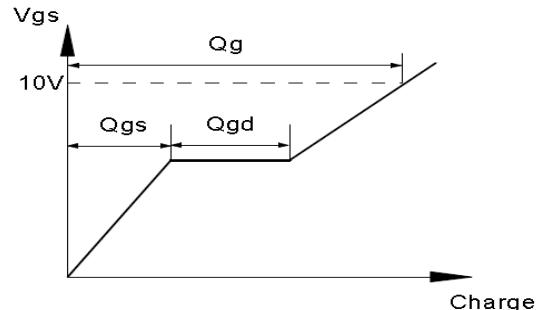
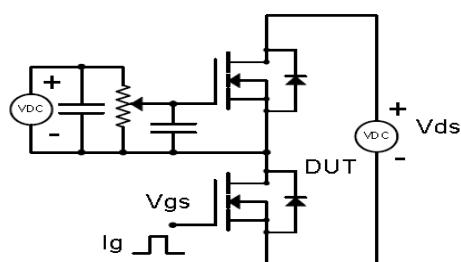


Fig 17: Max. Transient Thermal Impedance

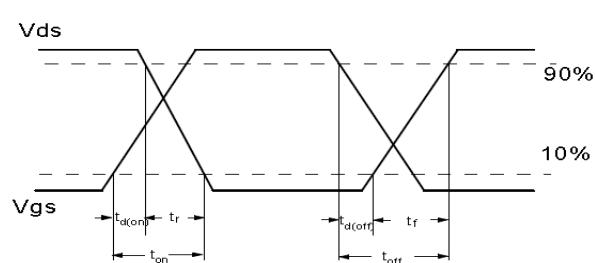
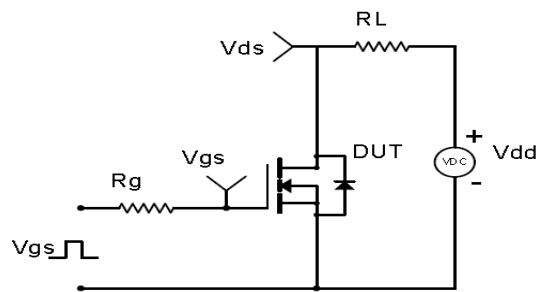


Test Circuit & Waveform

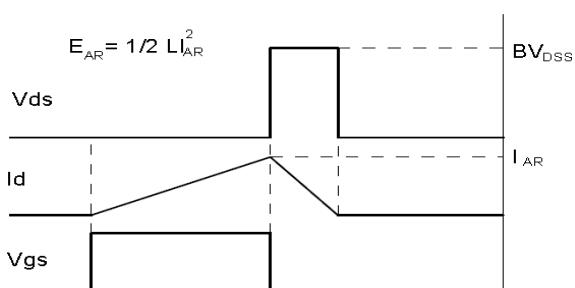
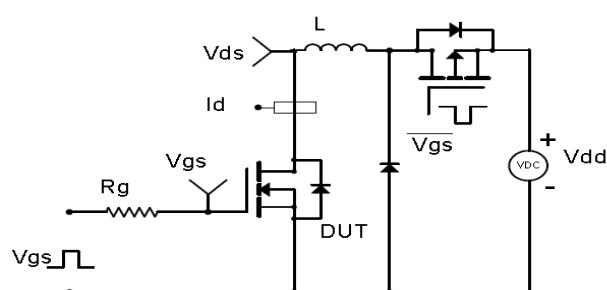
Gate Charge Test Circuit & Waveform



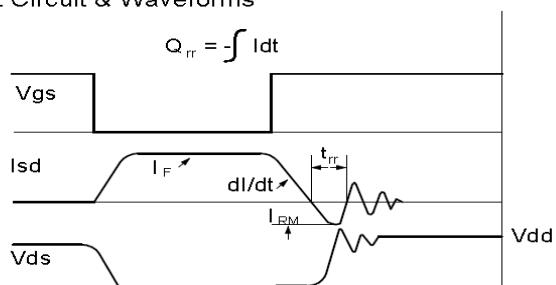
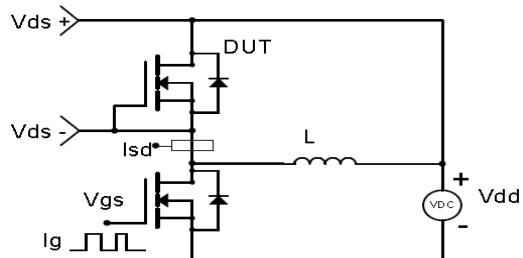
Resistive Switching Test Circuit & Waveforms

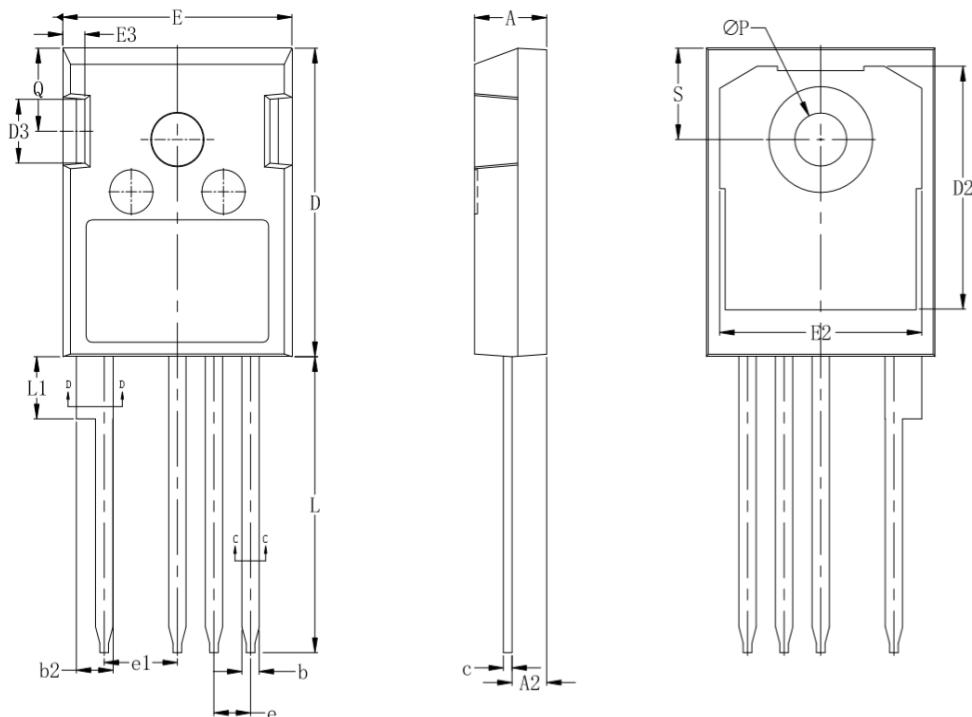


Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: TO-247-4L


Items	Values(mm)	
	MIN	MAX
A	4.8	5.2
A2	2.2	2.6
b	1.05	1.4
b2	2.4	2.75
c	0.5	0.75
D	20	21.5
D2	15.5	17.2
D3	4	5
E	15.5	16.1
E2	13	15
E3	1	2
e	2.54 BSC.	
e1	5.08 BSC.	
L	19	21
L1	4	4.45
ØP	3.5	3.7
Q	5.4	5.9
S	5.9	6.4



CRXQF25M065G1

SiC MOSFET 650V, 25mΩ, 97A

Revision History

Revison	Date	Major changes
1.0	2023/2/9	Release of formal version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM reserves the right to improve product design, function and reliability without notice.