



Silicon N-Channel Power MOSFET



CS100N06 A3

General Description:

CS100N06 A3, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trench technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. This device is suitable for use as a load switch and PWM applications. The package form is TO-251, which accords with the RoHS standard.

Features:

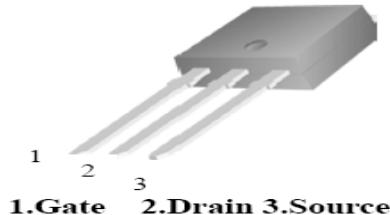
- | Fast Switching
- | Low ON Resistance
- | Low Gate Charge
- | Low Reverse transfer capacitances
- | 100% Single Pulse avalanche energy Test

Applications:

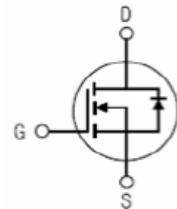
Power switch circuit of adaptor and charger.

V_{DSS}	60	V
I_D (Silicon limited current)	100	A
I_D (Package limited)	60	A
P_D	130.2	W
$R_{DS(ON)Typ}$	5.6	$m\Omega$

TO-251



Inner Equivalent Principium Chart



Absolute ($T_j = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	60	V
I_D	Continuous Drain Current $T_C = 25^\circ C$ (Silicon Limited)	100	A
	Continuous Drain Current $T_C = 25^\circ C$ (Package Limited) a1	60	A
	Continuous Drain Current $T_C = 100^\circ C$	60	A
I_{DM}^{a1}	Pulsed Drain Current $T_C = 25^\circ C$	240	A
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}^{a2}	Avalanche Energy	460	mJ
P_D	Power Dissipation $T_C = 25^\circ C$	130.2	W
	Derating Factor above $25^\circ C$	1.04	$W/\text{ }^\circ C$
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ C$
T_L	Maximum Temperature for Soldering	300	$^\circ C$

**Electrical Characteristics** ($T_j = 25^\circ\text{C}$ unless otherwise specified):

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	100	--	--	V
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=60\text{V}, V_{GS}=0\text{V}, T_j = 25^\circ\text{C}$	--	--	1	μA
		$V_{DS}=48\text{V}, V_{GS}=0\text{V}, T_j = 125^\circ\text{C}$	--	--	100	
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=20\text{V}$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-20\text{V}$	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10\text{V}, I_D=50\text{A}$	--	5.6	6.5	mΩ
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3	4	V
Pulse width $t_p \leq 300\mu\text{s}, \delta \leq 2\%$						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	--	1.4	--	Ω
C_{iss}	Input Capacitance		--	2945	--	pF
C_{oss}	Output Capacitance	$V_{GS} = 0\text{V} V_{DS} = 30\text{V}$ $f = 1.0\text{MHz}$	--	332	--	
C_{rss}	Reverse Transfer Capacitance		--	203	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$V_{GS}=10\text{V}, R_G=2.7\Omega$ $V_{DD}=30\text{V}, I_D=57\text{A}$	--	21.3	--	ns
t_r	Rise Time		--	17.2	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	44	--	
t_f	Fall Time		--	13.4	--	
Q_g	Total Gate Charge	$I_D=57\text{A} V_{DD}=30\text{V}$ $V_{GS} = 10\text{V}$	--	58	--	nC
Q_{gs}	Gate to Source Charge		--	13.9	--	
Q_{gd}	Gate to Drain ("Miller")Charge		--	20.5	--	



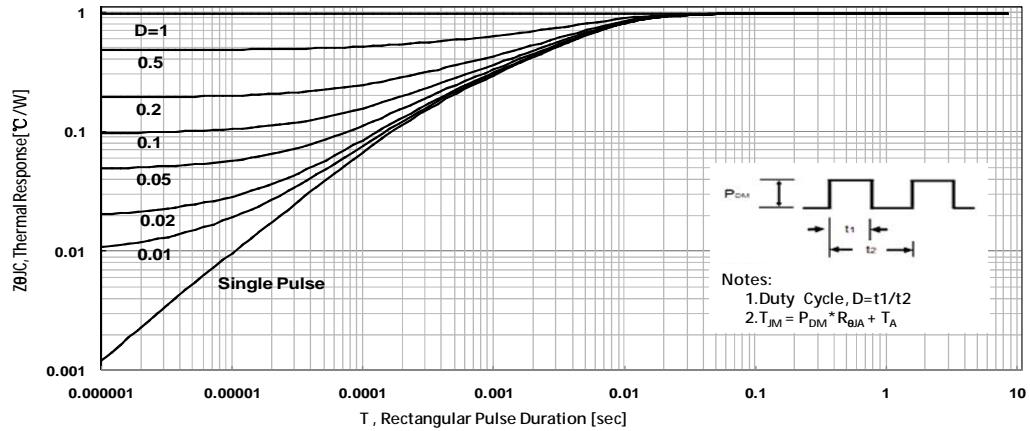
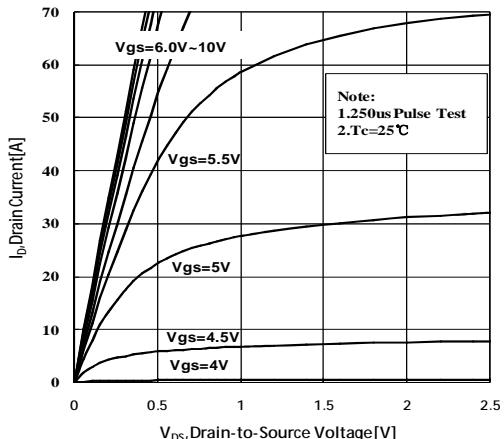
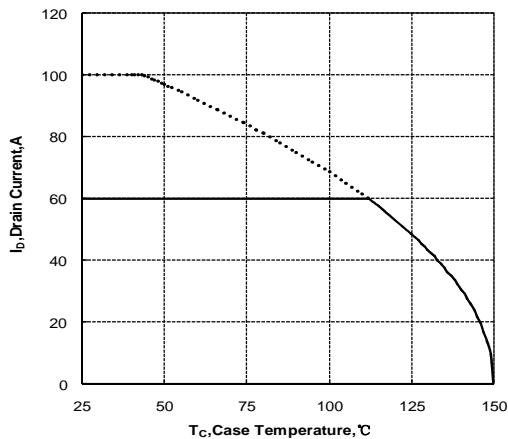
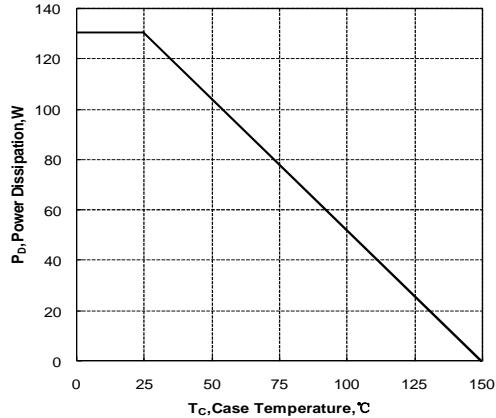
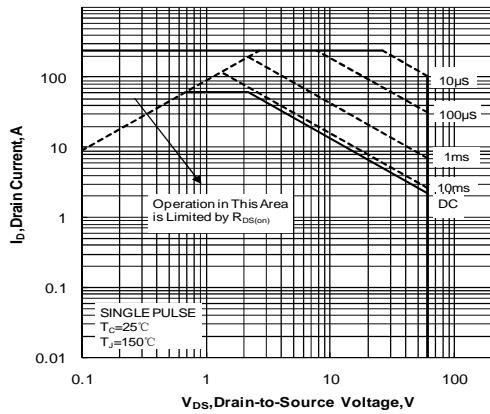
Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I _S	Continuous Source Current (Body Diode)	T _C = 25 °C	--	--	60	A
I _{SM}	Maximum Pulsed Current (Body Diode)		--	--	240	A
V _{SD}	Diode Forward Voltage	I _S =50A, V _{GS} =0V	--	--	1.2	V
t _{rr}	Reverse Recovery Time	di/dt=100A/us I _F =57A	--	48	--	ns
Q _{rr}	Reverse Recovery Charge		--	69.6	--	nC
Pulse width t _p ≤ 300μs, δ ≤ 2%						

Symbol	Parameter	Max.	Units
R _{θJC}	Junction-to-Case	0.96	°C/W
R _{θJA}	Junction-to-Ambient	75	°C/W

^{a1}: Calculated continuous current based on maximum allowable junction temperature. Note that current limitations arising from heating of the device leads may occur with some lead mounting arrangements.

^{a2}: L=0.5mH,I_{as}=42.9A Start T_j=25°C

Characteristics Curve:



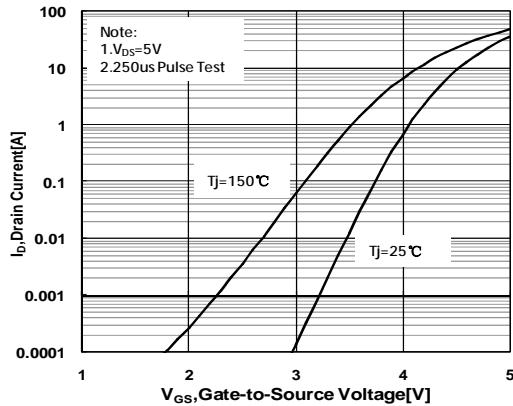


Figure 6 Typical Transfer Characteristics

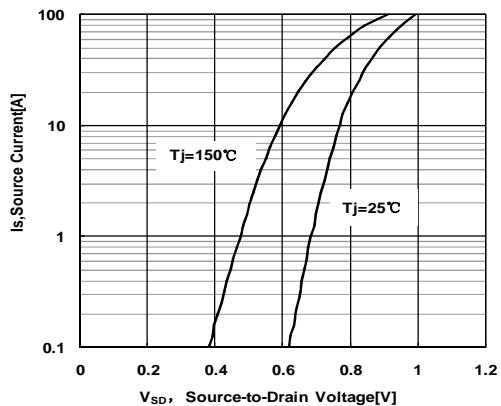


Figure 7 Typical Body Diode Transfer Characteristics

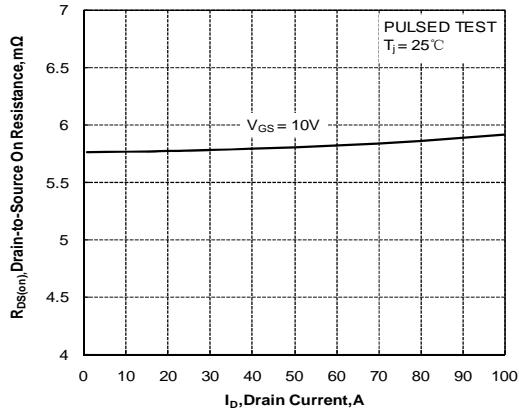


Figure 8. Drain-to-Source On Resistance vs Drain Current

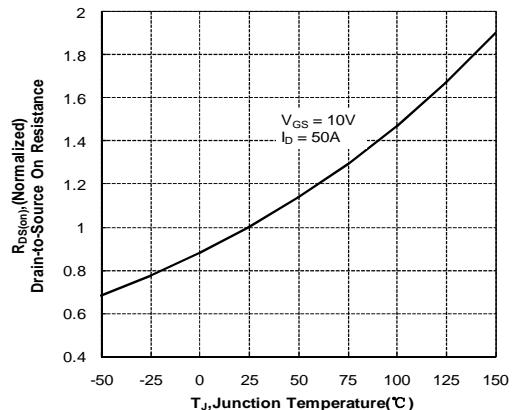


Figure 9. Normalized On Resistance vs Junction Temperature

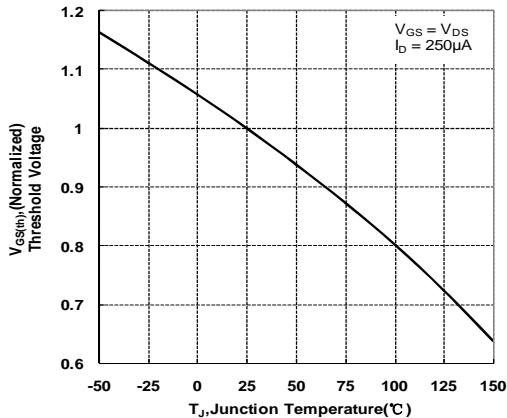


Figure10. Normalized Threshold Voltage vs Junction Temperature

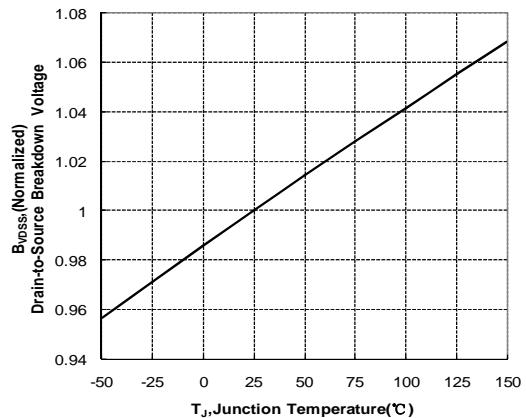
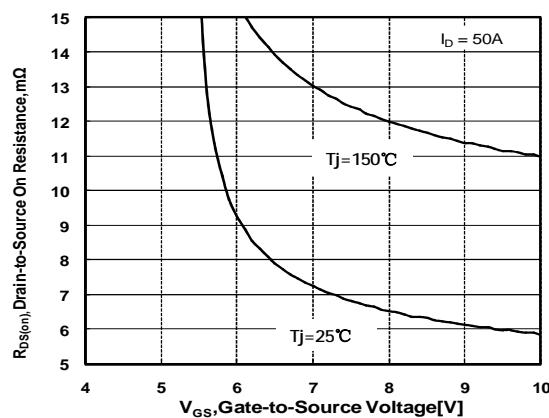
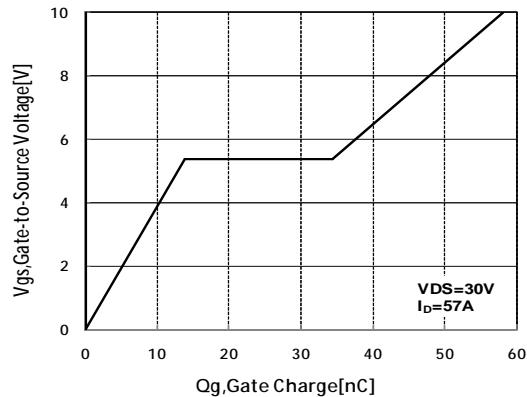
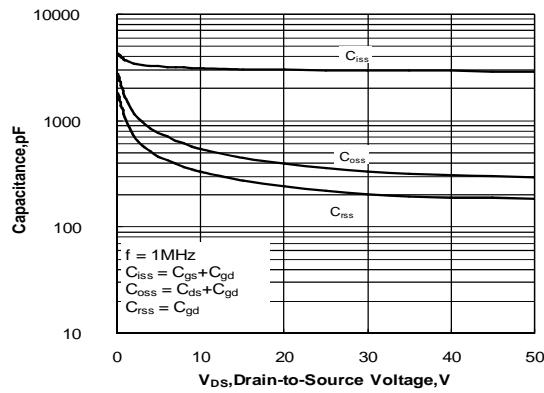


Figure11. Normalized Breakdown Voltage vs Junction Temperature



Test Circuit and Waveform

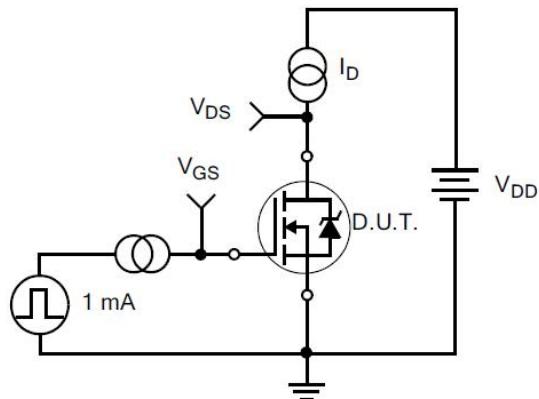


Figure 15. Gate Charge Test Circuit



Figure 16. Gate Charge Waveforms

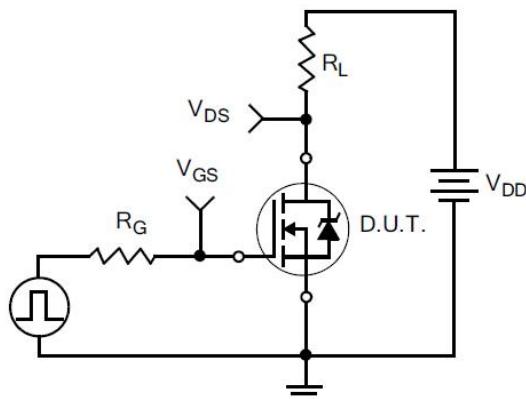


Figure 17. Resistive Switching Test Circuit

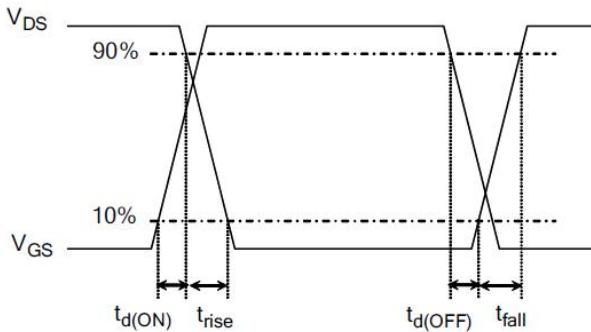


Figure 18. Resistive Switching Waveforms



Figure 19. Diode Reverse Recovery Test Circuit



Figure 20. Diode Reverse Recovery Waveform



Figure 21. Unclamped Inductive Switching Test Circuit

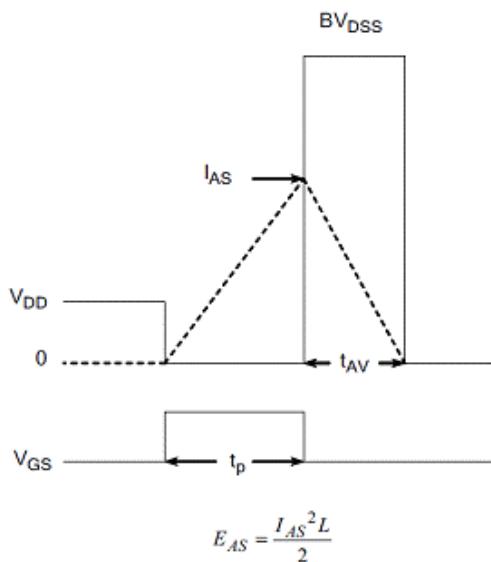
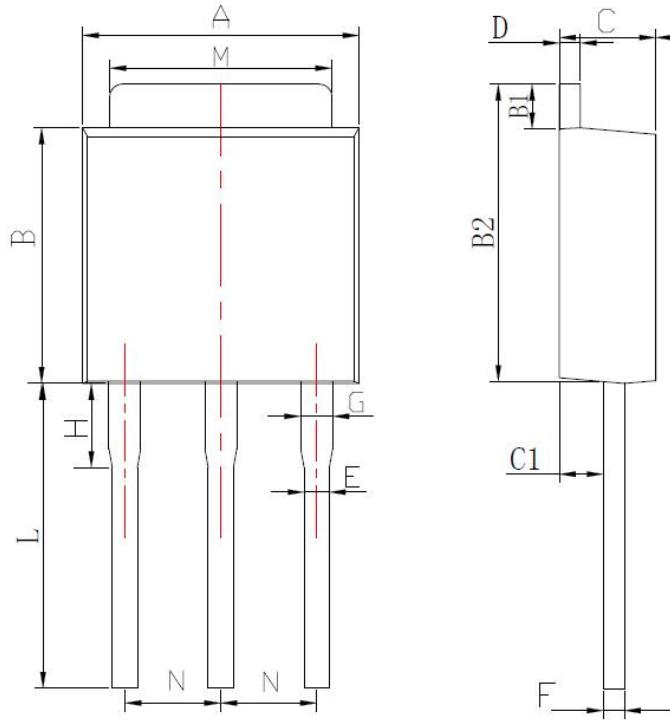


Figure 22. Unclamped Inductive Switching Waveform

**Package Information:**

Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
B	5.20	6.30
B1	0.70	1.30
B2	6.80	7.40
C	2.10	2.50
C1	0.90	1.20
D	0.30	0.60
E	0.50	0.86
F	0.30	0.60
G	0.70	1.00
H	1.40	2.40
L*	9.00	9.80
M	5.10	5.50
N	2.09	2.49

*adjustable

TO-251 Package

**The name and content of poisonous and harmful material in products**

	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤0.1%	≤0.1%	≤0.01%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Solder	×	○	○	○	○	○	○	○	○	○
Note	○: Means the hazardous material is under the criterion of 2011/65/EU. ×: Means the hazardous material exceeds the criterion of 2011/65/EU. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.									

Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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