

**CS10N80F A9D****General Description:**

CS10N80F A9D, the silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-220F, which accords with the RoHS standard.

Features:

- I **Fast Switching**
- I **ESD Improved Capability**
- I **Low Gate Charge** (Typical Data: 65nC)
- I **Low Reverse transfer capacitances**(Typical: 25pF)
- I **100% Single Pulse avalanche energy Test**

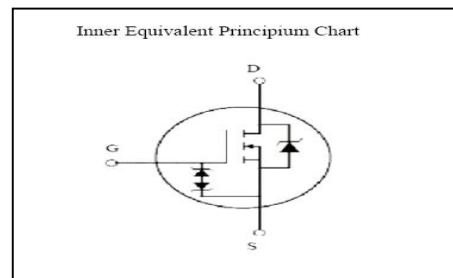
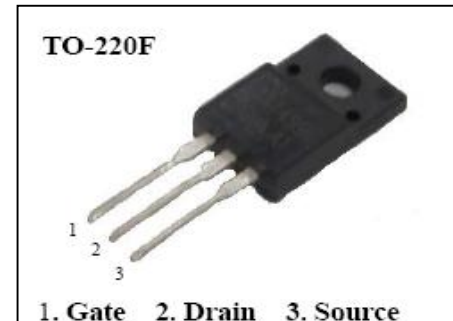
Applications:

Power switch circuit of PC POWER.

Absolute (Tc= 25°C unless otherwise specified):

| Symbol | Parameter | Rating | Units |
|-----------------------------------|--|-----------------|-------|
| V _{DSS} | Drain-to-Source Voltage | 800 | V |
| I _D | Continuous Drain Current | 10 | A |
| | Continuous Drain Current T _C = 100 °C | 6.5 | A |
| I _{DM} ^{a1} | Pulsed Drain Current | 40 | A |
| V _{GS} | Gate-to-Source Voltage | ±30 | V |
| E _{AS} | Single Pulse Avalanche Energy | 997 | mJ |
| E _{AR} ^{a1} | Avalanche Energy ,Repetitive | 40 | mJ |
| I _{AR} ^{a1} | Avalanche Current | 2.8 | A |
| dv/dt ^{a2} | Peak Diode Recovery dv/dt | 5.0 | V/ns |
| P _D | Power Dissipation | 60 | W |
| | Derating Factor above 25°C | 0.48 | W/°C |
| V _{ESD(G-S)} | Gate source ESD (HBM-C= 100pF, R=1.5kΩ) | 6000 | V |
| T _J , T _{stg} | Operating Junction and Storage Temperature Range | 150, -55 to 150 | °C |
| T _L | Maximum Temperature for Soldering | 300 | °C |

| | | |
|---------------------------------------|------|---|
| V _{DSS} | 800 | V |
| I _D | 10 | A |
| P _D (T _C =25°C) | 60 | W |
| R _{DS(ON)Typ} | 0.72 | Ω |



Electrical Characteristics (Tc= 25°C unless otherwise specified):

| OFF Characteristics | | | | | | |
|-------------------------------------|-----------------------------------|--|--------|------|------|-------|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| V _{DSS} | Drain to Source Breakdown Voltage | V _{GS} =0V, I _D =250μA | 800 | -- | -- | V |
| ΔBV _{DSS} /ΔT _J | Bvdss Temperature Coefficient | I _D =250uA, Reference 25°C | -- | 0.5 | -- | V/°C |
| I _{DSS} | Drain to Source Leakage Current | V _{DS} =900V, V _{GS} = 0V, T _a = 25°C | -- | -- | 25 | μA |
| | | V _{DS} =720V, V _{GS} = 0V, T _a = 25°C | -- | -- | 250 | μA |
| I _{GSS(F)} | Gate to Source Forward Leakage | V _{GS} =+20V | -- | -- | 10 | μA |
| I _{GSS(R)} | Gate to Source Reverse Leakage | V _{GS} =-20V | -- | -- | -10 | μA |

| ON Characteristics | | | | | | |
|----------------------------|-------------------------------|--|--------|------|------|-------|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| R _{DS(ON)} | Drain-to-Source On-Resistance | V _{GS} =10V, I _D =5A | -- | 0.72 | 0.9 | Ω |
| V _{GS(TH)} | Gate Threshold Voltage | V _{DS} = V _{GS} , I _D = 250μA | 2.0 | | 4.0 | V |
| Pulse width tp≤300μs, δ≤2% | | | | | | |

| Dynamic Characteristics | | | | | | |
|-------------------------|------------------------------|--|--------|------|------|-------|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| g _{fs} | Forward Trans conductance | V _{DS} =15V, I _D =10A | | 20 | -- | S |
| C _{iss} | Input Capacitance | V _{GS} = 0V V _{DS} = 25V f = 1.0MHz | -- | 2900 | | pF |
| C _{oss} | Output Capacitance | | -- | 200 | | |
| C _{rss} | Reverse Transfer Capacitance | | -- | 25 | | |

| Resistive Switching Characteristics | | | | | | |
|-------------------------------------|--------------------------------|--|--------|------|------|-------|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| t _{d(ON)} | Turn-on Delay Time | I _D =10A V _{DD} =400V V _{GS} = 10V R _G =4.7Ω | -- | 19 | -- | ns |
| t _r | Rise Time | | -- | 10 | -- | |
| t _{d(OFF)} | Turn-Off Delay Time | | -- | 68 | -- | |
| t _f | Fall Time | | -- | 23 | -- | |
| Q _g | Total Gate Charge | I _D =10A V _{DD} =640V V _{GS} = 10V | -- | 65 | | nC |
| Q _{gs} | Gate to Source Charge | | -- | 13 | | |
| Q _{gd} | Gate to Drain ("Miller")Charge | | -- | 25 | | |

Source-Drain Diode Characteristics

| Symbol | Parameter | Test Conditions | Rating | | | Units |
|----------|--|------------------------------|--------|------|------|---------|
| | | | Min. | Typ. | Max. | |
| I_S | Continuous Source Current (Body Diode) | | -- | -- | 10 | A |
| I_{SM} | Maximum Pulsed Current (Body Diode) | | -- | -- | 40 | A |
| V_{SD} | Diode Forward Voltage | $I_S=10A, V_{GS}=0V$ | -- | -- | 1.5 | V |
| t_{rr} | Reverse Recovery Time | $I_S=10A, T_j = 25^\circ C$ | -- | 200 | -- | ns |
| Q_{rr} | Reverse Recovery Charge | $dI_F/dt=100A/us, V_{GS}=0V$ | -- | 2.2 | -- | μC |

Pulse width $t_p \leq 300\mu s, \delta \leq 2\%$

| Symbol | Parameter | Typ. | Units |
|-----------------|---------------------|------|--------------|
| $R_{\theta JC}$ | Junction-to-Case | 2.08 | $^\circ C/W$ |
| $R_{\theta JA}$ | Junction-to-Ambient | 100 | $^\circ C/W$ |

Gate-source Zener diode

| Symbol | Parameter | Test Conditions | Rating | | | Units |
|-----------|-------------------------------|---------------------------------|--------|------|------|-------|
| | | | Min. | Typ. | Max. | |
| V_{GSO} | Gate-source breakdown voltage | $I_{GS} = \pm 1mA (Open Drain)$ | 30 | | | V |

The built-in back-to-back Zener diodes have specifically been designed to enhance not only the device's ESD capability, but also to make them safely absorb possible voltage transients that may occasionally be applied from gate to source. In this respect the Zener voltage is appropriate to achieve an efficient and cost-effective intervention to protect the device's integrity. These integrated Zener diodes thus avoid the usage of external components.

^{a1}: Repetitive rating; pulse width limited by maximum junction temperature

^{a2}: $I_{SD} = 9A, di/dt \leq 100A/us, V_{DD} \leq BV_{DS}, Start T_j = 25^\circ C$

Characteristics Curve:

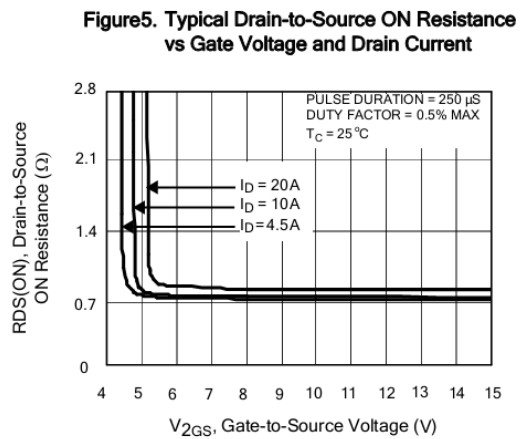
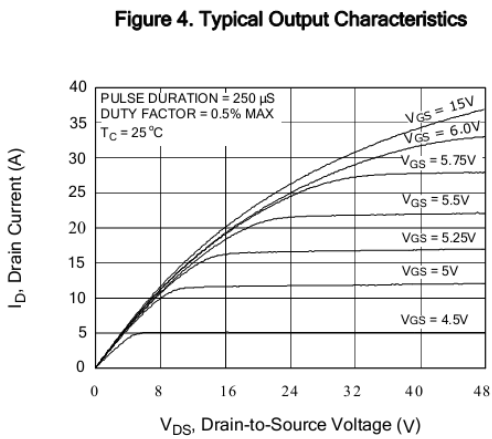
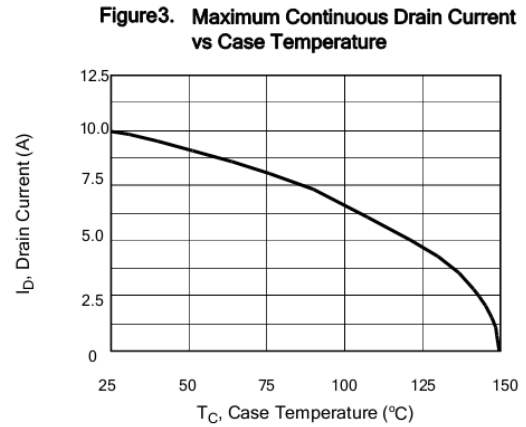
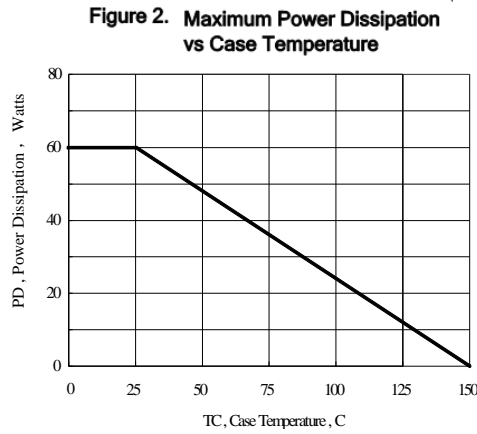
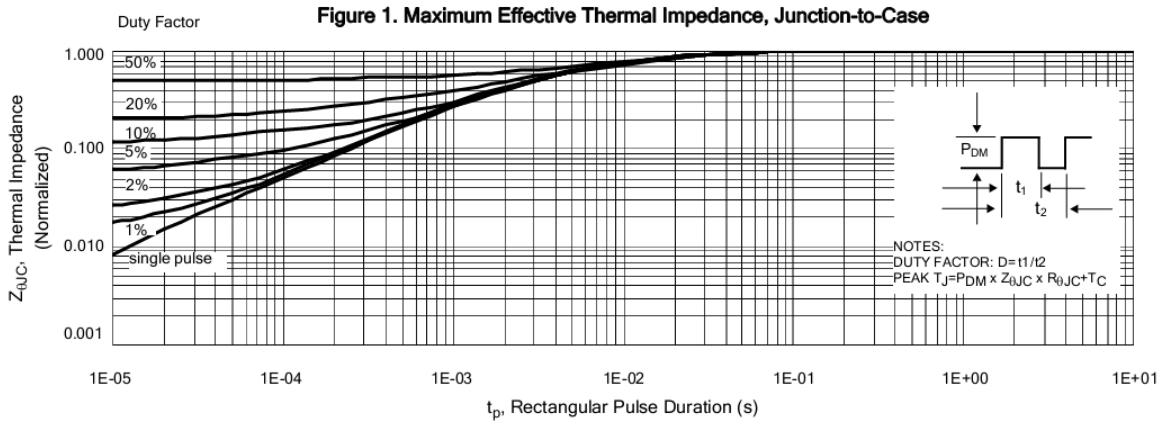


Figure 6. Maximum Peak Current Capability

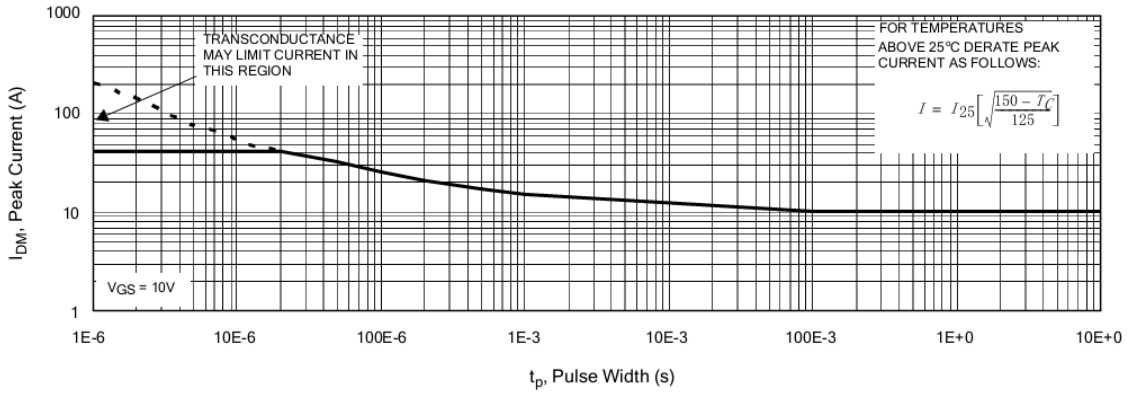


Figure 7. Typical Transfer Characteristics

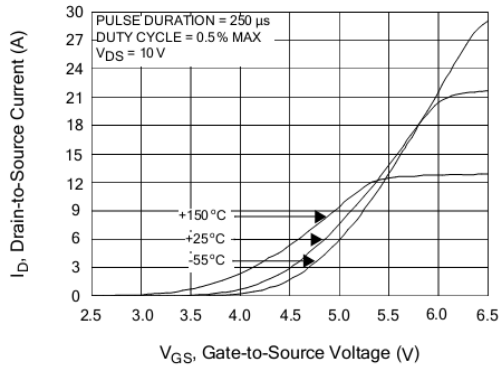


Figure 8. Unclamped Inductive Switching Capability

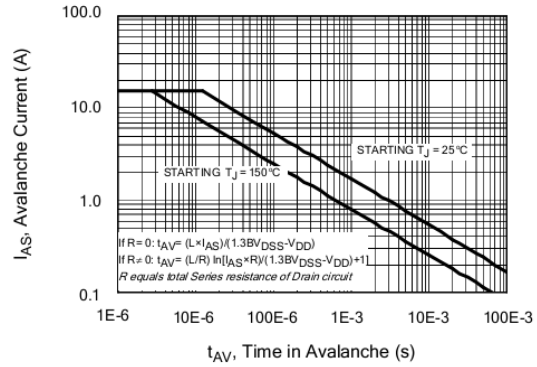


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

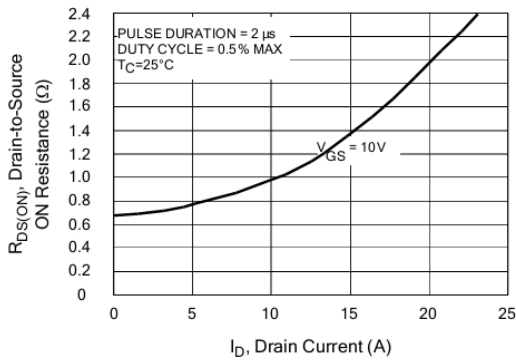


Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature

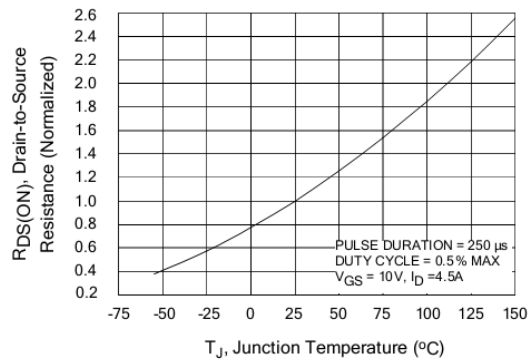


Figure 11. Typical Breakdown Voltage vs Junction Temperature

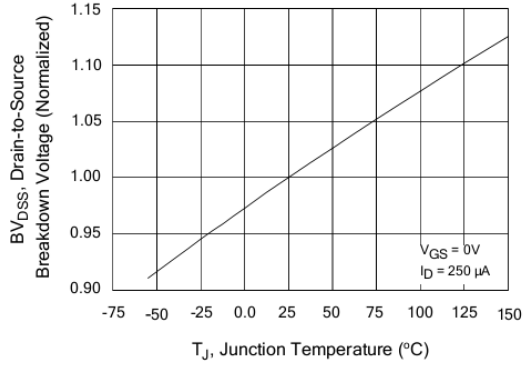


Figure 12. Typical Threshold Voltage vs Junction Temperature

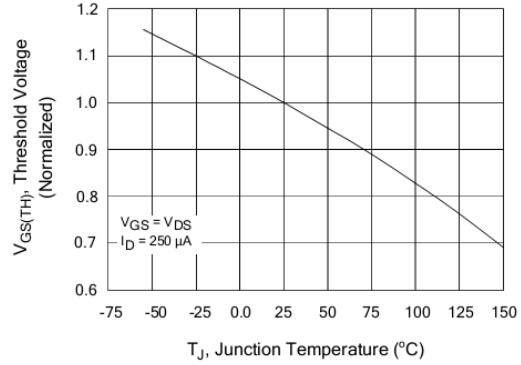


Figure 13. Maximum Forward Bias Safe Operating Area

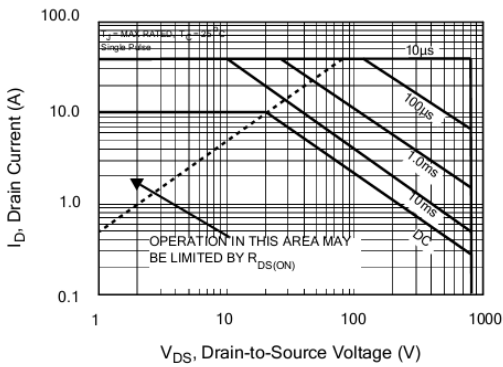


Figure 14. Typical Capacitance vs Drain-to-Source Voltage

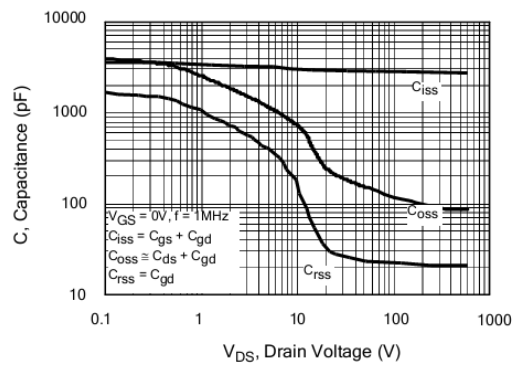


Figure 15. Typical Gate Charge vs Gate-to-Source Voltage

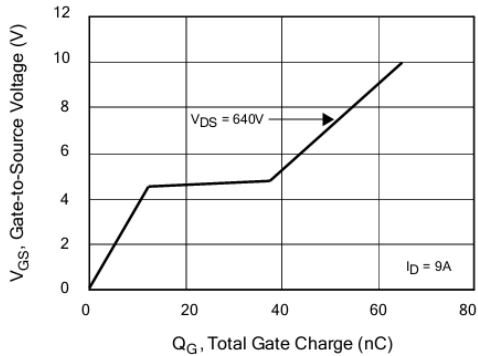
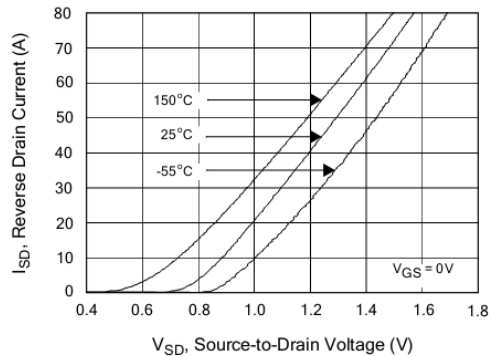


Figure 16. Typical Body Diode Transfer Characteristics



Test Circuit and Waveform

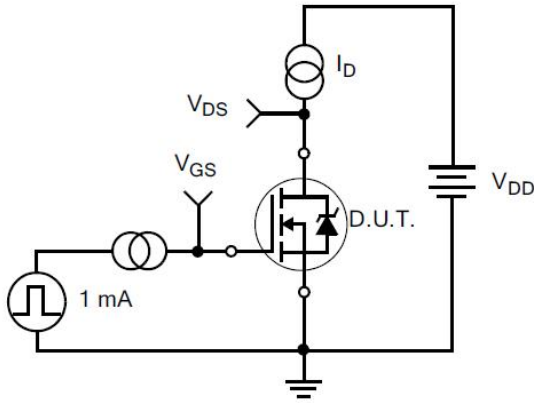


Figure 17. Gate Charge Test Circuit



Figure 18. Gate Charge Waveform



Figure 19. Resistive Switching Test Circuit

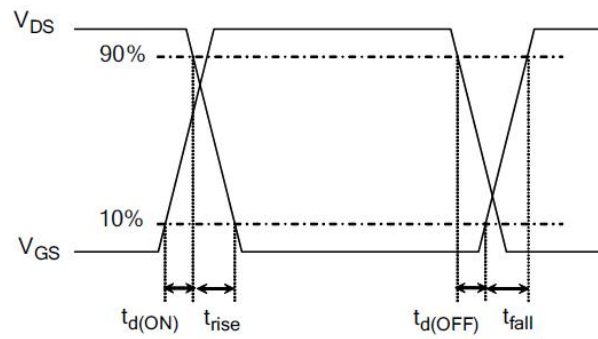


Figure 20. Resistive Switching Waveforms

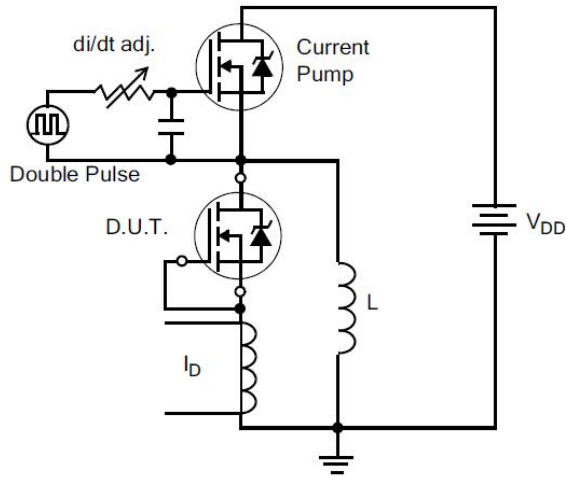


Figure 21. Diode Reverse Recovery Test Circuit

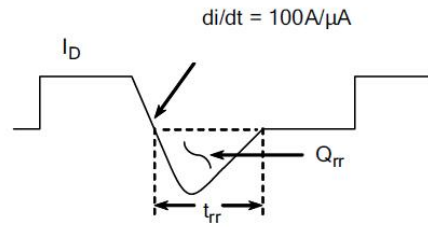


Figure 22. Diode Reverse Recovery Waveform

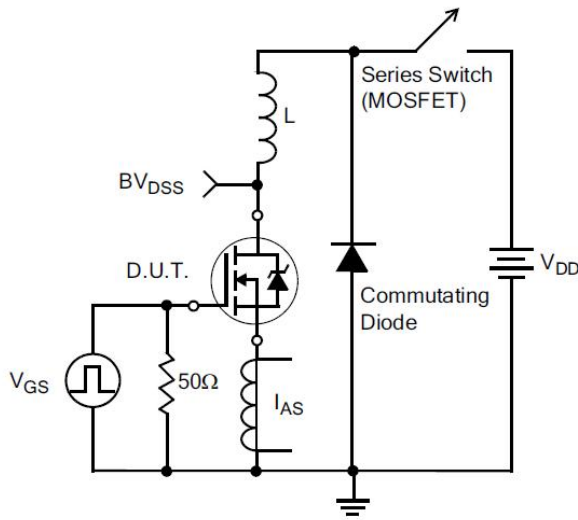


Figure 23. Unclamped Inductive Switching Test Circuit

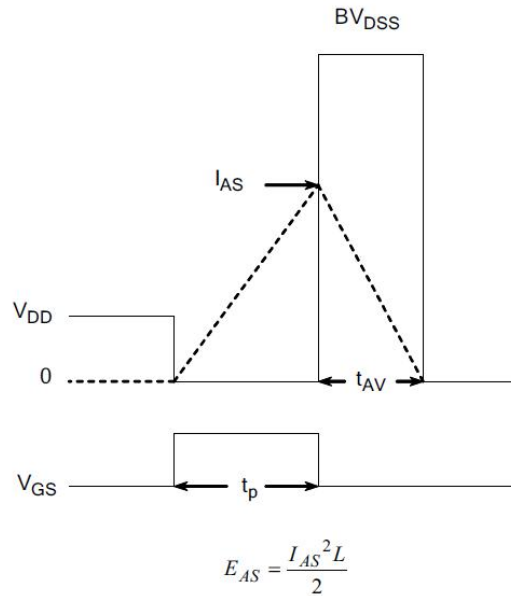
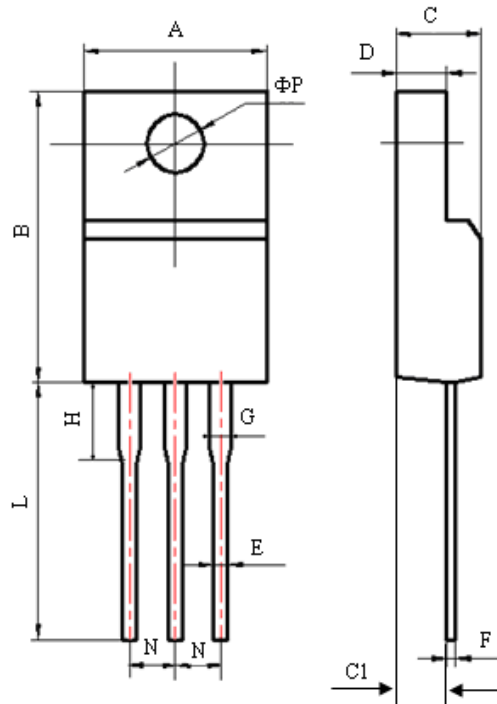


Figure 24. Unclamped Inductive Switching Waveforms

Package Information:



| Items | Values(mm) | |
|----------|------------|-------|
| | MIN | MAX |
| A | 9.60 | 10.40 |
| B | 15.40 | 16.20 |
| C | 4.30 | 4.90 |
| C1 | 2.10 | 3.00 |
| D | 2.40 | 3.00 |
| E | 0.60 | 1.00 |
| F | 0.30 | 0.60 |
| G | 1.12 | 1.42 |
| H | 3.40 | 3.80 |
| | 2.00 | 2.40 |
| L | 12.00 | 14.00 |
| | 6.30 | 7.70 |
| N | 2.34 | 2.74 |
| ϕP | 3.00 | 3.30 |

TO-220F Package

The name and content of poisonous and harmful material in products

| Part's Name | Hazardous Substance | | | | | |
|------------------|---|-------|--------|--------|-------|-------|
| | Pb | Hg | Cd | Cr(VI) | PBB | PBDE |
| Limit | ≤0.1% | ≤0.1% | ≤0.01% | ≤0.1% | ≤0.1% | ≤0.1% |
| Lead Frame | ○ | ○ | ○ | ○ | ○ | ○ |
| Molding Compound | ○ | ○ | ○ | ○ | ○ | ○ |
| Chip | ○ | ○ | ○ | ○ | ○ | ○ |
| Wire Bonding | ○ | ○ | ○ | ○ | ○ | ○ |
| Solder | × | ○ | ○ | ○ | ○ | ○ |
| Note | ○: means the hazardous material is under the criterion of SJ/T11363-2006. ×: means the hazardous material exceeds the criterion of SJ/T11363-2006. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS. | | | | | |

Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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