

### ■ Features

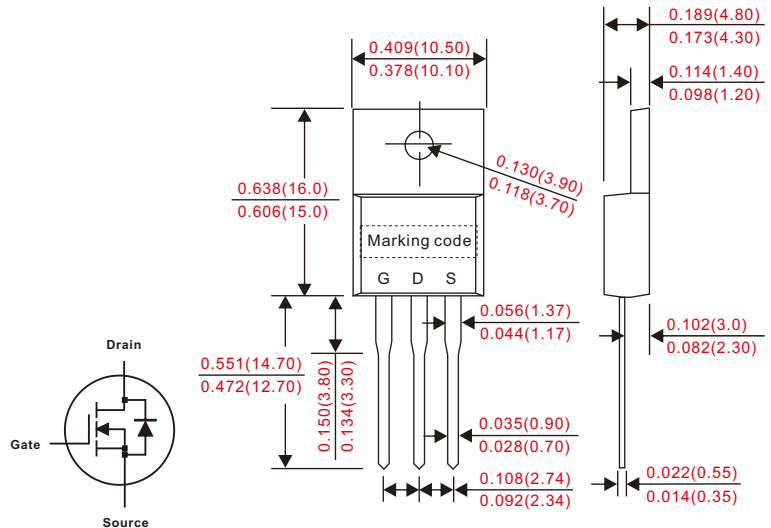
- Fast switching.
- ESD improved capability.
- Low gate charge.
- Low reverse transfer capacitances.
- 100% single pulse avalanche energy test.

### ■ Mechanical data

- Epoxy : UL94-V0 rated flame retardant.
- Case : JEDEC TO-220AB molded plastic body.
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026.
- Polarity: As marked.
- Mounting Position : Any.
- Weight : Approximated 2.25 gram.

### ■ Outline

TO-220AB



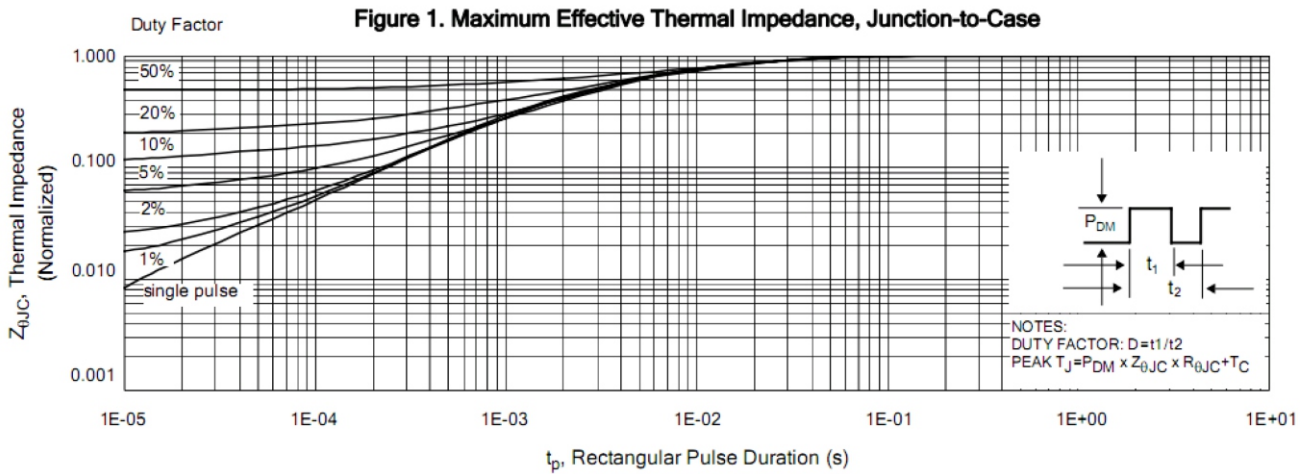
### ■ Absolute ( $T_c = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER	CONDITIONS	Symbol	CS3025A8	UNIT
Drain-Source Voltage		$V_{DSS}$	60	V
Continuous Drain Current(1)		$I_D$	120	A
Continuous Drain Current	$T_c = 100^\circ\text{C}$		84	
Pulsed Drain Current(2)		$I_{DM}$	480	
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Single Pulse Avalanche Energy(3)		$E_{AS}$	1300	mJ
Power Dissipation		$P_D$	230	W
	Derating factor above $25^\circ\text{C}$		1.4	$\text{W}/^\circ\text{C}$
Peak Diode Recovery $dv/dt$ (4)		$dv/dt$	3	V/ns
Operating and Storage Temperature Range		$T_J, T_{STG}$	$-55 \sim +175$	$^\circ\text{C}$
Maximum temperature for soldering		$T_L$	300	$^\circ\text{C}$

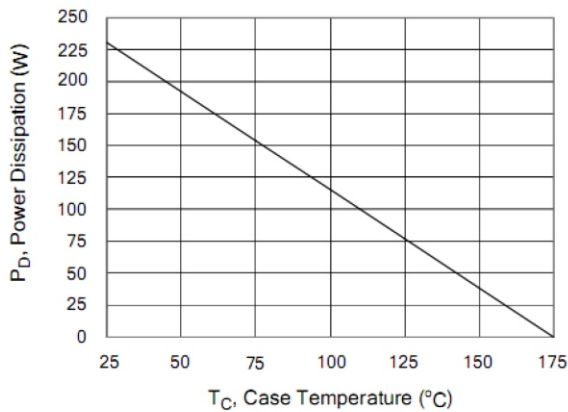
NOTE : 1. Drain Current limited by Maximum Package Current Rating, 75 Amps.  
 2. Repetitive rating; pulse width limited by maximum junction temperature.  
 3.  $L=260\mu\text{H}$ ,  $I_b = 101.5\text{A}$ , Start  $T_J = 25^\circ\text{C}$ .  
 4.  $I_{SD} = 50\text{A}$ ,  $di/dt \leq 100\text{A}/\mu\text{s}$ ,  $V_{DS} \leq BV_{DS}$ , Start  $T_J = 25^\circ\text{C}$ .

■ Electrical characteristics( $T_c = 25^\circ\text{C}$ unless otherwise specified)						
PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu\text{A}$	$V_{DSS}$	60			V
Bvdss Temperature Coefficient	$I_D = 250\mu\text{A}$ , Reference $25^\circ\text{C}$	$BV_{DSS} / T_J$		0.08		$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	$V_{DS} = 60V, V_{GS} = 0V, T_a = 25^\circ\text{C}$	$I_{DSS}$			1	uA
	$V_{DS} = 48V, V_{GS} = 0V, T_a = 85^\circ\text{C}$				125	
Gate-Source Leakage Current, Forward	$V_{GS} = 20V$	$I_{GSS(F)}$			100	nA
Gate-Source Leakage Current, Reverse	$V_{GS} = -20V$	$I_{GSS(R)}$			-100	
■ ON Characteristics						
PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(th)}$	2.0		4.0	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 50A$	$R_{DS(on)}$		7	8	$\Omega$
■ Dynamic Characteristics						
PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward Transconductance	$V_{DS} = 15V, I_D = 50A$	$g_{fs}$		80		S
Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0\text{MHz}$	$C_{iss}$		4000		pF
Output Capacitance		$C_{oss}$		750		
Reverse Transfer Capacitance		$C_{rss}$		75		
■ Resistive Switching Characteristics						
PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Turn-on Delay Time	$I_D = 50A, V_{DD} = 30V, V_{GS} = 10V, R_G = 3.6\Omega$	$td_{(ON)}$		17		ns
Rise Time		$tr$		82		
Turn-off Delay Time		$td_{(OFF)}$		58		
Fail Time		$tf$		30		
Total Gate Charge	$I_D = 50A, V_{DD} = 30V, V_{GS} = 10V$	$Q_g$		75		nC
Gate-Source Charge		$Q_{gs}$		18		
Gate-Drain Charge		$Q_{gd}$		26		
■ Source-Drain Diode Characteristics						
PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Continuous Source-Drain Diode Current	Body Diode	$I_S$			120	A
Pulse Diode Forward Current	Body Diode	$I_{SM}$			480	
Body Diode Voltage	$I_S = 50A, V_{GS} = 0V$	$V_{SD}$			1.5	V
Reverse recovery time	$I_S = 50A, T_J = 25^\circ\text{C}, di_f/dt = 100\text{A}/\mu\text{s}, V_{GS} = 0V$	$t_{rr}$		135		ns
Reverse recovery charge		$Q_{rr}$			360	
■ Thermal characteristics						
PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Thermal Resistance	Junction to Case	$R_{\theta JC}$		0.65		$^\circ\text{C}/\text{W}$
	Junction to Ambient	$R_{\theta JA}$		62		

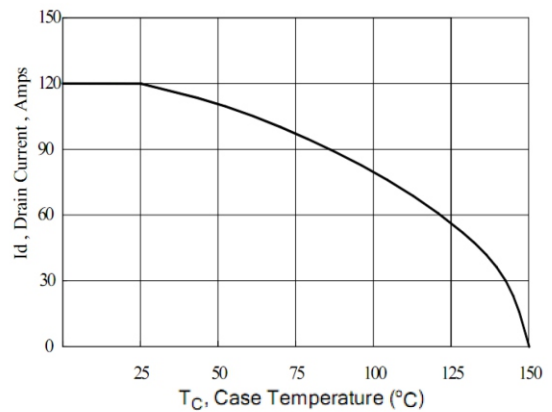
■ Rating and characteristic curves



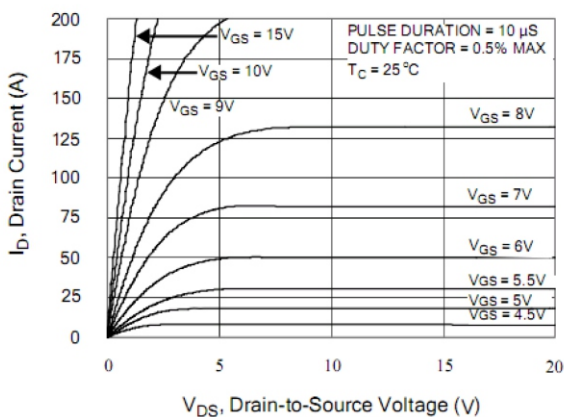
**Figure 2. Maximum Power Dissipation vs Case Temperature**



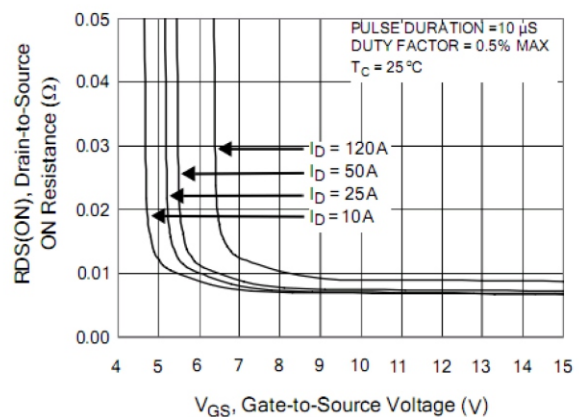
**Figure 3. Maximum Continuous Drain Current vs Case Temperature**



**Figure 4. Typical Output Characteristics**

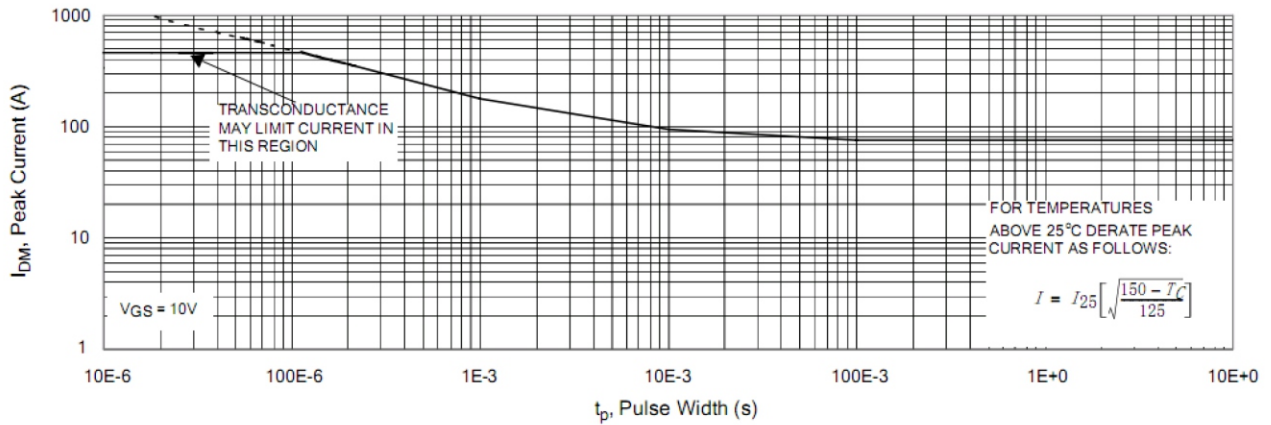


**Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current**

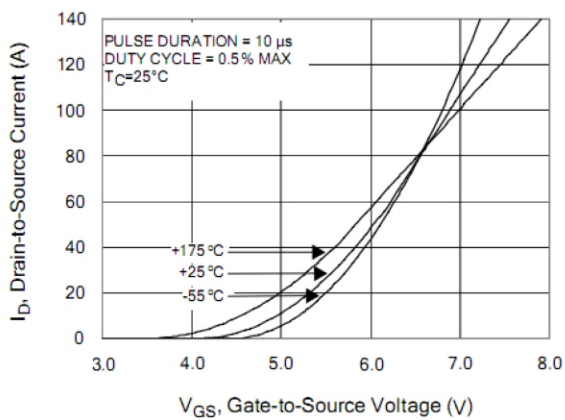


Rating and characteristic curves

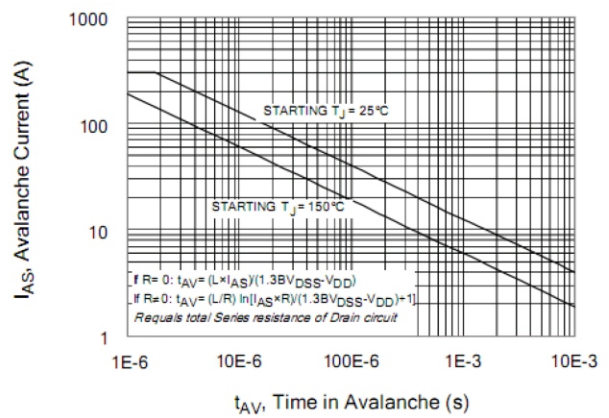
**Figure 6. Maximum Peak Current Capability**



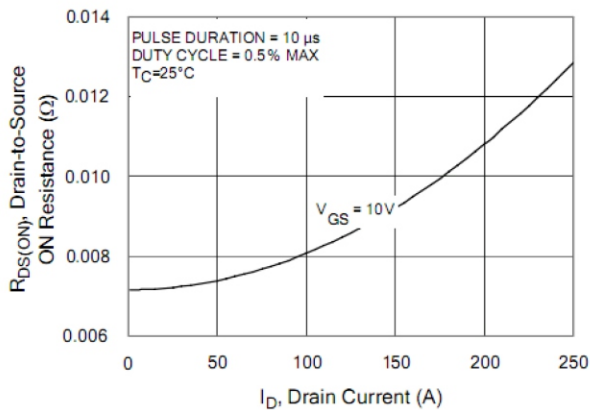
**Figure 7. Typical Transfer Characteristics**



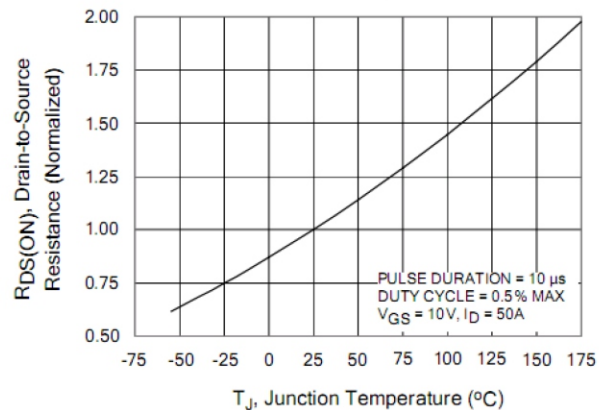
**Figure 8. Unclamped Inductive Switching Capability**



**Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current**

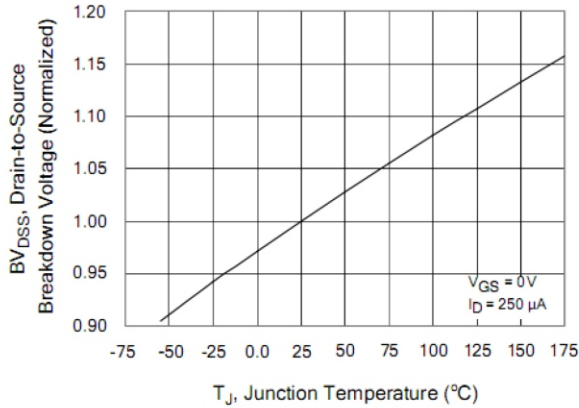


**Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature**

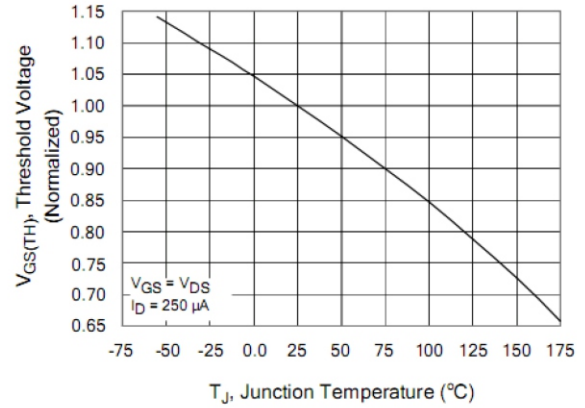


Rating and characteristic curves

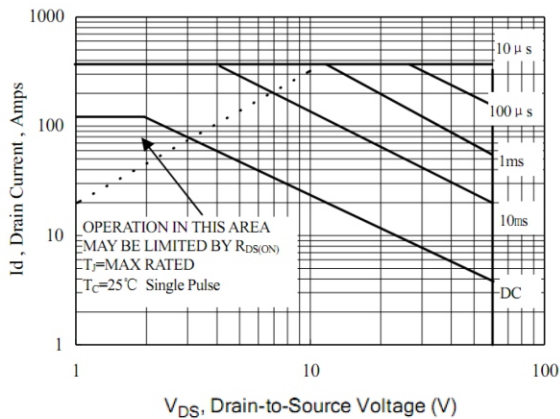
**Figure 11. Typical Breakdown Voltage vs Junction Temperature**



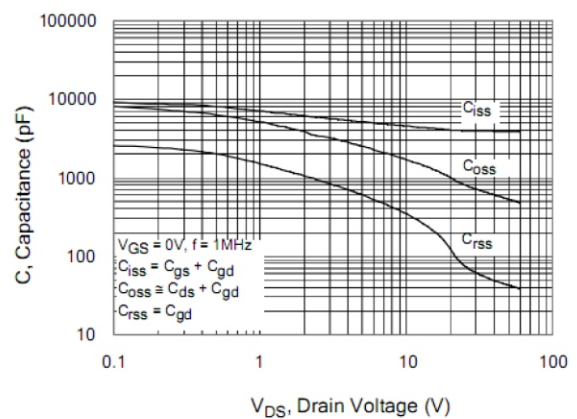
**Figure 12. Typical Threshold Voltage vs Junction Temperature**



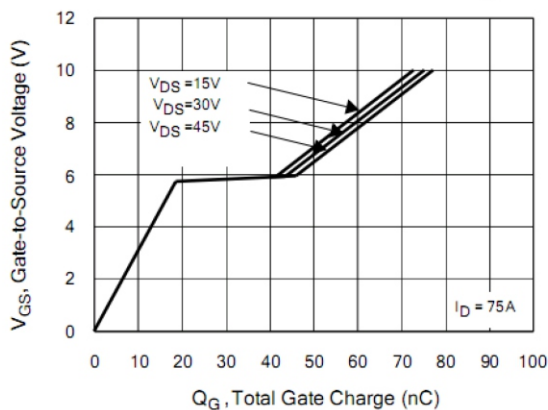
**Figure 13. Maximum Forward Bias Safe Operating Area**



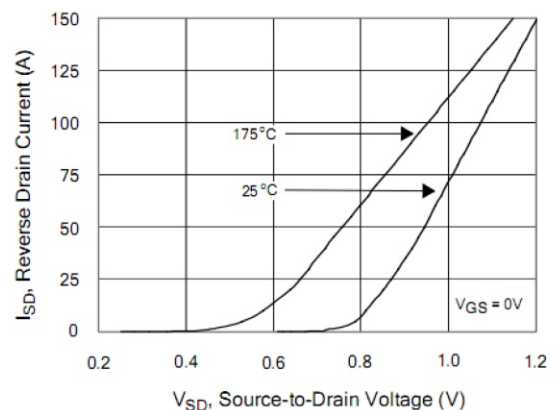
**Figure 14. Typical Capacitance vs Drain-to-Source Voltage**



**Figure 15. Typical Gate Charge vs Gate-to-Source Voltage**



**Figure 16. Typical Body Diode Transfer Characteristics**



■ Test circuit and waveform

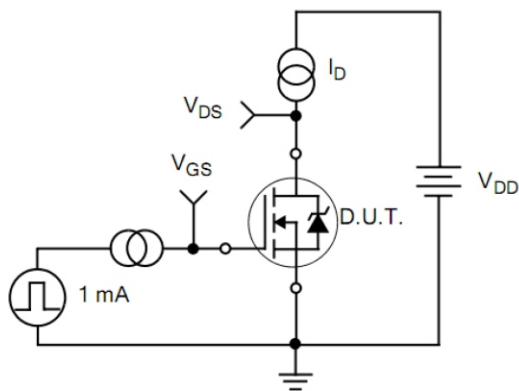


Figure 17. Gate Charge Test Circuit

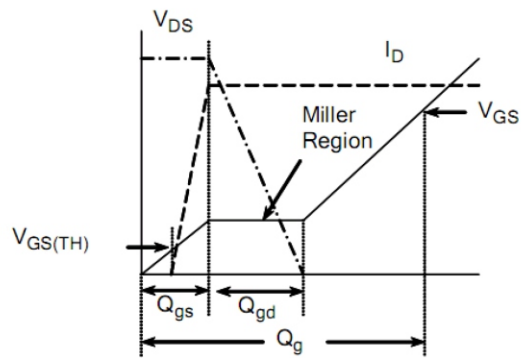


Figure 18. Gate Charge Waveform

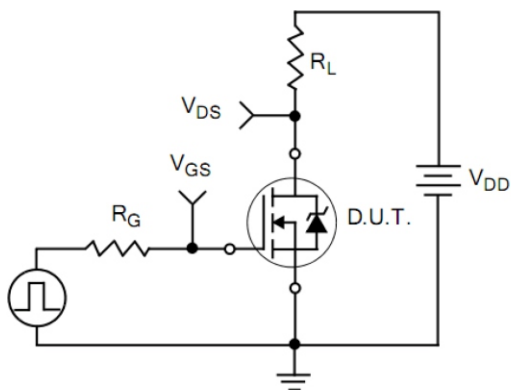


Figure 19. Resistive Switching Test Circuit

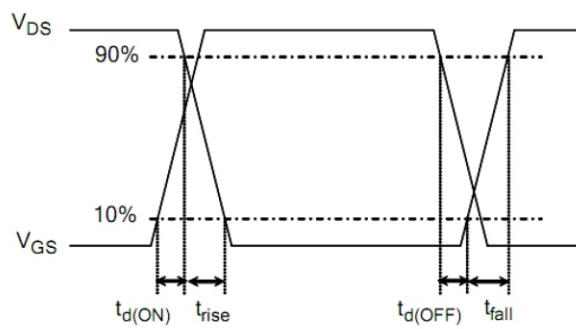


Figure 20. Resistive Switching Waveforms

■ Test circuit and waveform

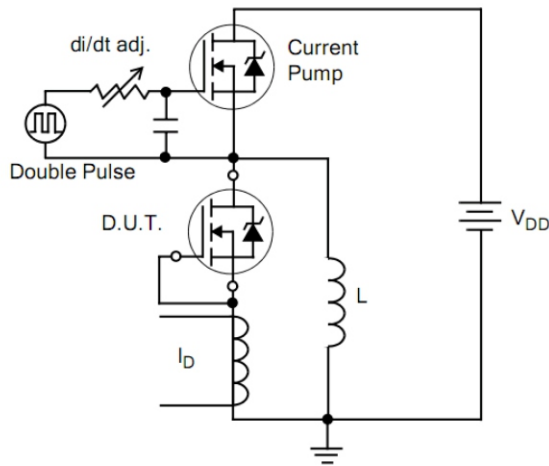


Figure 21. Diode Reverse Recovery Test Circuit

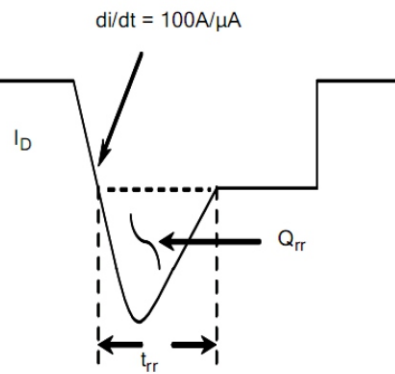


Figure 22. Diode Reverse Recovery Waveform

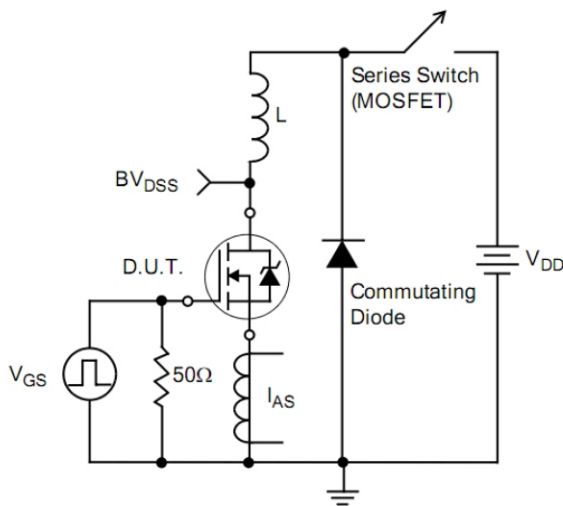


Figure 23. Unclamped Inductive Switching Test Circuit

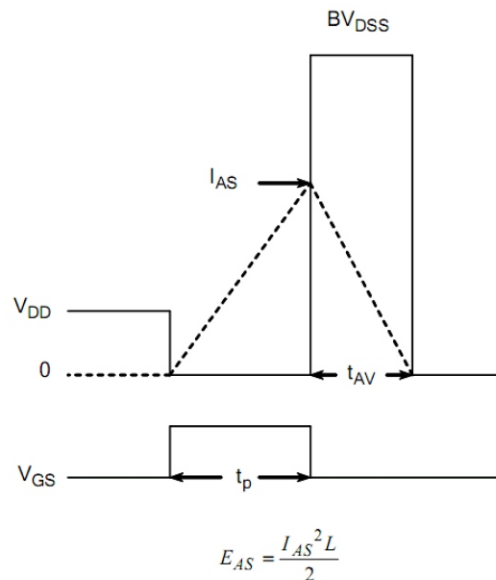


Figure 24. Unclamped Inductive Switching Waveforms

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