

Main Product Characteristics

$I_{F(AV)}$	2x15A
V_{RRM}	60V
T_J	150°C
$V_{f(max)}$	0.58V

■ Features

- Low forward voltage drop.
- Excellent high temperature stability.
- Fast switching capability.
- Halogen-free part
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

■ Mechanical data

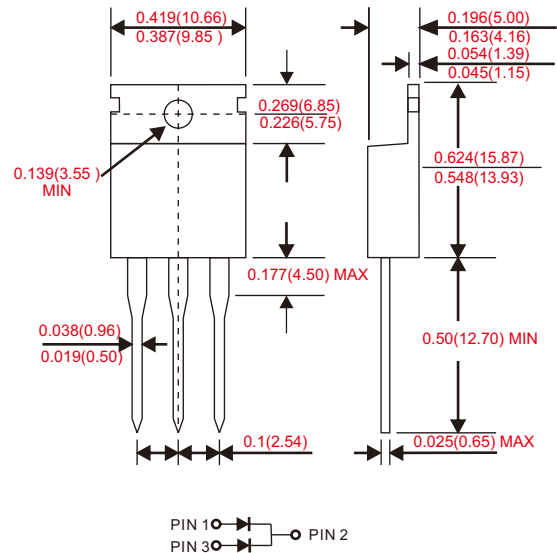
- Epoxy : UL94-V0 rated flame retardant.
- Case : JEDEC TO-220AB molded plastic body.
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026.
- Polarity: As marked.
- Mounting Position : Any.
- Weight : Approximated 2.25 gram.

■ Maximum ratings and electrical characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

■ Outline

TO-220AB



Parameter	Conditions	Symbol	CS30L60CTG-A			UNIT
			MIN.	TYP.	MAX.	
Marking code			CS30L60CT			
Peak repetitive reverse voltage		V_{RRM}	60			V
Working peak reverse voltage		V_{RWM}				
DC blocking voltage		V_{RM}				
Forward rectified current (total device)	$T_c = 110^\circ\text{C}$	I_O	30			A
Forward surge current (per diode)	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I_{FSM}	280			A
Thermal resistance(1) (per diode)	Junction to case	$R_{\theta JC}$	2			°C/W
Operating and Storage temperature		T_J, T_{STG}	-65 ~ +150			°C
Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward voltage drop (per diode)	$I_F = 15\text{A}, T_J = 25^\circ\text{C}$	V_F			600	mV
	$I_F = 15\text{A}, T_J = 125^\circ\text{C}$				550	
Reverse current (per diode)	$V_R = V_{RRM}, T_J = 25^\circ\text{C}$	I_R			0.5	mA
	$V_R = V_{RRM}, T_J = 125^\circ\text{C}$				60	

Note : 1. Thermal resistance from junction to case per leg, with heatsink size(1.35" x 0.95" x 0.18") Al-plate.

Rating and characteristic curves

Fig. 1 - Forward Power Dissipation (per diode)

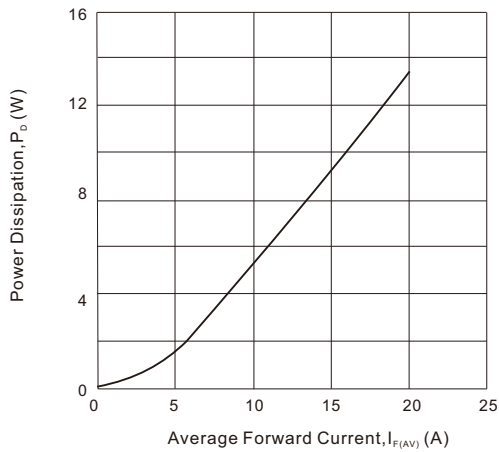


Fig. 2 - Instantaneous Forward Characteristics (per diode)

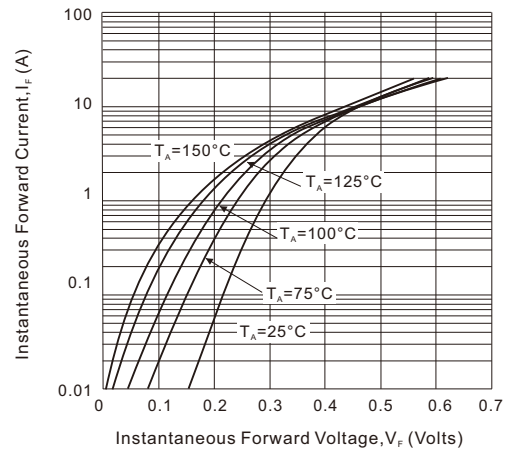


Fig. 3 - Reverse Characteristics (per diode)

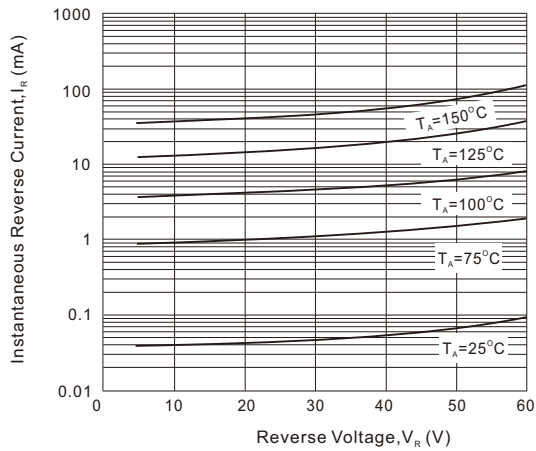


Fig. 4 - Forward Current Derating Curve (total device)

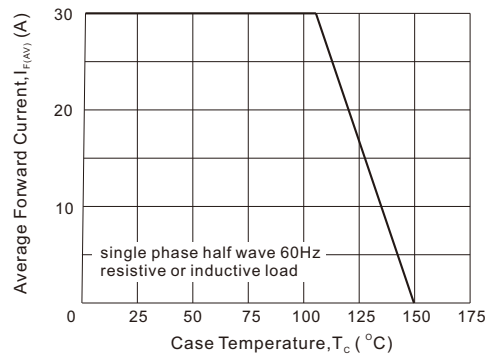
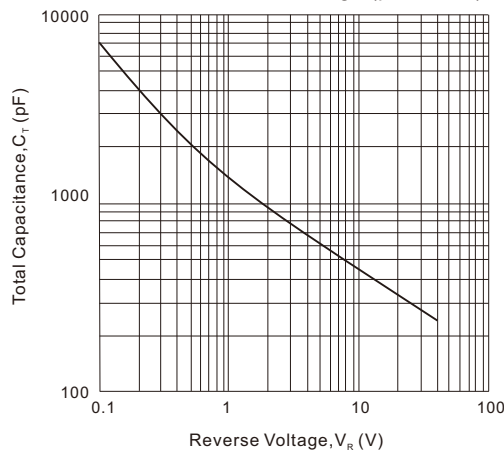


Fig. 5 - Total Capacitance VS. Reverse Voltage (per diode)



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