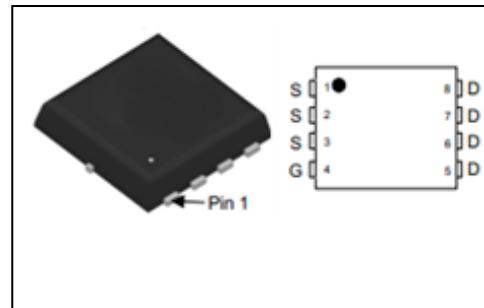




General Description:

CS60N03 AQ4-G, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trench technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. This device is suitable for use as a load switch and PWM applications. The package form is PDFN3.3*3.3, which accords with the RoHS standard.

V _{DSS}	30	V
I _D	60	A
P _D	32.8	W
R _{DS(ON)Typ}	3.6	mΩ



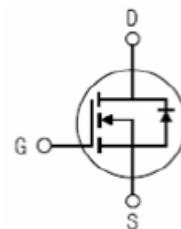
Features:

- Fast Switching
- Low ON Resistance (R_{dson}≤4.5 mΩ)
- Low Gate Charge
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test
- Halogen Free

Applications:

Power switch circuit of adaptor and charger.

Inner Equivalent Principium Chart



Absolute (T_j= 25 °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V _{DSS}	Drain-to-Source Voltage	30	V
I _D	Continuous Drain Current T _C = 25 °C	60	A
	Continuous Drain Current T _C = 100 °C	37.2	A
I _{DM} ^{a1}	Pulsed Drain Current T _C = 25 °C	240	A
V _{GS}	Gate-to-Source Voltage	±20	V
E _{AS} ^{a2}	Avalanche Energy	240	mJ
P _D	Power Dissipation T _C = 25 °C	32.8	W
	Derating Factor above 25 °C	0.26	W/°C
T _J , T _{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	°C

**Electrical Characteristics (T_j= 25°C unless otherwise specified):**

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V _{DSS}	Drain to Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	30	--	--	V
I _{DSS}	Drain to Source Leakage Current	V _{DS} =30V, V _{GS} = 0V, T _j = 25°C	--	--	1	μA
		V _{DS} =24V, V _{GS} = 0V, T _j = 125°C	--	--	100	
I _{GSS(F)}	Gate to Source Forward Leakage	V _{GS} =20V	--	--	100	nA
I _{GSS(R)}	Gate to Source Reverse Leakage	V _{GS} = -20V	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R _{DS(ON)}	Drain-to-Source On-Resistance	V _{GS} =10V, I _D =19A	--	3.6	4.5	mΩ
		V _{GS} =4.5V, I _D =19A	--	5	6.5	mΩ
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1.0	1.5	2.0	V
Pulse width tp≤300 μs, δ≤2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	--	2.2	--	Ω
C _{iss}	Input Capacitance	V _{GS} = 0V V _{DS} =15V f = 1.0MHz	--	2848	--	pF
C _{oss}	Output Capacitance		--	356	--	
C _{rss}	Reverse Transfer Capacitance		--	316	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t _{d(ON)}	Turn-on Delay Time	V _{GS} =10V, R _G =3Ω V _{DD} =15V, I _D =45A	--	13	--	ns
tr	Rise Time		--	8	--	
t _{d(OFF)}	Turn-Off Delay Time		--	56.5	--	
t _f	Fall Time		--	12	--	
Q _g	Total Gate Charge	I _D =45A V _{DD} =15V V _{GS} = 10V	--	53.5	--	nC
Q _{gs}	Gate to Source Charge		--	8.2	--	
Q _{gd}	Gate to Drain ("Miller")Charge		--	12	--	

**Source-Drain Diode Characteristics**

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I _S	Continuous Source Current (Body Diode)	T _C = 25 °C	--	--	60	A
I _{SM}	Maximum Pulsed Current (Body Diode)		--	--	240	A
V _{SD}	Diode Forward Voltage	I _S =19A, V _{GS} =0V	--	--	1.2	V
trr	Reverse Recovery Time	di/dt=100A/us IF=45A	--	12	--	ns
Qrr	Reverse Recovery Charge		--	4.2	--	nC
Pulse width t _p ≤300μs, δ ≤2%						

Symbol	Parameter	Max.	Units
R _{θJC}	Junction-to-Case	3.8	°C/W
R _{θJA}	Junction-to-Ambient	47	°C/W

^{a1}: Repetitive rating; pulse width limited by maximum junction temperature

^{a2}: L=0.5mH,I_{as}=31A Start T_j=25°C

^{a3}: Recommend soldering temperature defined by IPC/JEDEC J-STD 020

Characteristics Curve:

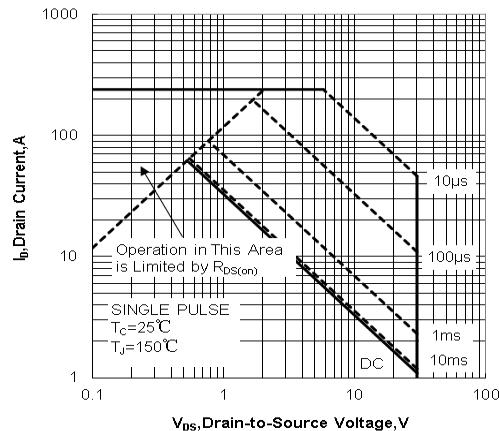


Figure 1. Maximum Safe Operating

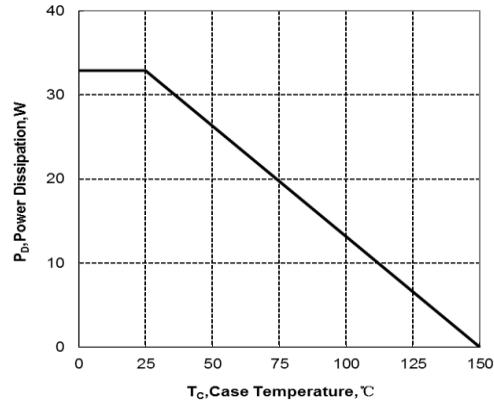


Figure 2. Maximum Power Dissipation vs Case Temperature

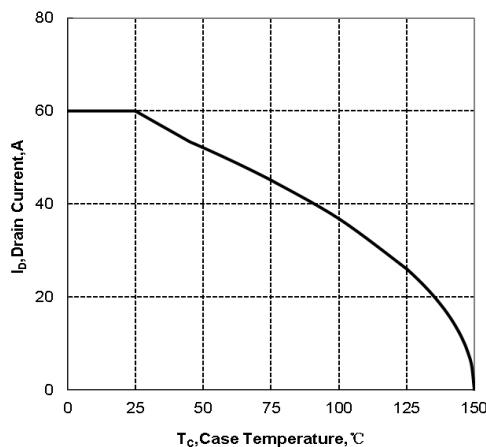


Figure3. Maximum Continuous Drain Current vs Case Temperature

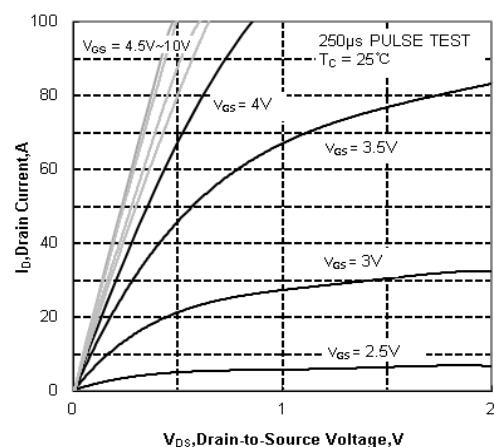


Figure4. Typical Output Characteristics

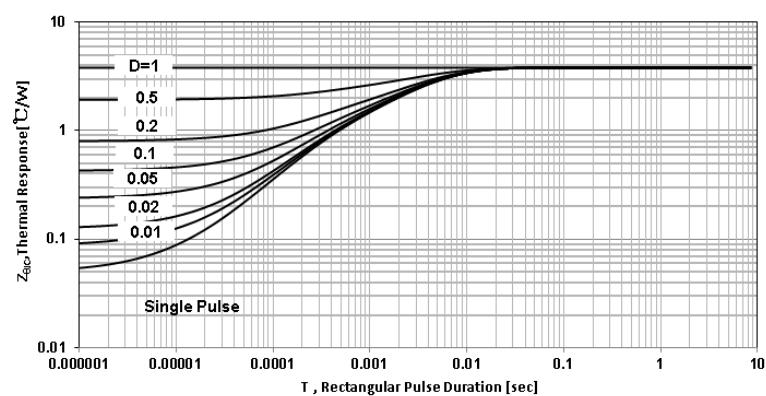


Figure 5. Maximum Effective Transient Thermal Impedance, Junction-to-Case

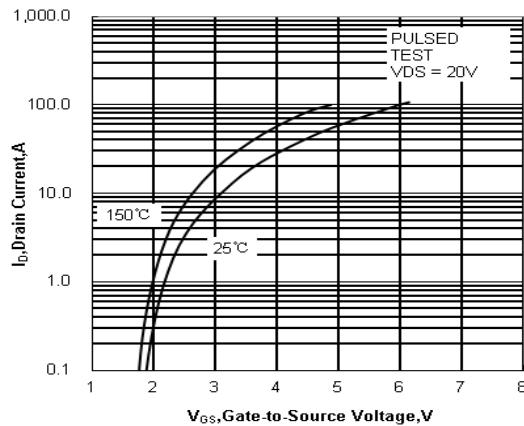


Figure 6. Typical Transfer Characteristics

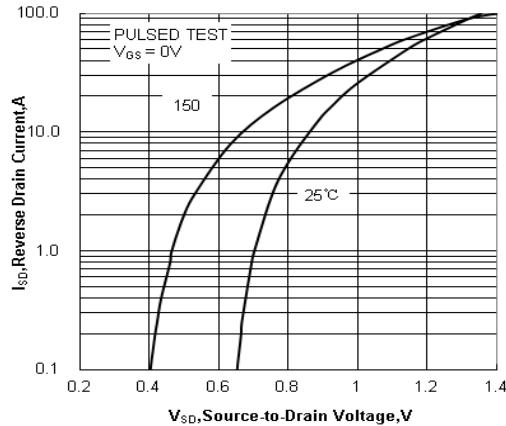


Figure 7. Typical Body Diode Transfer Characteristics

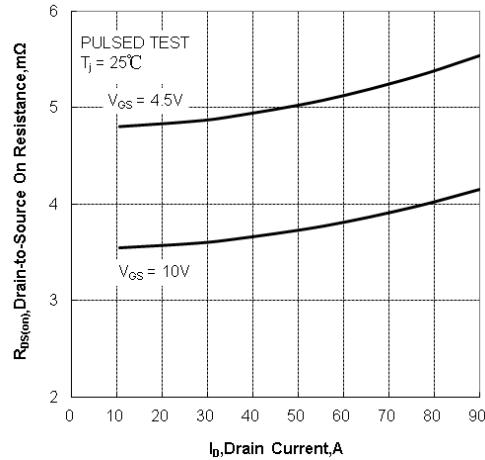


Figure 8. Drain-to-Source On Resistance vs Drain Current

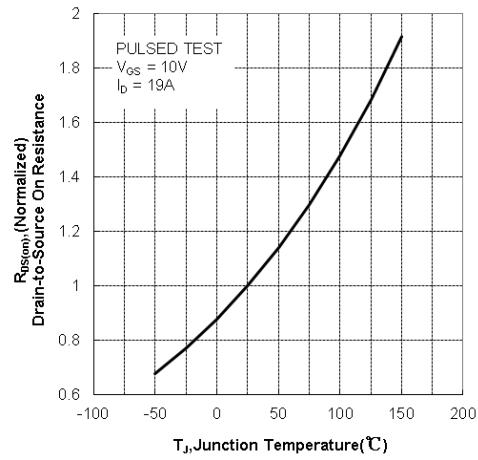


Figure 9. Typical Drian to Source on Resistance vs Junction Temperature

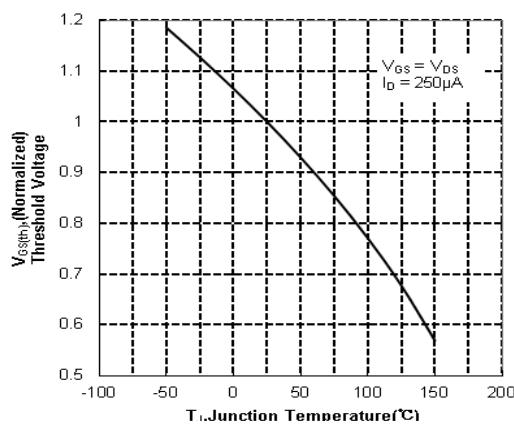


Figure 10. Typical Theshold Voltage vs Junction Temperature

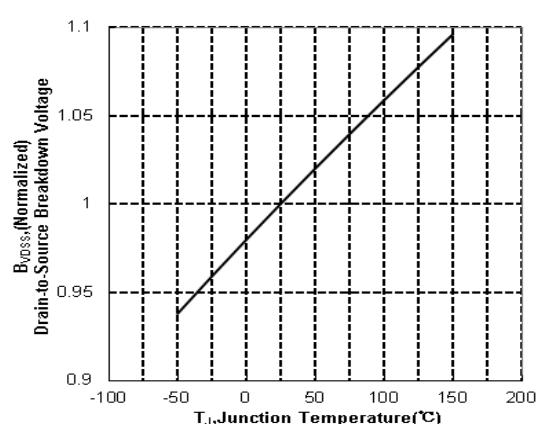


Figure 11. Typical Breakdown Voltage vs Junction Temperature

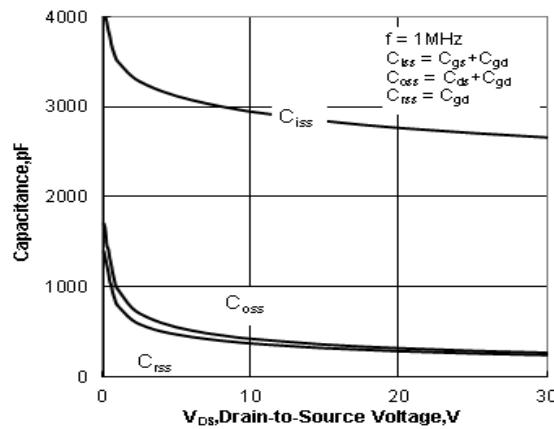


Figure 12. Typical Capacitance vs Drain to Source Voltage

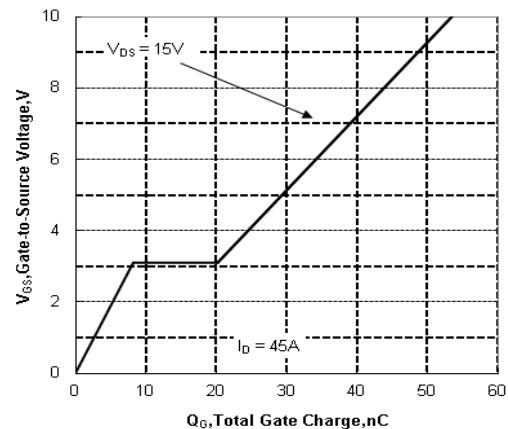


Figure 13. Typical Gate Charge vs Gate to Source Voltage

Test Circuit and Waveform

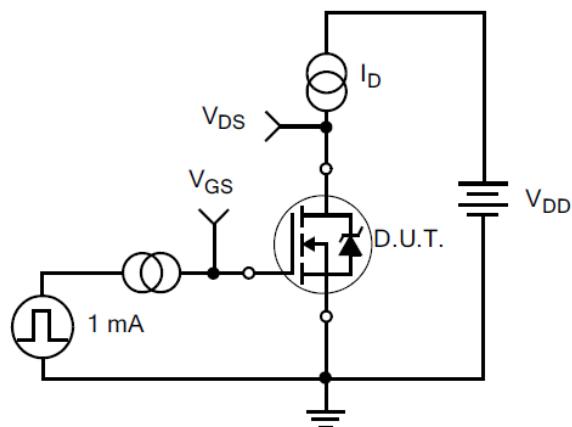


Figure 14. Gate Charge Test Circuit



Figure 15. Gate Charge Waveforms



Figure 16. Resistive Switching Test Circuit

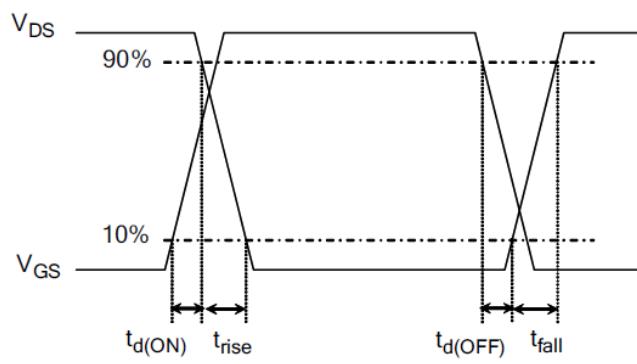


Figure 17. Resistive Switching Waveforms

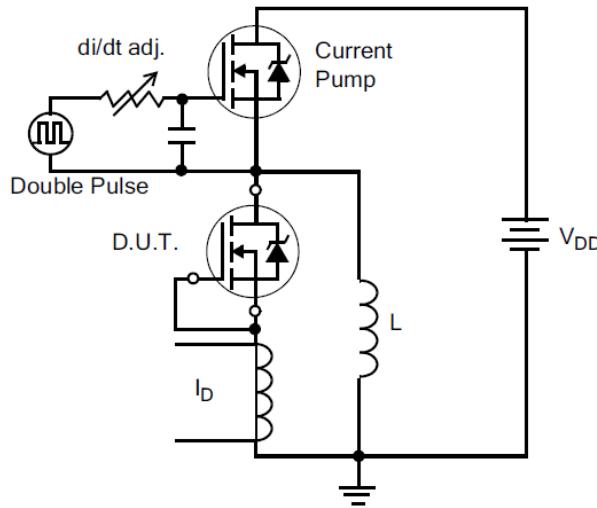


Figure 18. Diode Reverse Recovery Test Circuit

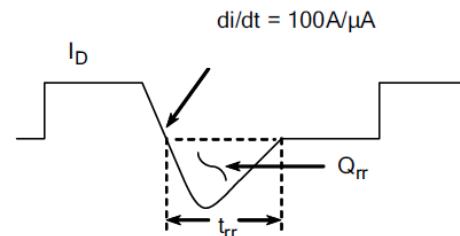


Figure 19. Diode Reverse Recovery Waveform

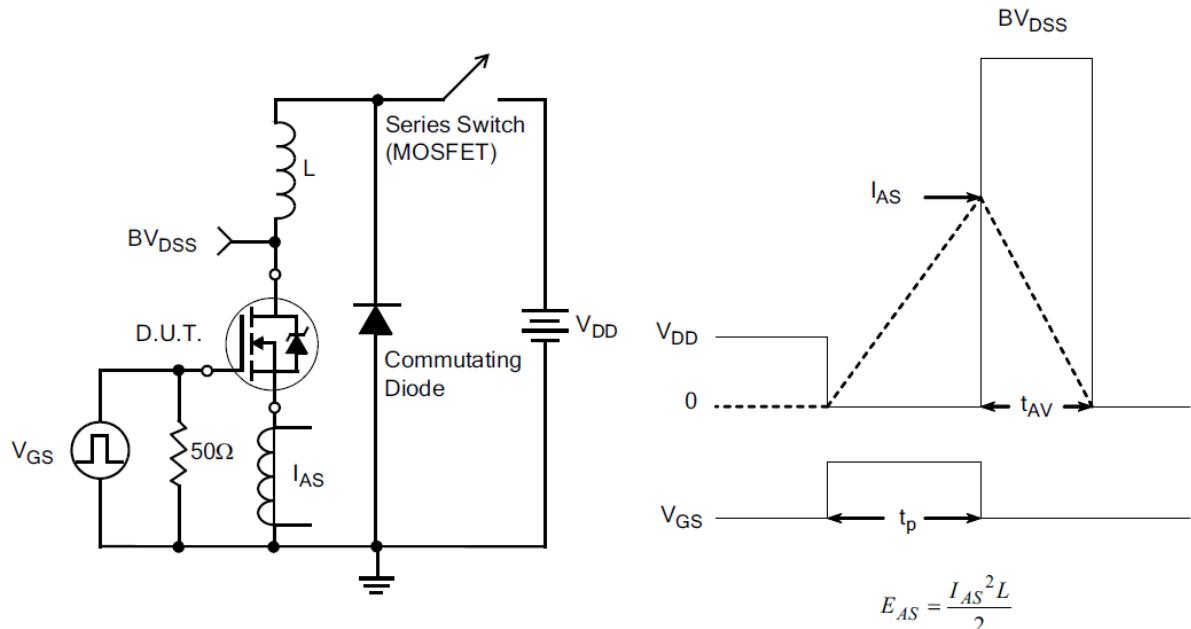
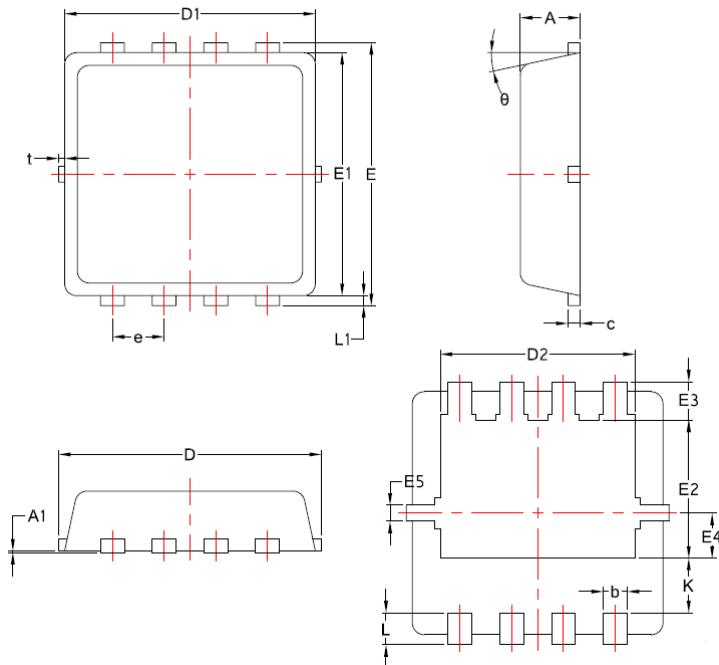


Figure 20. Unclamped Inductive Switching Test Circuit

Figure 21. Unclamped Inductive Switching Waveform

Package Information:



Items	Values(mm)	
	MIN	MAX
A	0.7	0.9
A1	/	0.05
b	0.2	0.4
c	0.1	0.25
D	3.05	3.55
D1	2.90	3.35
D2	2.15	2.65
E	3.05	3.55
E1	2.80	3.30
E2	1.54	1.94
E3	0.18	0.65
e	0.55	0.75
L	0.2	0.6
L1	0.05	0.25
t	/	0.15
θ	8 °	14 °

PDFN3.3×3.3Package

**The name and content of poisonous and harmful material in products**

Part's Name	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤0.1%	≤0.1%	≤0.01 %	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Electric glue	○	○	○	○	○	○	○	○	○	○
Note	○: Means the hazardous material is under the criterion of 2011/65/EU. ✕: Means the hazardous material exceeds the criterion of 2011/65/EU. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.									

Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

WUXI CHINA RESOURCES HUAJING MICROELECTRONICS CO., LTD.

Add: No.14 Liangxi RD. Wuxi, Jiangsu, China Mail:214061 <http://www.crhj.com.cn>
Tel: +86 0510-85807228 Fax: +86- 0510-85800864

Marketing Part: Post: 214061 Tel: +86 0510-81805277/81805336
Fax: +86 0510-85800360/85803016

Application and Service: Post: 214061 Tel / Fax: +86- 0510-81805243/81805110