



## CS82N25 AKR-G

**General Description:**

CS82N25 AKR-G, the silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-247, which accords with the RoHS standard.

**Features:**

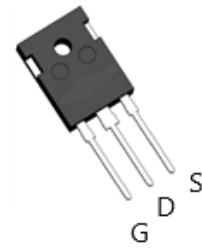
- **Fast Switching**
- **Low ON Resistance**( $R_{dson} \leq 35m\Omega$ )
- **Low Gate Charge** (Typical Data: 148.5nC)
- **Low Reverse transfer capacitances**(Typical: 73.9pF)
- **100% Single Pulse avalanche energy Test**
- **Halogen Free**

**Applications:**

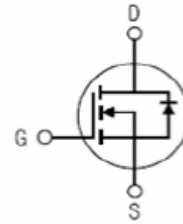
Power switch circuit of electron ballast and adaptor.

$V_{DSS}$	250	V
$I_D$	82	A
$P_D$ ( $T_C=25^\circ C$ )	250	W
$R_{DS(ON)Typ}$	25	m $\Omega$

TO-247



Inner Equivalent Principium Chart

**Absolute** ( $T_c = 25^\circ C$  unless otherwise specified):

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	250	V
$I_D$	Continuous Drain Current	82	A
	Continuous Drain Current $T_C = 100^\circ C$	48	A
$I_{DM}^{a1}$	Pulsed Drain Current	328	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 30$	V
$E_{AS}^{a2}$	Single Pulse Avalanche Energy	3600	mJ
$dv/dt^{a3}$	Peak Diode Recovery $dv/dt$	5.0	V/ns
$P_D$	Power Dissipation	250	W
	Derating Factor above $25^\circ C$	2	W/ $^\circ C$
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	150, $-55$ to $150$	$^\circ C$


**Electrical Characteristics** (Tc= 25°C unless otherwise specified):

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250 μA	250	--	--	V
Δ BV <sub>DSS</sub> / Δ T <sub>J</sub>	Bvdss Temperature Coefficient	I <sub>D</sub> =250uA, Reference 25°C	--	0.32	--	V/°C
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> = 250V, V <sub>GS</sub> = 0V, T <sub>a</sub> = 25°C	--	--	1	μA
		V <sub>DS</sub> = 200V, V <sub>GS</sub> = 0V, T <sub>a</sub> = 125°C			100	
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 30V	--	--	100	nA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>DS</sub> = 0V, V <sub>GS</sub> = -30V	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =41A	--	25	35	mΩ
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	3.0	--	5.0	V
Pulse width tp ≤ 300 μs, δ ≤ 2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g <sub>fs</sub>	Forward Trans conductance	V <sub>DS</sub> =15V, I <sub>D</sub> =41A		61	--	S
R <sub>g</sub>	Gate resistance	f = 1.0MHz		3.8		Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 25V f = 1.0MHz	--	7652		pF
C <sub>oss</sub>	Output Capacitance		--	1119		
C <sub>rss</sub>	Reverse Transfer Capacitance		--	73.9		

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	I <sub>D</sub> = 82A V <sub>DD</sub> = 125V R <sub>G</sub> = 25Ω	--	78		ns
t <sub>r</sub>	Rise Time		--	430		
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	180		
t <sub>f</sub>	Fall Time		--	178		
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> = 82A V <sub>DD</sub> = 200V V <sub>GS</sub> = 10V	--	148.5		nC
Q <sub>gs</sub>	Gate to Source Charge		--	46.5		
Q <sub>gd</sub>	Gate to Drain ("Miller") Charge		--	62.25		

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$I_S$	Continuous Source Current (Body Diode)		--	--	82	A
$I_{SM}$	Maximum Pulsed Current (Body Diode)		--	--	328	A
$V_{SD}$	Diode Forward Voltage	$I_S=82A, V_{GS}=0V$	--	--	1.5	V
$t_{rr}$	Reverse Recovery Time	$I_S=82A, T_j = 25^\circ C$ $dI_F/dt=100A/us,$ $V_{GS}=0V$	--	290	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	3640	--	nC
$I_{RRM}$	Reverse Recovery Current		--	25.5	--	A
Pulse width $t_p \leq 300 \mu s, \delta \leq 2\%$						

Symbol	Parameter	Max.	Units
$R_{\theta JC}$	Junction-to-Case	0.5	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient	40	$^\circ C/W$

<sup>a1</sup>: Repetitive rating; pulse width limited by maximum junction temperature

<sup>a2</sup>:  $L=10.0mH, I_D=26.8A, Start T_j=25^\circ C$

<sup>a3</sup>:  $I_{SD}=82A, di/dt \leq 300A/us, V_{DD} \leq BV_{DS}, Start T_j=25^\circ C$

## Characteristics Curve:

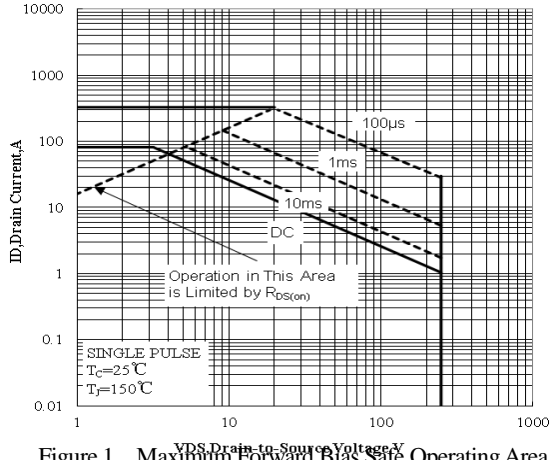


Figure 1 Maximum Forward Bias Safe Operating Area

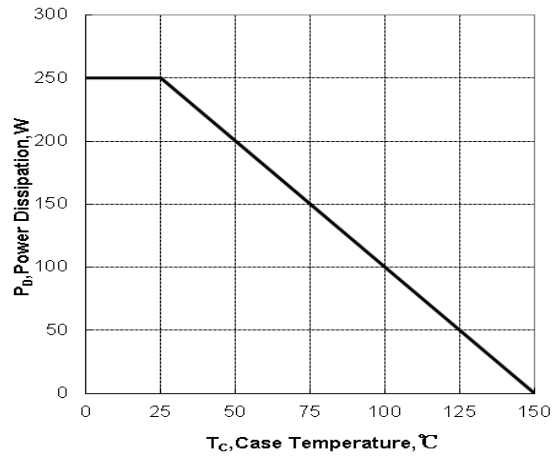


Figure 2 Maximum Power dissipation vs Case Temperature

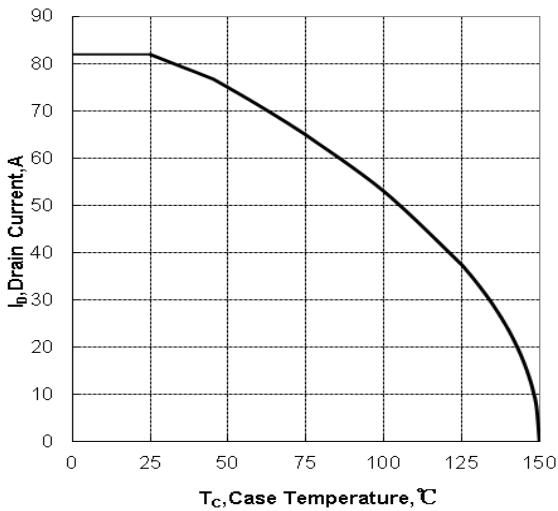


Figure 3 Maximum Continuous Drain Current vs Case Temperature

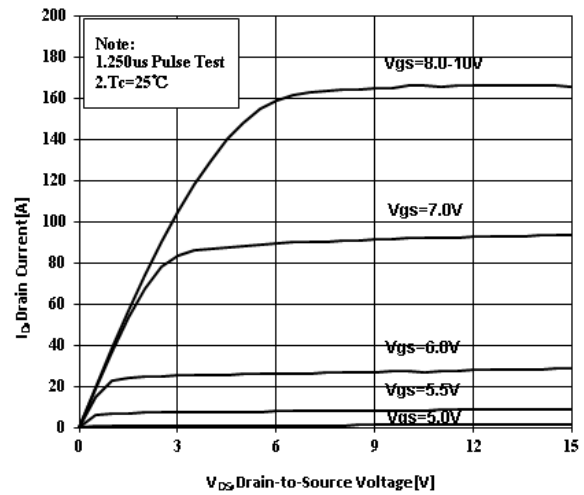


Figure 4 Typical Output Characteristics

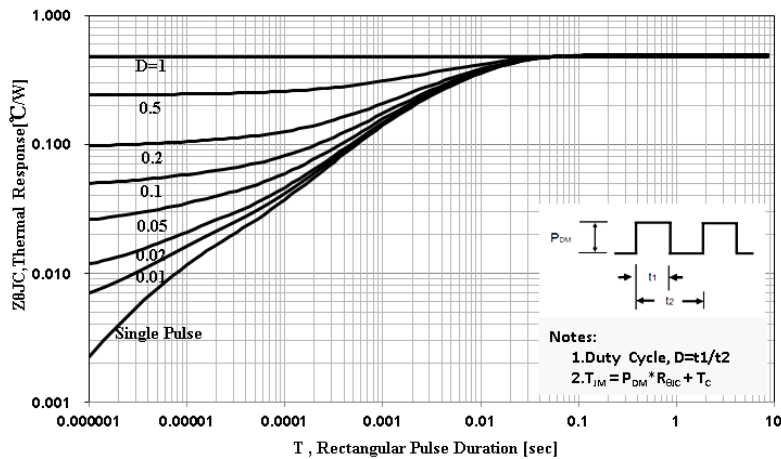


Figure 5 Maximum Effective Thermal Impedance , Junction to Case

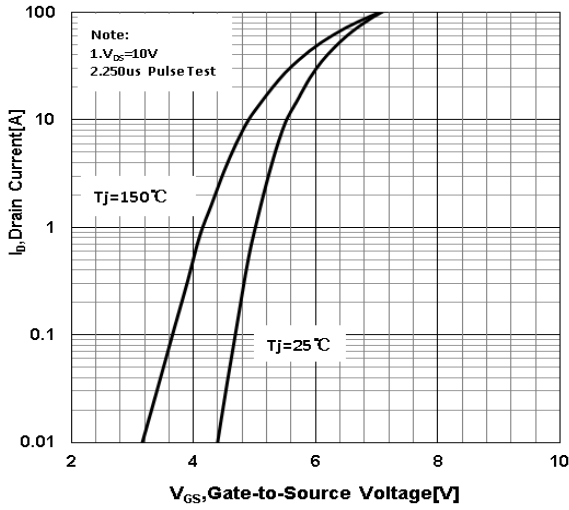


Figure 6 Typical Transfer Characteristics

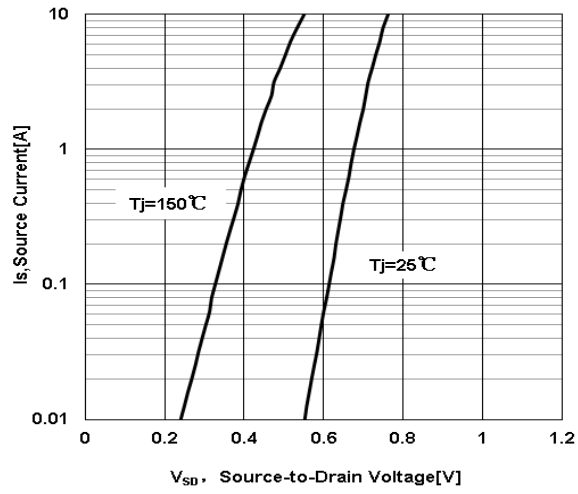


Figure 7 Typical Body Diode Transfer Characteristics

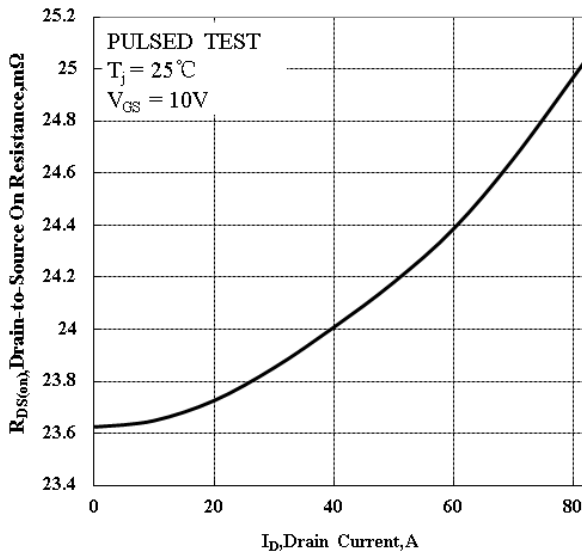


Figure 8 Typical Drain to Source ON Resistance vs Drain Current

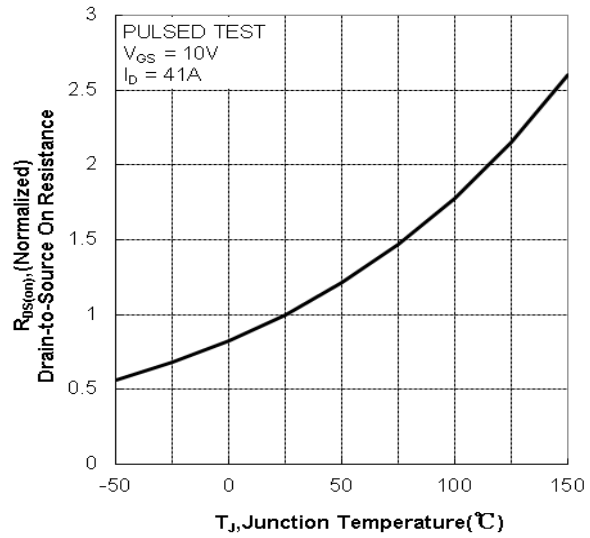


Figure 9 Typical Drain to Source on Resistance vs Junction Temperature

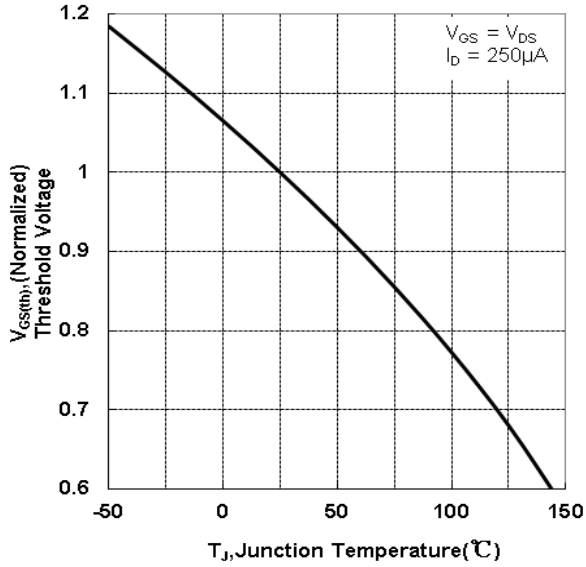


Figure 10 Typical Theshold Voltage vs Junction Temperature

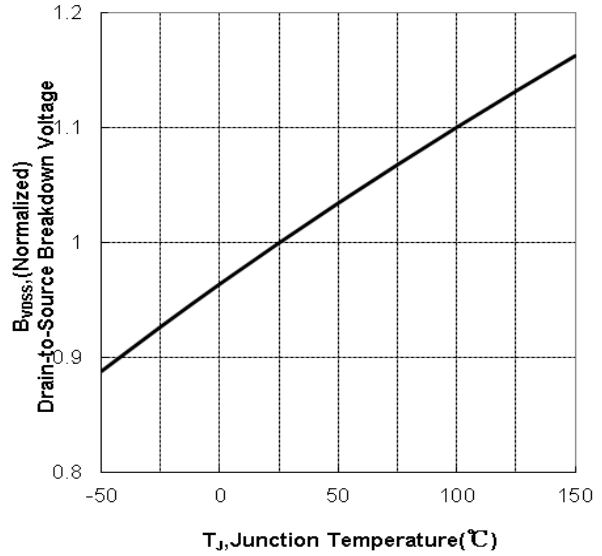


Figure 11 Typical Breakdown Voltage vs Junction Temperature

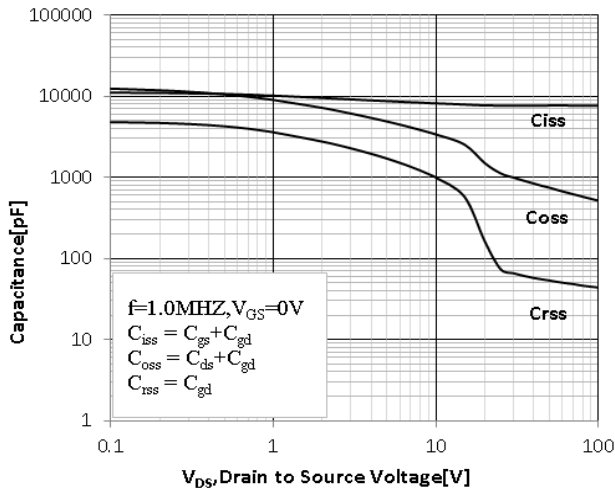


Figure 12 Typical Capacitance vs Drain to Source Voltage

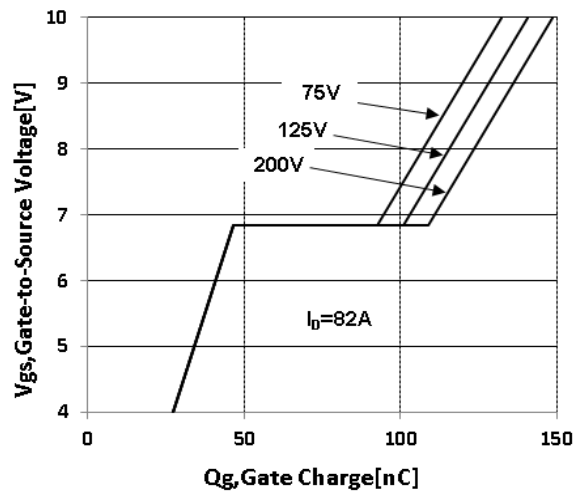


Figure 13 Typical Gate Charge vs Gate to Source Voltage

Test Circuit and Waveform



Figure 14. Gate Charge Test Circuit

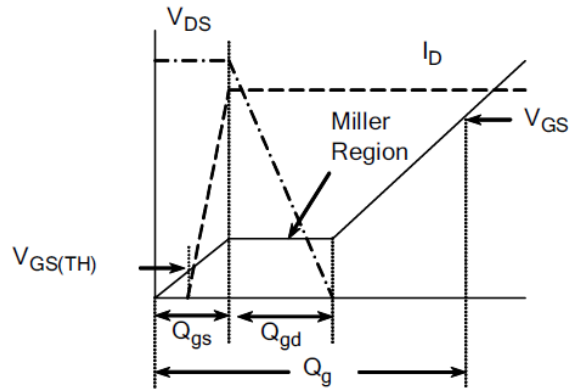


Figure 15. Gate Charge Waveforms



Figure 16. Resistive Switching Test Circuit

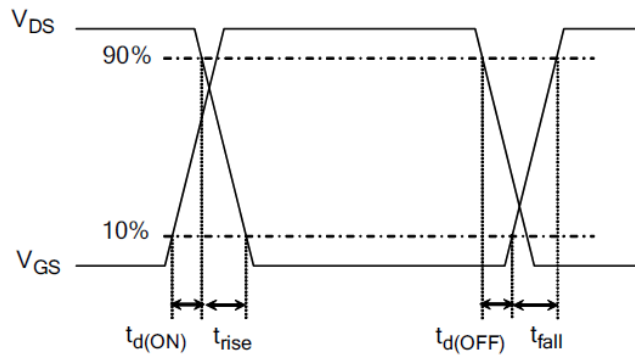


Figure 17. Resistive Switching Waveforms

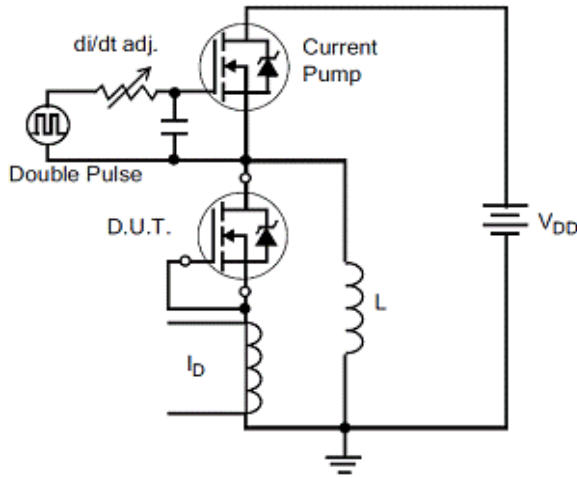


Figure 18. Diode Reverse Recovery Test Circuit

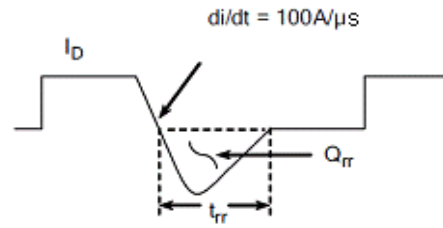


Figure 19. Diode Reverse Recovery Waveform

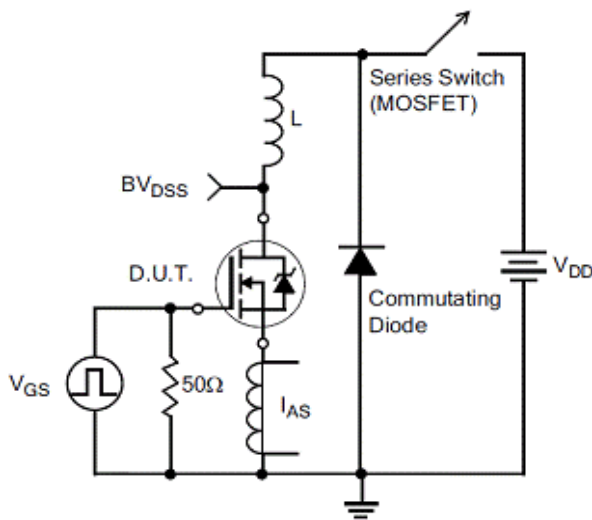


Figure 20. Unclamped Inductive Switching Test Circuit

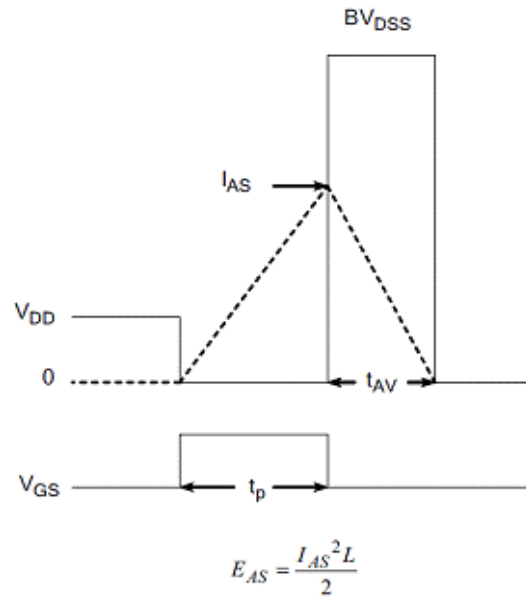
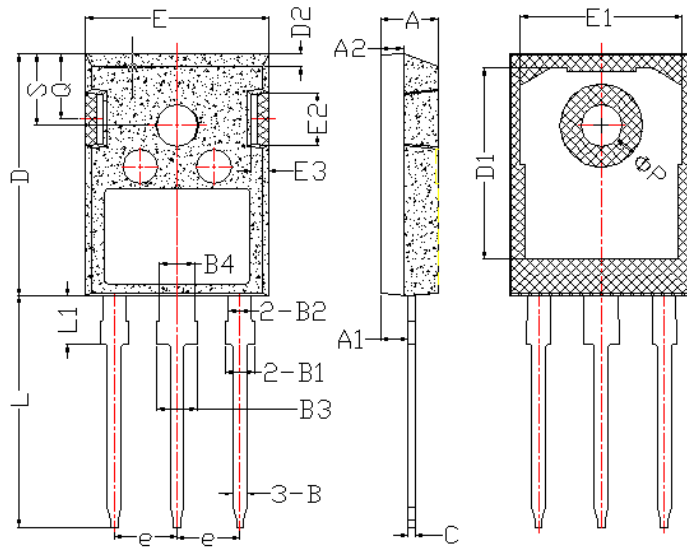


Figure 21. Unclamped Inductive Switching Waveform



## Package Information



项 目	规范(mm)	
	MIN	MAX
A	4.6	5.2
A1	2.2	2.6
B	0.9	1.4
B1	1.75	2.35
B2	1.75	2.15
B3	2.8	3.35
B4	2.8	3.15
C	0.5	0.7
D	20.60	21.30
D1	16	18
E	15.5	16.10
E1	13	14.7
E2	3.80	5.3
E3	0.8	2.60
e	5.2	5.7
L	19	20.5
L1	3.9	4.6
ΦP	3.3	3.70
Q	5.2	6.00
S	5.8	6.6

TO-247 Package



**The name and content of poisonous and harmful material in products**

Part's Name	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤ 0.1%	≤ 0.1%	≤ 0.01%	≤0.1%	≤ 0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Solder	×	○	○	○	○	○	○	○	○	○
Note	○: Means the hazardous material is under the criterion of 2011/65/EU. ×: Means the hazardous material exceeds the criterion of 2011/65/EU. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.									

**Warnings**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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