

## N-Channel Trench Power MOSFET

### General Description

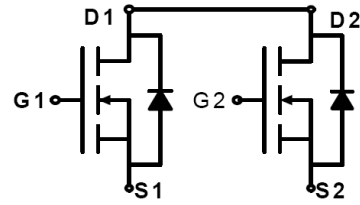
The CS8810 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching applications.

### Features

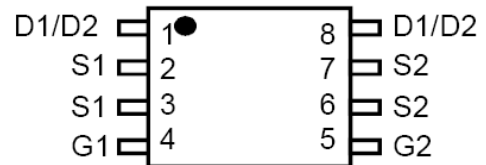
- $V_{DS} = 20V, I_D = 7A$   
 $R_{DS(ON)} < 28m\Omega @ V_{GS} = 4.5V$   
 $R_{DS(ON)} < 35m\Omega @ V_{GS} = 2.5V$
- High Power and current handling capability
- Lead free product is acquired
- Surface Mount Package

### Application

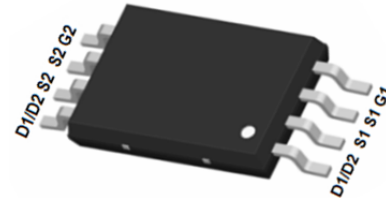
- Battery protection
- Load switch
- Power management



Schematic Diagram



Marking and pin Assignment



TSSOP-8 top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
CS8810	CS8810	TSSOP-8	Ø330mm	12mm	3000 units

Table 1. Absolute Maximum Ratings ( $T_A=25^\circ C$ )

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-Source Voltage ( $V_{GS}=0V$ )	20	V
$V_{GS}$	Gate-Source Voltage ( $V_{DS}=0V$ )	$\pm 10$	V
$I_D$	Drain Current-Continuous ( $T_A=25^\circ C$ )	7	A
	Drain Current-Continuous ( $T_A=70^\circ C$ )	5.7	A
$I_{DM (pluse)}$	Drain Current-Continuous@ Current-Pulsed (Note 1)	25	A
$P_D$	Maximum Power Dissipation ( $T_A=25^\circ C$ )	1.5	W
	Maximum Power Dissipation ( $T_A=70^\circ C$ )	1	W
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ C$

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

**Table 2. Thermal Characteristic**

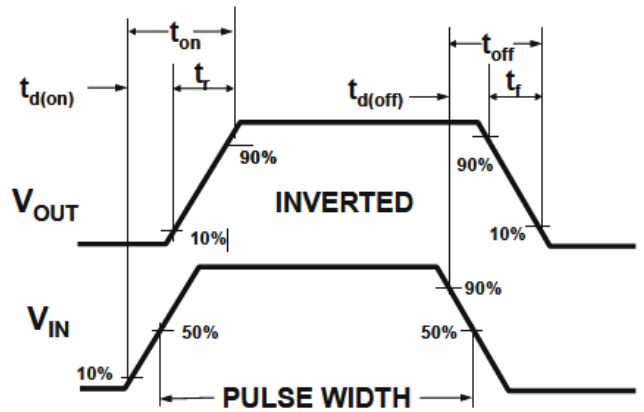
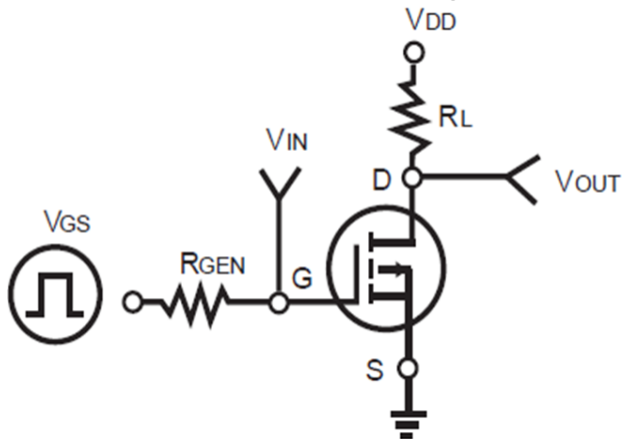
Symbol	Parameter	Value	Unit
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	83	°C/W

**Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	20	21.5		V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =19.5V, V <sub>GS</sub> =0V			1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5	0.65	1.1	V
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =6A	4	11		S
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A		20	28	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A		26	35	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =8V, V <sub>GS</sub> =0V, f=1.0MHz		605		pF
C <sub>oss</sub>	Output Capacitance			315		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			132		pF
<b>Switching Times</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =10V, I <sub>D</sub> =1A, V <sub>GS</sub> =4.5V, R <sub>G</sub> =6Ω		11		nS
t <sub>r</sub>	Turn-on Rise Time			12		nS
t <sub>d(off)</sub>	Turn-Off Delay Time			36		nS
t <sub>f</sub>	Turn-Off Fall Time			32		nS
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =10V, I <sub>D</sub> =6A, V <sub>GS</sub> =4.5V		10		nC
Q <sub>gs</sub>	Gate-Source Charge			2.8		nC
Q <sub>gd</sub>	Gate-Drain Charge			1.8		nC
<b>Source-Drain Diode Characteristics</b>						
I <sub>SD</sub>	Source-Drain Current(Body Diode)				1.7	A
V <sub>SD</sub>	Forward on Voltage <sup>(Note 1)</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1.7A		0.79	1.2	V

Notes 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

### Switch Time Test Circuit and Switching Waveforms:



### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)

Figure1. Power Dissipation

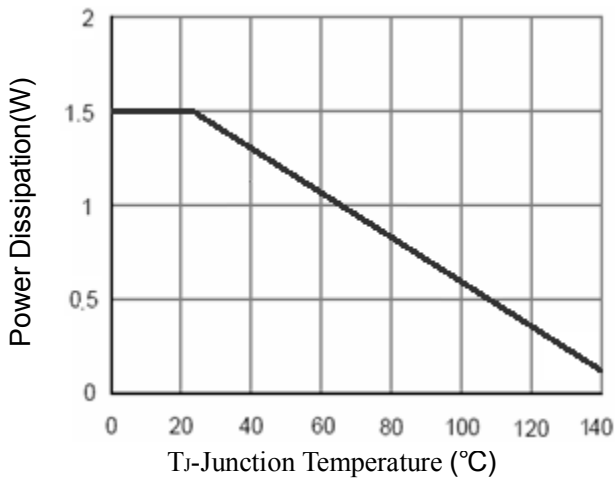


Figure2. Drain Current

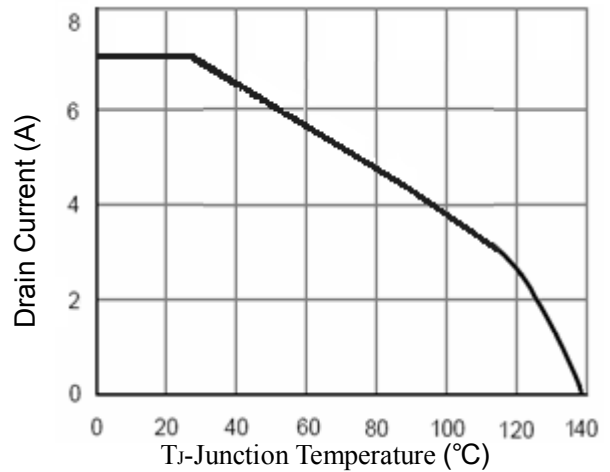


Figure3. Output Characteristics

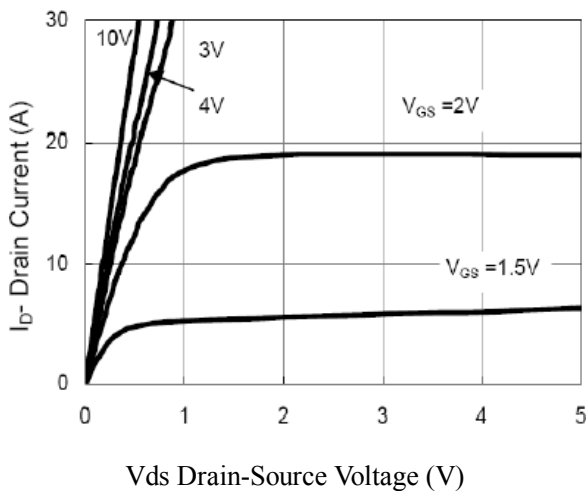
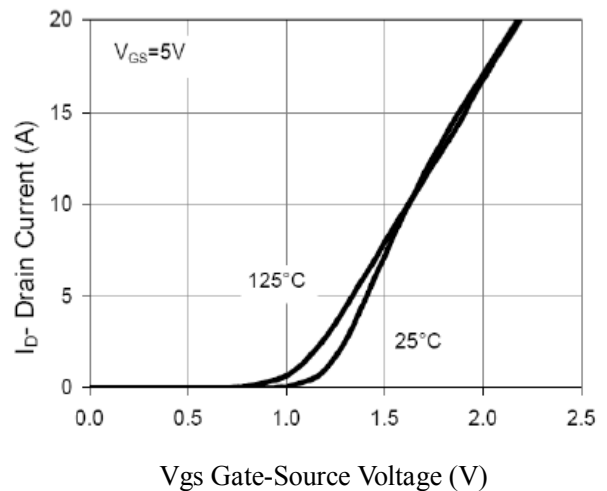
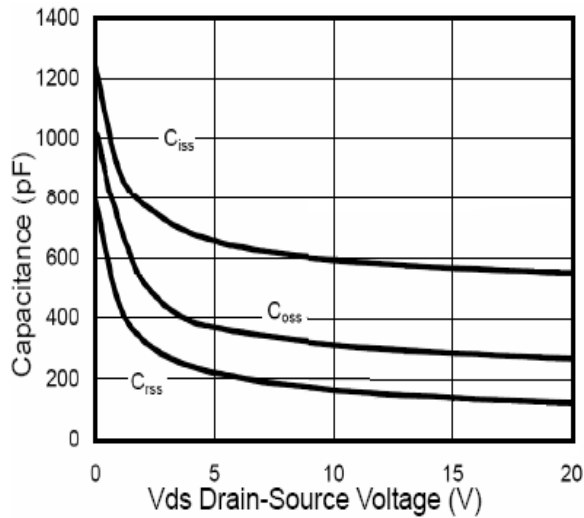


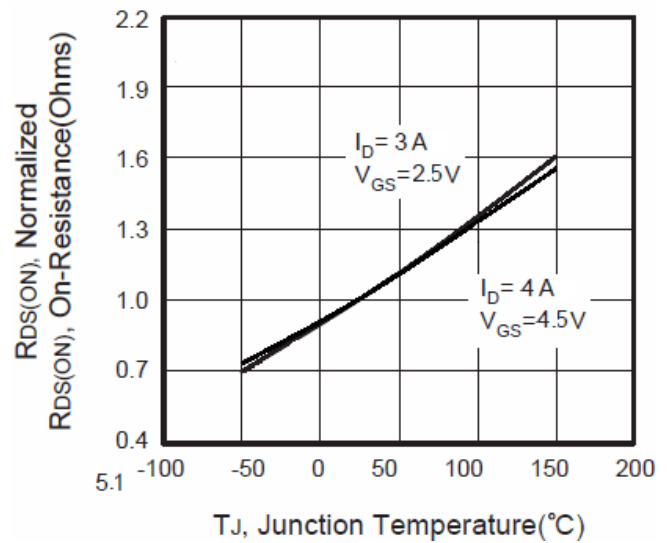
Figure4. Transfer Characteristics



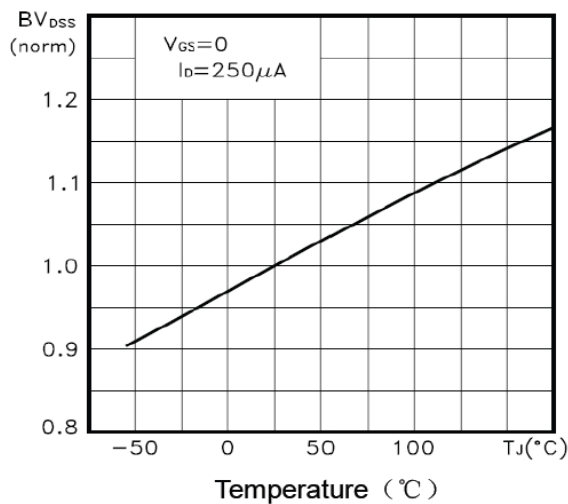
**Figure5. Capacitance**



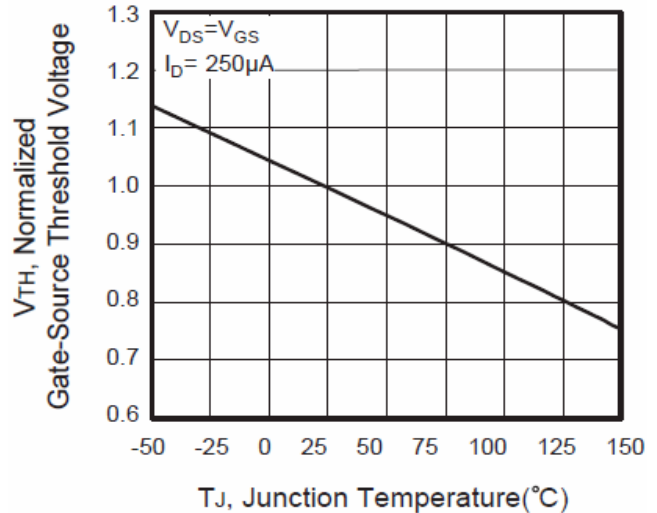
**Figure6.  $R_{DS(ON)}$  vs Junction Temperature**



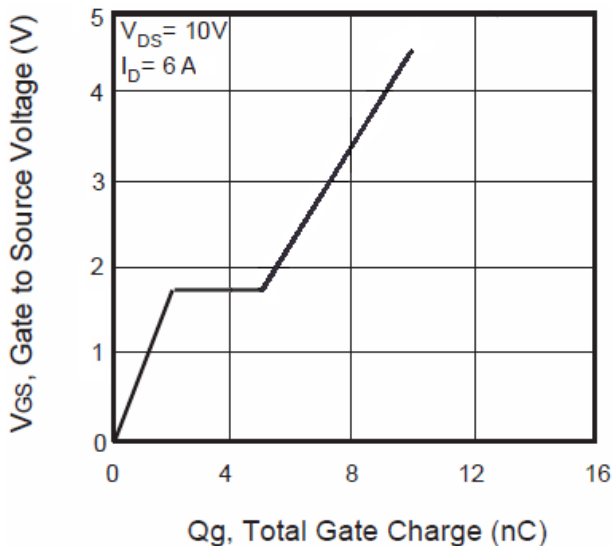
**Figure7. Max  $BV_{DSS}$  vs Junction Temperature**



**Figure8.  $V_{GS(th)}$  vs Junction Temperature**



**Figure9. Gate Charge Waveforms**



**Figure10. Maximum Safe Operating Area**

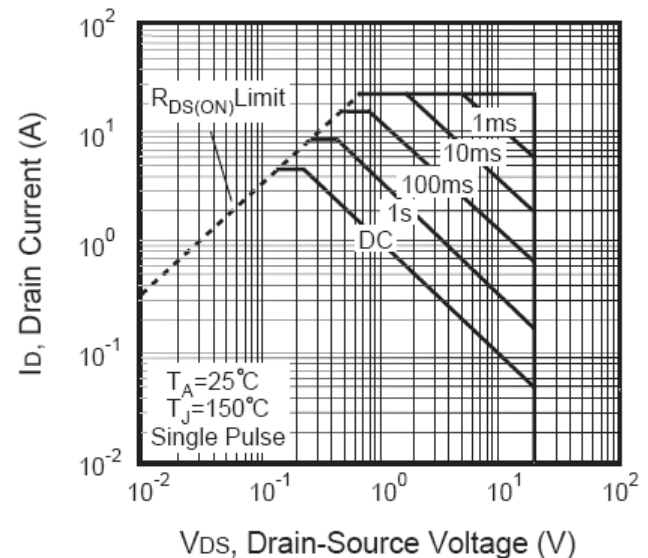
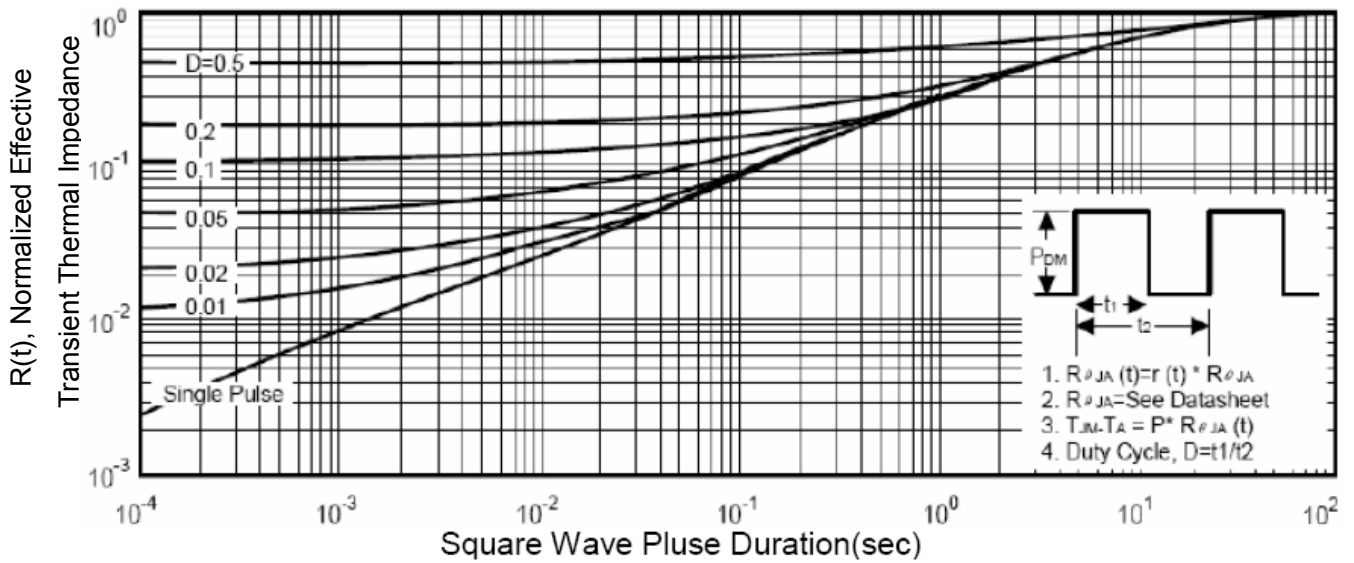
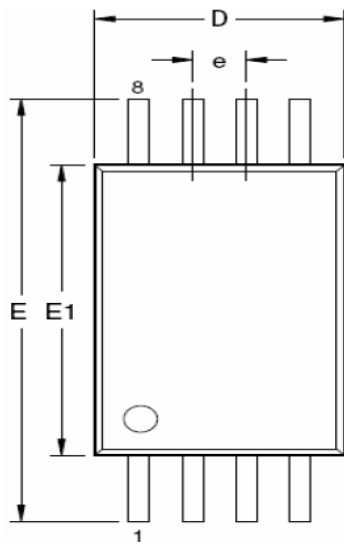


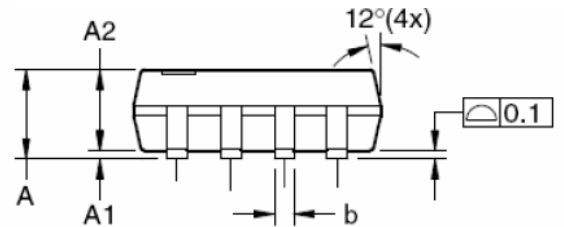
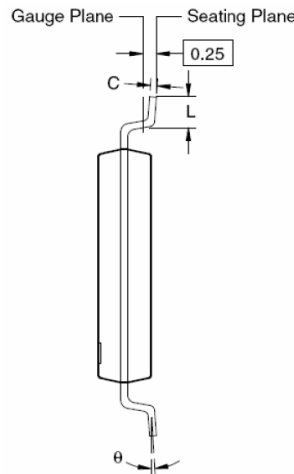
Figure11. Normalized Maximum Transient Thermal Impedance



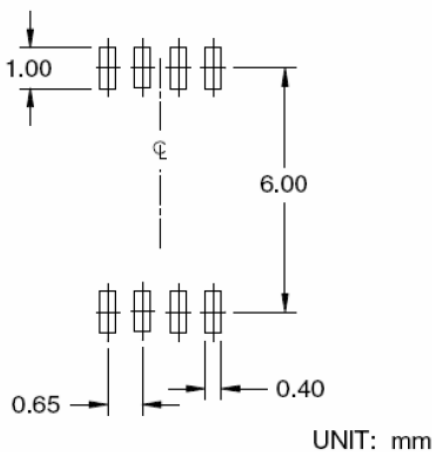
## TSSOP-8 Package Information



Dimensions in Millimeters (UNIT:mm)



### RECOMMENDED LAND PATTERN



### Dimensions in millimeters

Symbols	Min.	Nom.	Max.
A	—	—	1.20
A1	0.05	—	0.15
A2	0.80	1.00	1.05
b	0.19	—	0.30
C	0.09	—	0.20
D	2.90	3.00	3.10
E	6.40 BSC		
E1	4.30	4.40	4.50
e	0.65 BSC		
L	0.45	0.60	0.75
θ	0°	—	8°

### Dimensions in inches

Symbols	Min.	Nom.	Max.
A	—	—	0.047
A1	0.002	—	0.006
A2	0.031	0.039	0.041
b	0.007	—	0.012
C	0.004	—	0.008
D	0.114	0.118	0.122
E	0.252 BSC		
E1	0.169	0.173	0.177
e	0.026 BSC		
L	0.018	0.024	0.030
θ	0°	—	8°