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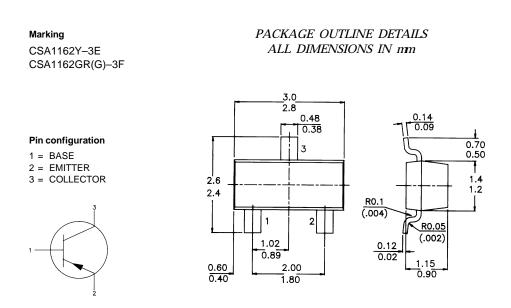
SOT-23 Formed SMD Package



CSA1162

LOW FREQUENCY GENERAL PURPOSE AMPLIFIER TRANSISTOR

P-N-P transistor



ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	50 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	50 V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5 V
Collector current (d.c.)	$-I_C$	max.	150 mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P _{tot}	max.	150 mW
Junction temperature	T_j	max.	150 °C
D.C. current gain	0		
$-I_C = 2 mA; -V_{CE} = 6V$	h _{FE}	min.	70
		max.	400

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)			
Limiting values			
Collector-base voltage (open emitter)	$-V_{CBO}$	max.	50 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	50 V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5 V
Collector current (d.c.)	$-I_C$	max.	150 mA
Base current	$-I_B$	max.	30 mA

Total power dissipation at T _{amb} = 25°C Storage temperature Junction temperature	P _{tot} Tstg T	max. -50 to max.	150 mW +150 °C 150 °C
	T_j	шах.	150 C
CHARACTERISTICS (at $T_A = 25^{\circ}C$ unless otherwise	specified)		
Collector-emitter breakdown voltage $-I_C = 1 \text{ mA}; I_B = 0$	-V _(BR) CEC) min	50 V
Collector cut-off current			
$-V_{CB} = 50 V; I_E = 0$	-I _{CBO}	max.	100 nA
Emitter cut-off current			
$V_{EB} = 5 V; I_C = 0$	I _{EBO}	max.	100 nA
Saturation voltage			
$-I_C = 100 \text{ mA}; -I_B = 10 \text{ mA}$	-V _{CEsat}	max.	0.3 V
D.C. current gain			
$I_C = 2 m A; -V_{CE} = 6 V$	h_{FE}	min.	70
		max.	400
	Y	min. max.	120 240
	GR(G)	min.	200
	an(a)	max.	<i>400</i>
Transition frequency			
$V_{CE} = 10 \ V; \ I_C = 1 \ mA$	f_T	min.	80 MHz
Collector output capacitance			
$V_{CB} = 10 V; I_E = 0; f = 1 MHz$	Cob	max.	7 pF
Noise figure			
$V_{CE} = 6 V; I_C = 0.1 mA$	N.7		
$f = 1 \text{ kHz}; R_g = 10 \text{ kw}$	N_F	max.	10 dB

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Notes

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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