Asymmetric thyristors PSTCSA932-28IX5

Blocking - Off State

V_{DRM} (1)	V_{DSM} (1)	V_{RRM} (1)	V_{RSM}(1)
2800	2800	30	30

V_{RRM} = Repetitive peak reverse voltage
V_{DRM} = Repetitive peak off state voltage
V_{RSM} = Non repetitive peak reverse voltage

Repetitive peak reverse leakage and off state	I_{RRM} / I_{DRM}	10 mA 60 mA (3)
Critical rate of voltage rise	dV/dt (4)	3000 V/μsec

Notes:
All ratings are specified for Tj=25 °C unless otherwise stated.
(1) All voltage ratings are specified for an applied 50Hz/60zHz sinusoidal waveform over the temperature range -40 to +125 °C.
(2) 10 msec. max. pulse width
(3) Maximum value for Tj = 125 °C.
(4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM}. Gate open. Tj = 125 °C.
(5) Non-repetitive value.
(6) The value of di/dt is established in accordance with EIA/NIMA Standard RS-397, Section 5-2-2-6. The value defined would be in addition to that obtained from a snubber circuit, comprising a 0.2 μF capacitor and 20 ohms resistance in parallel with the thristor under test.

Conducting - on state

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Average value of on-state current	$I_{T(AV)M}$		840		A	Sinewave,180° conduction,T_c=85°C
RMS value of on-state current	I_{TRMSM}		2070		A	Nominal value
Peak one cycle surge (non repetitive) current	I_{TSM}		- 18		KA KA	8.3 msec (60Hz), sinusoidal wave-shape, 180° conduction, T_j = 125 °C 10.0 msec (50Hz), sinusoidal wave- shape, 180° conduction, T_j = 125 °C
I square t	I^2t		$1.54x10^3$		KA^2s	8.3 msec and 10.0 msec
Latching current	I_L		-		mA	V_D = 24 V; R_L= 12 ohms
Holding current	I_H		-		mA	$V_{D=}$ 24 V; I = 2.5 A
Peak on-state voltage	V_{TM}		2.4		V	I_{TM} = 1000 A; Duty Cycle ≤ 0.01%; T_j =25 °C
Threshold vlotage	V_{T0}		1.54		V	
Slope resistance	r_T		0.40		mΩ	
Critical rate of rise of on-state current (5, 6)	di/dt		-		A/μs	Switching from V_{DRM} ≤ 1000 V, non-repetitive
Critical rate of rise of on-state current (6)	di/dt		500		A/μs	Switching from V_{DRM} ≤ 1000 V

Tel: +86-514-87782298, 87782296 FAX:+86-514-87782297, 87367519
E-mail :positioning@china.com; yzforever@vip.tom.com; yzpst@pst888.com
WEB SITE：http://www.pst888.com www.yzpst.cc

ELECTRICAL CHARACTERISTICS AND RATINGS

Gating

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Peak gate power dissipation	P_{GM}		-		W	
Average gate power dissipation	$P_{G(AV)}$		-		W	
Peak gate current	I_{GM}		-		A	
Gate current required to trigger all units	I_{GT}		- 300 -		mA mA mA	V_D = 10 V;R_L = 3 ohms;T_j = -40 °C V_D = 10 V;R_L = 3 ohms;T_j = +25 °C V_D = 10 V;R_L = 3 ohms;T_j = +125°C
Gate voltage required to trigger all units	V_{GT}		- 2.5 -		V V V	V_D = 10 V;R_L = 3 ohms;T_j = -40 °C V_D = 10 V;R_L = 3 ohms;T_j = 0-125°C V_D = Rated V_{DRM}; R_L = 1000 ohms; T_j = + 125 °C
Peak negative voltage	V_{RGM}		-		V	

Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Delay time	t_d		-	-	μs	I_{TM} = 50 A; V_D = Rated V_{DRM} Gate pulse: V_G = 20 V; R_G = 20 ohms; t_r = 0.1 μs; t_p = 20 μs
Turn-off time (with V_R = -50 V)	t_q		-	40	μs	I_{TM} = 1000 A; di/dt = 25 A/μs; V_R ≥ -50 V; Re-applied dV/dt = 20 V/μs linear to 80% V_{DRM}; V_G = 0; T_j = 125 °C; Duty cycle ≥ 0.01%
Reverse recovery charge	Q_{rr}		-	-	μC	I_{TM} = 1000 A; di/dt = 25 A/μs; V_R ≥ -50 V

* For guaranteed max. value, contact factory.

THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	T_j	-40	+125		°C	
Storage temperature	T_{stg}	-40	+125		°C	
Thermal resistance - junction to case	$R_{\Theta(j-c)}$		20 -		K/KW	Double sided cooled Single sided cooled
Thermal resistamce - case to heatsink	$R_{\Theta(c-s)}$		- -		K/KW	Double sided cooled * Single sided cooled *
Thermal resistamce - junction to heatsink	$R_{\Theta(j-s)}$		- -		K/KW	Double sided cooled * Single sided cooled *
Mounting force	F	-	-	**22**	kN	
Weight	W			510	g	About

* Mounting surfaces smooth, flat and greased

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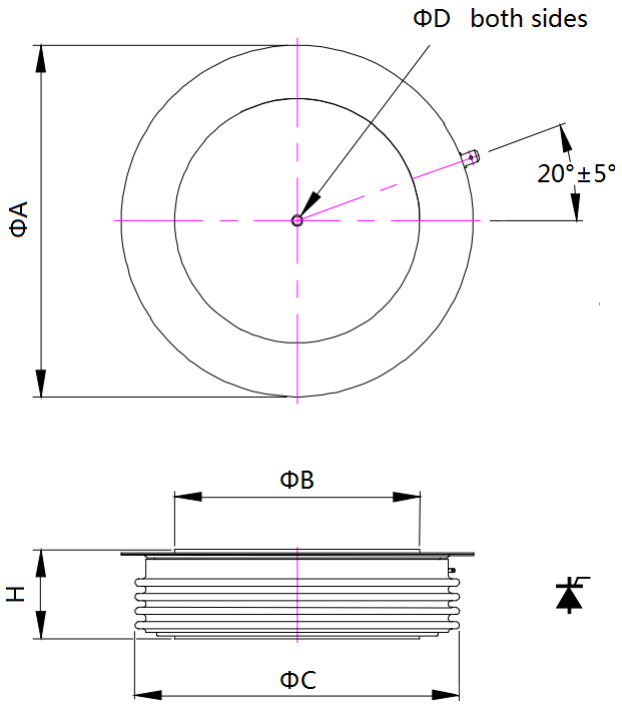
CASE OUTLINE AND DIMENSIONS



Sym	A	B	C	D	H
mm	59	34	53	3.5×3	26±1

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