

N-Channel Trench Power MOSFET

General Description

The CSJ75N62 is N-channel MOS Field Effect Transistor designed for high current switching applications. Rugged EAS capability and ultra low $R_{DS(ON)}$ is suitable for PWM.

Features

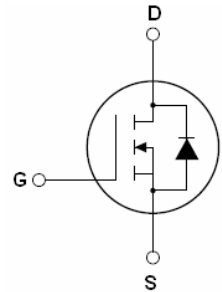
- $V_{DS}=75V$; $I_D=92A$ @ $V_{GS}=10V$;
 $R_{DS(ON)}<7.45m\Omega$ @ $V_{GS}=10V$
- Ultra Low On-Resistance
- High UIS and UIS 100% Test

Application

- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



To-263 Top View



Schematic Diagram

$$V_{DS} = 75 V$$

$$I_D = 92A$$

$$R_{DS(ON)} = 6.2 m\Omega$$

Package Marking and Ordering Information

Device	Device Marking	Device Package	Package Typ	Quantity
CSJ75N62	CSJ75N62	TO-263	Tape&Reel	800pcs
CSJ75N62A	CSJ75N62	TO-263	Tube	50pcs

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	75	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 25	V
$I_D (DC)$	Drain Current (DC) at $T_c=25^\circ C$	92	A
$I_D (DC)$	Drain Current (DC) at $T_c=100^\circ C$	64.4	A
$I_{DM} (pulse)$	Drain Current-Continuous@ Current-Pulsed (Note 1)	368	A
dv/dt	Peak Diode Recovery Voltage	7.3	V/ns
P_D	Maximum Power Dissipation($T_c=25^\circ C$)	147	W
	Derating Factor	0.98	W/°C
E_{AS}	Single Pulse Avalanche Energy (Note 2)	625	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 175	°C

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2.EAS condition: $T_J=25^{\circ}\text{C}$, $V_{DD}=40\text{V}$, $V_G=10\text{V}$, $R_G=25\ \Omega$

Table 2. Thermal Characteristic

Symbol	Parameter	Value	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	---	1.02	$^{\circ}\text{C/W}$

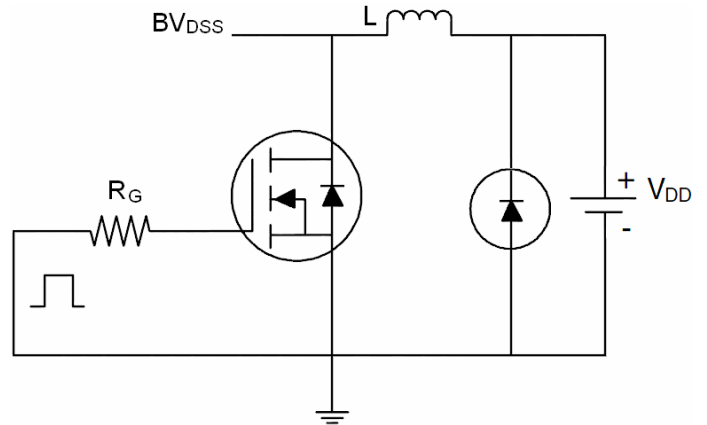
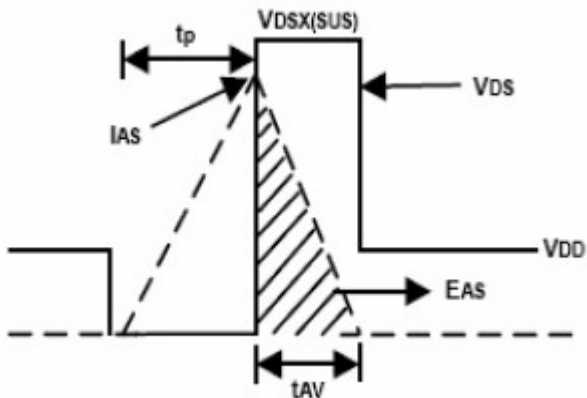
Table 3. Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	75			V
I _{DSS}	Zero Gate Voltage Drain Current(Tc=25℃)	V _{DS} =75V,V _{GS} =0V			1	μA
I _{DSS}	Zero Gate Voltage Drain Current(Tc=125℃)	V _{DS} =75V,V _{GS} =0V			10	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V,V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ,I _D =250μA	2		4	V
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =40A		6.2	7.45	mΩ
Dynamic Characteristics						
g _{FS}	Forward Transconductance	V _{DS} =10V,I _D =15A	20			S
C _{iss}	Input Capacitance	V _{DS} =25V,V _{GS} =0V, f=1.0MHz		5053		PF
C _{oss}	Output Capacitance			442		PF
C _{rss}	Reverse Transfer Capacitance			145		PF
Q _g	Total Gate Charge	V _{DS} =50V,I _D =40A, V _{GS} =10V		115		nC
Q _{gs}	Gate-Source Charge			20		nC
Q _{gd}	Gate-Drain Charge			50		nC
Switching Times						
t _{d(on)}	Turn-on Delay Time	V _{DD} =30V,I _D =40A,R _L =15Ω V _{GS} =10V,R _G =2.5Ω		23		nS
t _r	Turn-on Rise Time			51		nS
t _{d(off)}	Turn-Off Delay Time			66		nS
t _f	Turn-Off Fall Time			23		nS
Source-Drain Diode Characteristics						
I _{SD}	Source-drain Current(Body Diode)			92		A
I _{SDM}	Pulsed Source-Drain Current(Body Diode)			368		A
V _{SD}	Forward On Voltage ^(Note 1)	T _J =25℃,I _{SD} =40A,V _{GS} =0V		0.89	0.99	V
t _{rr}	Reverse Recovery Time ^(Note 1)	T _J =25℃,I _F =75A di/dt=100A/μs		41		nS
Q _{rr}	Reverse Recovery Charge ^(Note 1)			86		nC
t _{on}	Forward Turn-on Time	Intrinsic turn-on time is negligible(turn-on is dominated by L _S +L _D)				

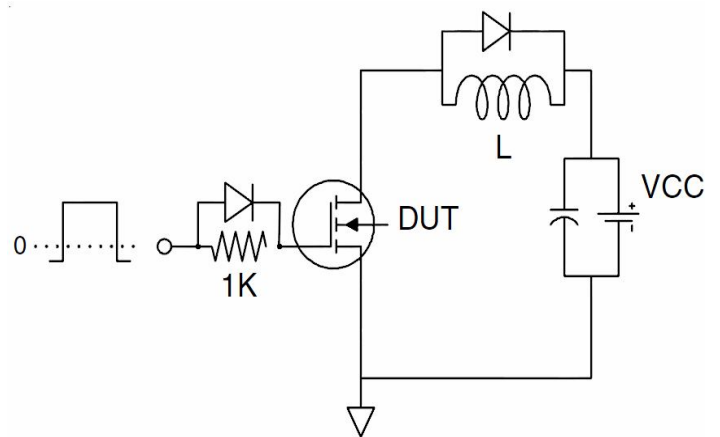
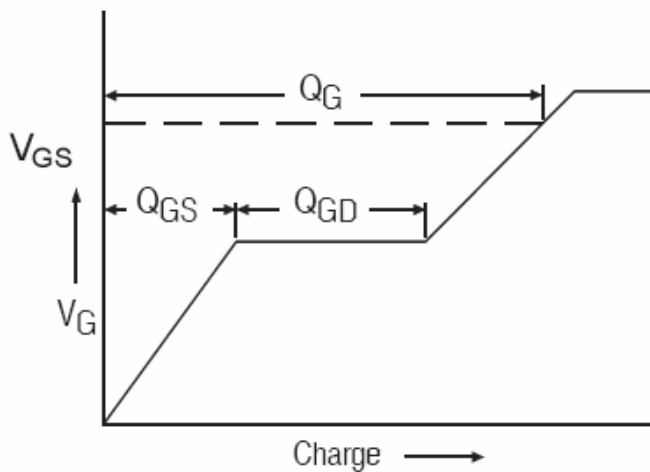
Notes 1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 1.5\%$, $R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$

Test Circuit

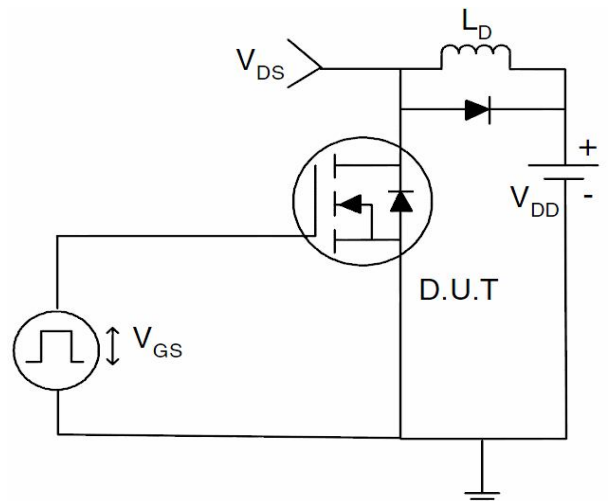
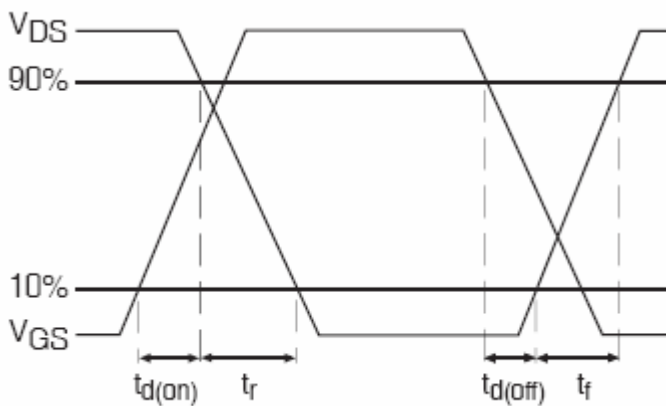
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit:



3) Switch Time Test Circuit:



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)

Figure1. Output Characteristics

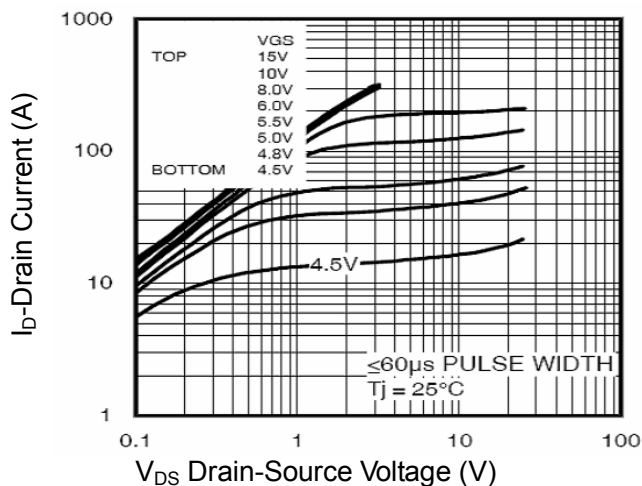


Figure2. Transfer Characteristics

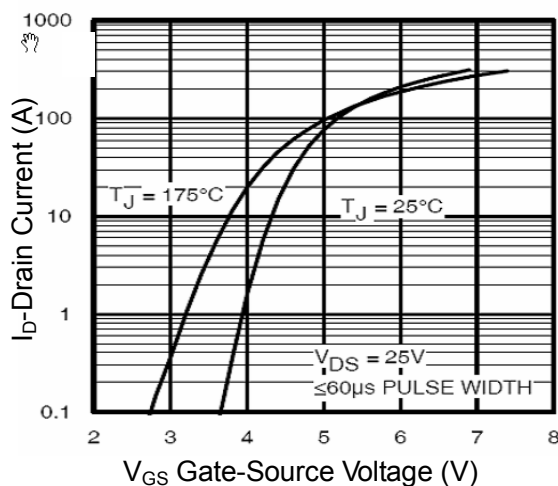


Figure3. BVDSS vs Junction Temperature

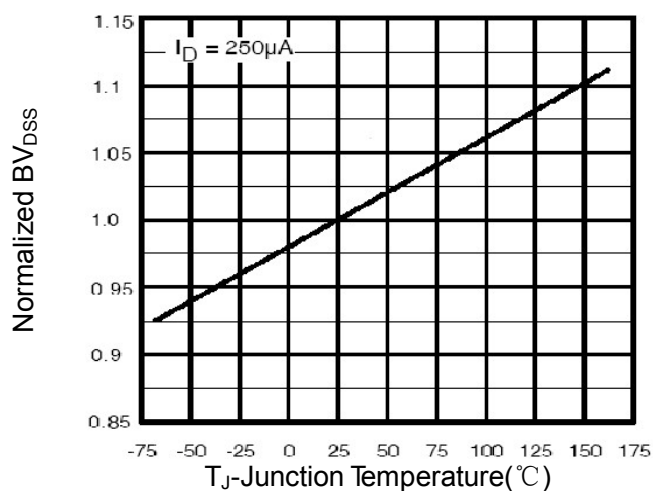


Figure4. ID vs Junction Temperature

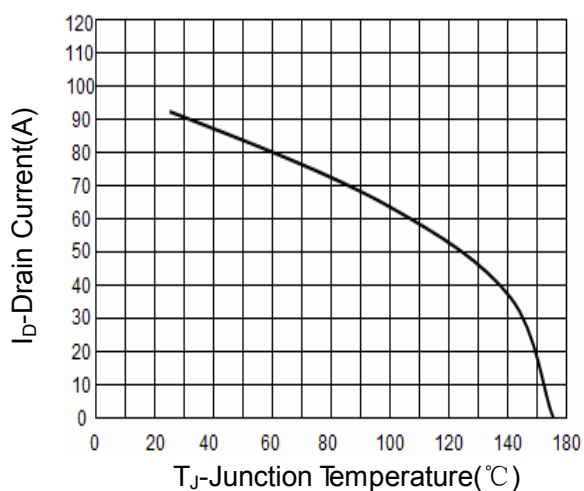


Figure5. VGS(th) vs Junction Temperature

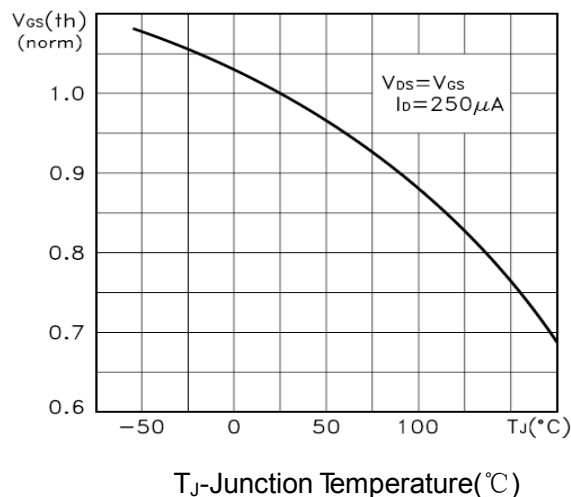


Figure6. Rdson Vs Junction Temperature

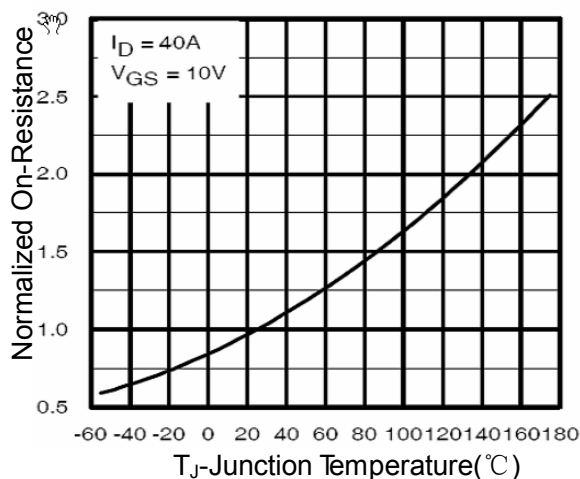


Figure7. Gate Charge

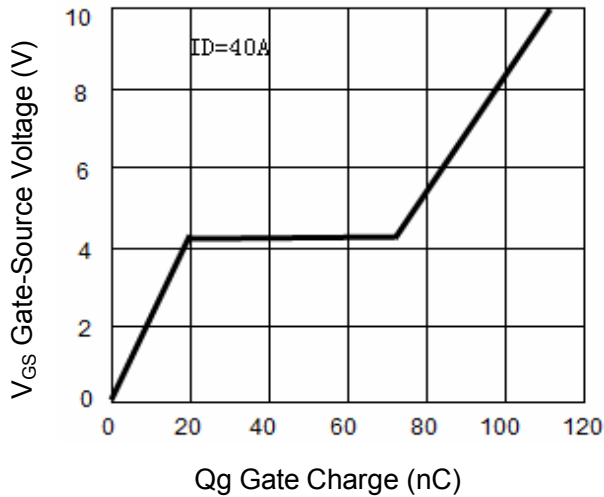


Figure8. Capacitance vs Vds

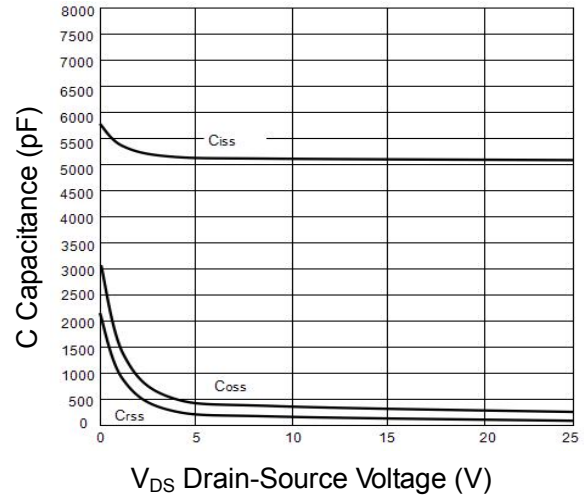


Figure9. Source- Drain Diode Forward

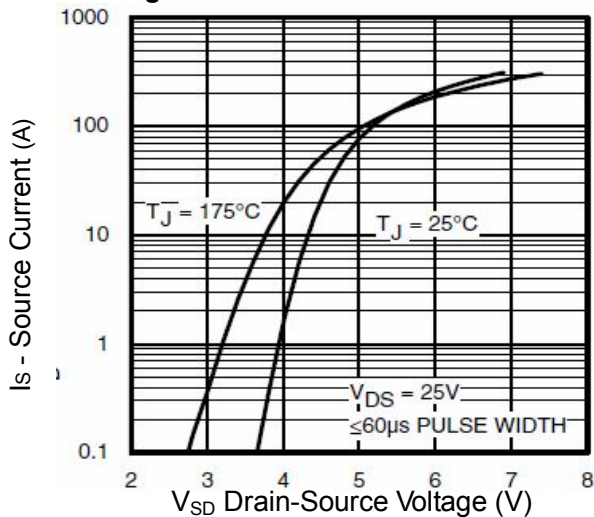


Figure10. Safe Operation Area

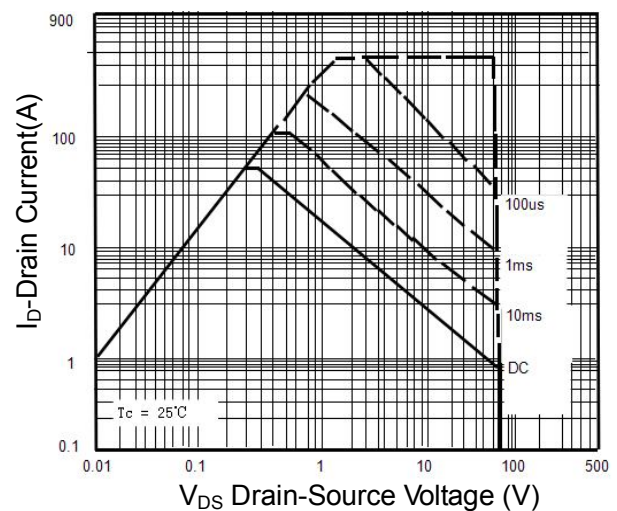
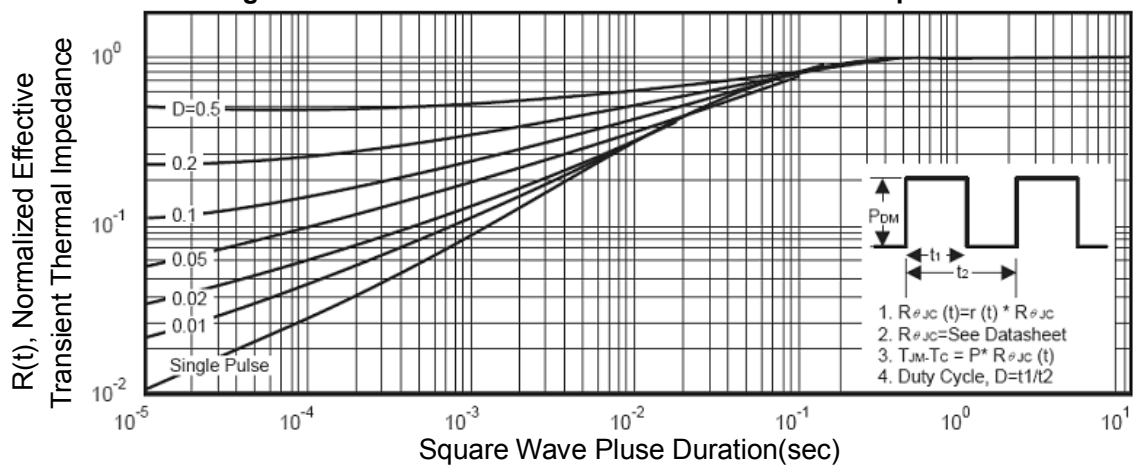
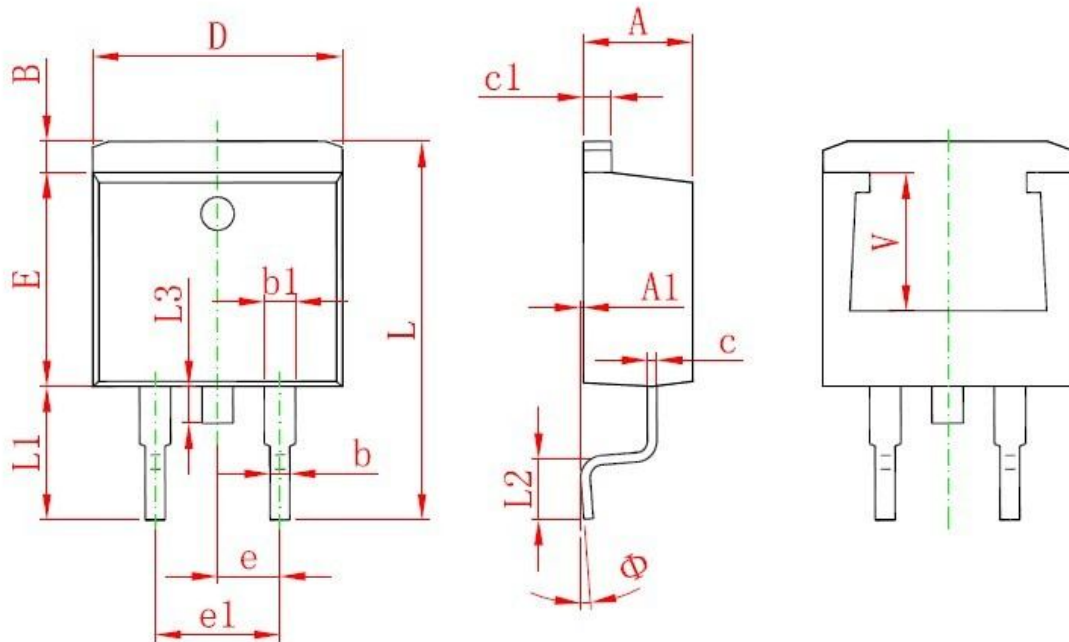


Figure11. Normalized Maximum Transient Thermal Impedance



TO-263 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Ma
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.120	1.420	0.044	0.056
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP.		0.100TYP.	
e1	4.980	5.180	0.196	0.204
L	14.940	15.500	0.588	0.610
L1	4.950	5.450	0.195	0.215
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
V	5.600 REF.		0.220REF.	
Φ	0°	8°	0°	8°