

Chip Integration Technology Corporation

Super Low Barrier High Voltage Power Rectifier

Main Product Characteristics

I _{F(AV)}	2x5A
V _{RRM}	60V
T _J	150°C
$V_{(Typ)}$	0.49V

■ Features

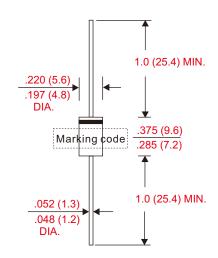
- Axial lead type devices for through hole design.
- · Low forward voltage drop.
- Excellent high temperature stability.
- Fast switching capability.
- Suffix "G" indicates Halogen-free part, ex.CSRL1060G-A.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

■ Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- · Case: Molded plastic, DO-201AD / DO-27
- Lead: Axial leads, solderable per MIL-STD-202, Method 208 guranteed
- Polarity: Color band denotes cathode end
- Weight : Approximated 1.10 gram
- Maximum ratings and electrical characteristics

Outline

DO-27(DO-201AD)



Dimensions in inches and (millimeters)

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Conditions	Symbol	CSRL1060-A		
Marking code		С		UNIT	
Peak repetitive reverse voltage		V _{RRM}			
Working peak reverse voltage		V _{RWM}	60	V	
DC blocking voltage		V _{RM}			
Forward rectified current		Io	10	Α	
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I _{FSM}	180	А	
Peak repetitive reverse surge current	2us - 1kHz	I _{RRM}	3	Α	
Thermal resistance	Junction to case	R _{eJC}	18	°C/W	
Operating and Storage temperature		T _J , T _{STG}	-55 ~ +150	°C	

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward voltage drop	$I_F = 10A, T_J = 25^{\circ}C$				650	mV
	$I_F = 10A, T_J = 125^{\circ}C$	$V_{\scriptscriptstyle F}$		490	560	
	$I_F = 20A, T_J = 25^{\circ}C$				790	
I Pavarca current	$V_R = V_{RRM} T_J = 25^{\circ}C$				0.5	mA
	$V_R = V_{RRM} T_J = 125^{\circ}C$	I _R			100	

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■ Rating and characteristic curves

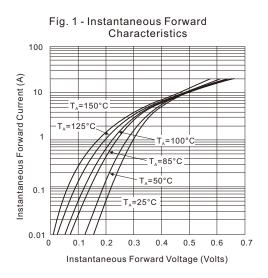
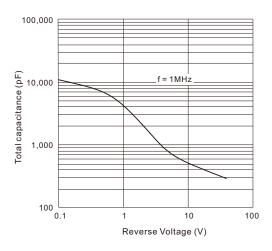


Fig. 2 - Reverse Characteristics 1000 Instantaneous Reverse Current (mA) 100 $T_A = 150^{\circ}C$ 10 T_A=125°C $T_A = 100^{\circ}C$ T_A=85°C T₄=50°C 0.1 T_A=25°C 0.01 40 50 10 20 60 Reverse Voltage (V)

Fig. 3 - Total Capacitance VS. Reverse Voltage



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