



CT815 Series DC Input 4-Pin DMC-Isolator® Photodarlington Optocoupler

Features

- High isolation 5000 VRMS
- Patented coplanar structure DMC-Isolator®
- DC input with Darlington output
- Operating Temperature range - 55 °C to 110 °C
- External creepage distance $\geq 7.4\text{mm}$
- Distance Through Isolation $\geq 0.4\text{mm}$
- Clearance Distance $\geq 7.5\text{mm}$ (S/SL Type)
- Clearance Distance $\geq 8.0\text{mm}$ (M/SLM Type)
- RoHS and REACH Compliance
- Halogen Free Compliance (Optional)
- MSL class 1
- Regulatory Approvals
 - ✓ UL - UL1577 (E364000)
 - ✓ VDE - EN60747-5-5(VDE0884-5)
 - ✓ CQC – GB4943.1, GB8898 (14001104781)
 - ✓ IEC62368 (FI/41119)

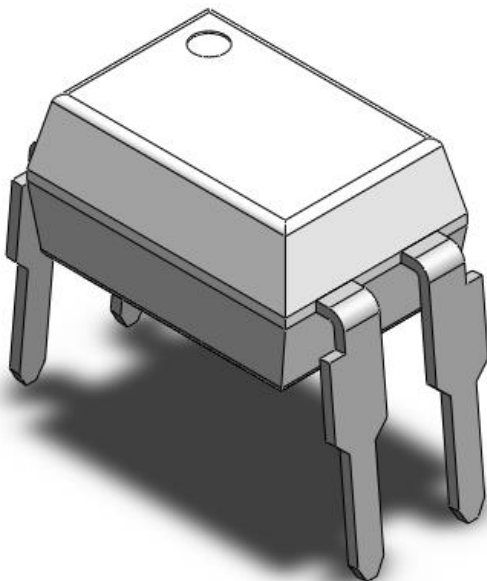
Description

The CT815 series consists of a photodarlington transistor optically coupled to an Infrared-emitting diode in a 4-lead DIP DMC-Isolator® package with different lead forming options.

Applications

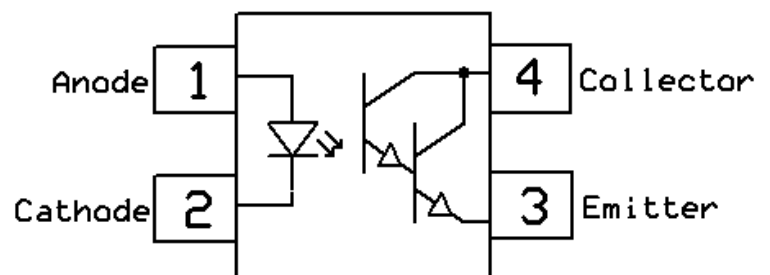
- Power supply regulators
- Digital logic outputs
- Microprocessor inputs

Package Outline



Note: Different lead forming options available. See package dimension.

Schematic





CT815 Series DC Input 4-Pin DMC-Isolator® Photodarlington Optocoupler

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$, unless otherwise specified

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters	Ratings	Units	Notes
V_{ISO}	Isolation voltage (AC, 1 minute, 40 ~ 60% R.H.)	5000	V_{RMS}	
T_{OPR}	Operating temperature	-55 ~ +110	$^\circ\text{C}$	
T_{STG}	Storage temperature	-55 ~ +150	$^\circ\text{C}$	
T_{SOL}	Soldering temperature (For 10 seconds)	260	$^\circ\text{C}$	
P_{TOT}	Total power dissipation	200	mW	
Emitter				
I_F	Forward current	60	mA	
$I_{F(TRANS)}$	Peak transient current ($\leq 1\mu\text{s P.W, 300pps}$)	1	A	
V_R	Reverse voltage	6	V	
P_D	Power dissipation	100	mW	
Detector				
P_C	Power dissipation	150	mW	
B_{VCEO}	Collector-Emitter Breakdown Voltage	40	V	
B_{VECO}	Emitter-Collector Breakdown Voltage	7	V	
I_C	Collector Current	80	mA	



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Electrical Characteristics $T_A = 25^\circ\text{C}$, unless otherwise specified

Emitter Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
V_F	Forward voltage	$I_F = 10\text{mA}$	-	1.24	1.4	V	
I_R	Reverse Current	$V_R = 6\text{V}$	-	-	5	μA	
C_{IN}	Input Capacitance	$f = 1\text{MHz}$	-	30	250	pF	

Detector Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
$B_{V_{CEO}}$	Collector-Emitter Breakdown	$I_C = 100\mu\text{A}$	40	-	-	V	
$B_{V_{ECO}}$	Emitter-Collector Breakdown	$I_E = 100\mu\text{A}$	7	-	-	V	
I_{CEO}	Collector-Emitter Dark Current	$V_{CE} = 10\text{V}, I_F = 0\text{mA}$	-	-	1	μA	

Transfer Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
CTR	Current Transfer Ratio	$I_F = 1\text{mA}, V_{CE} = 2\text{V}$	600	-	7500	%	
$V_{CE(SAT)}$	Collector-Emitter Saturation Voltage	$I_F = 20\text{mA}, I_C = 5\text{mA}$	-	0.8	1	V	
R_{IO}	Isolation Resistance	$V_{IO} = 500\text{V}_{DC}$	5×10^{10}	-	-	Ω	
C_{IO}	Isolation Capacitance	$f = 1\text{MHz}$	-	0.25	1	pF	

Switching Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
t_r	Rise Time	$I_C = 10\text{mA}, V_{CE} = 2\text{V},$	-	-	300	μs	
t_f	Fall Time	$R_L = 100\Omega$	-	-	250		



Typical Characteristic Curves $T_A = 25^\circ\text{C}$, unless otherwise specified

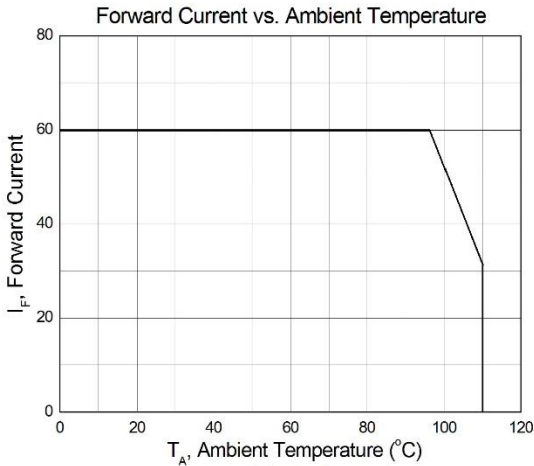


Figure 1

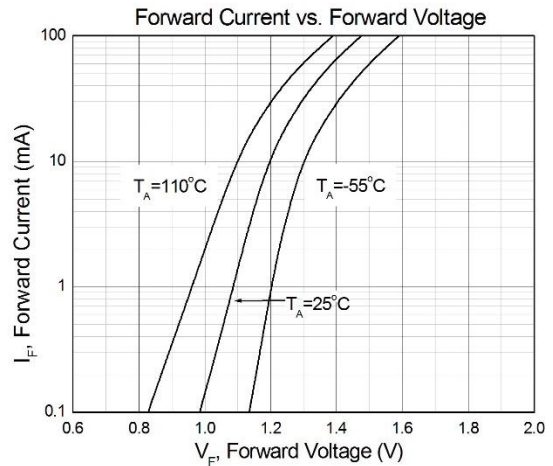


Figure 2

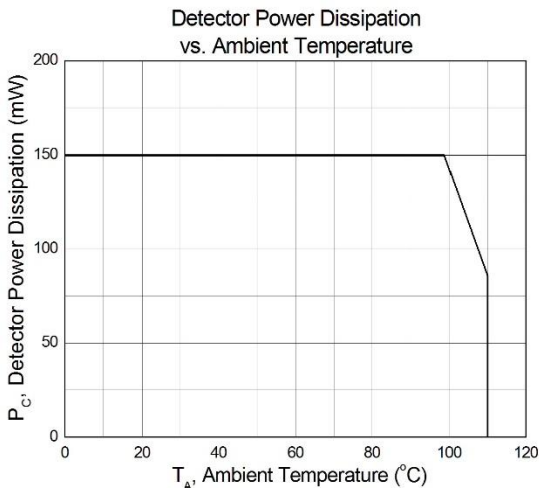


Figure 3

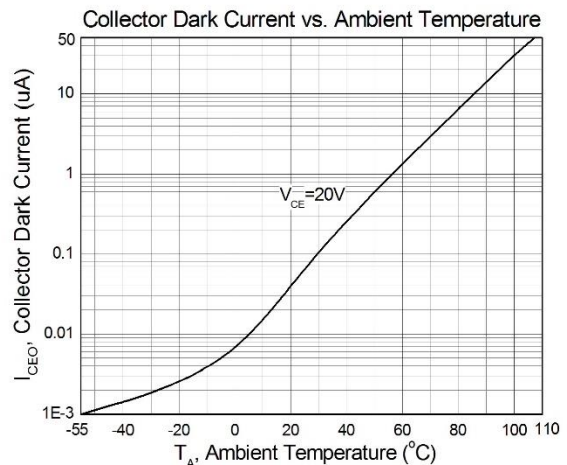


Figure 4

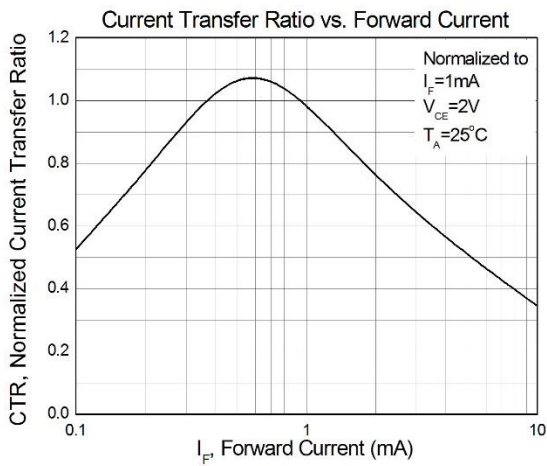


Figure 5

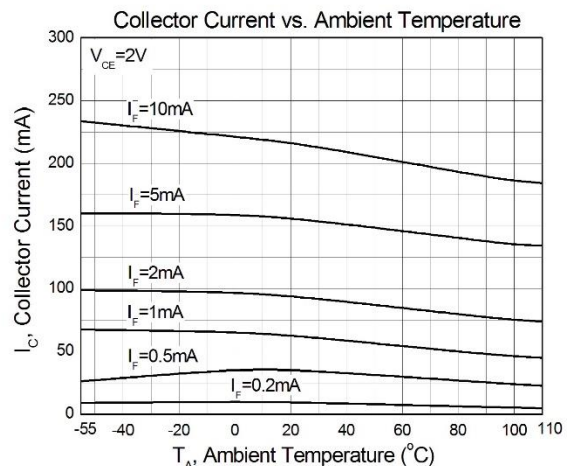


Figure 6



Typical Characteristic Curves $T_A = 25^\circ\text{C}$, unless otherwise specified (Continued)

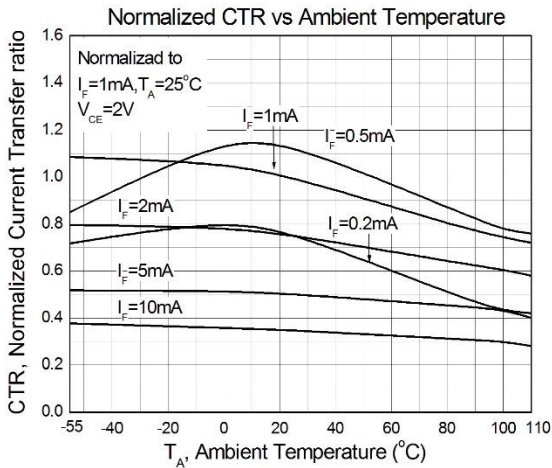


Figure 7

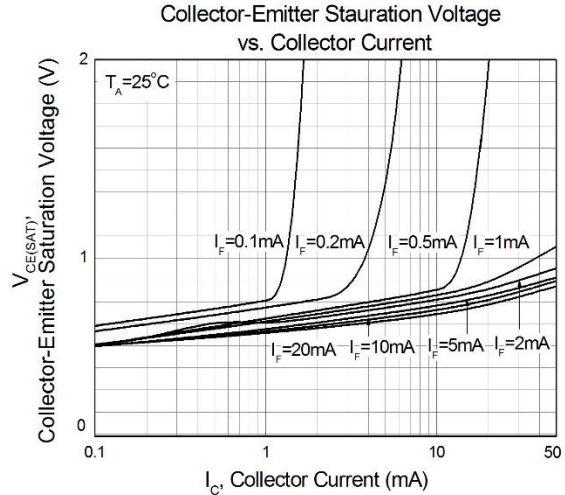


Figure 8

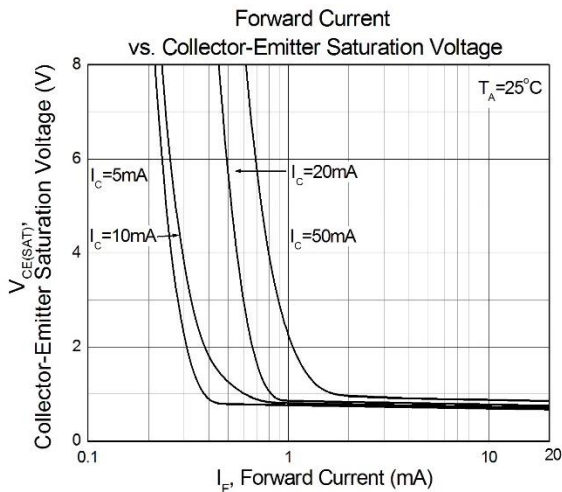


Figure 9

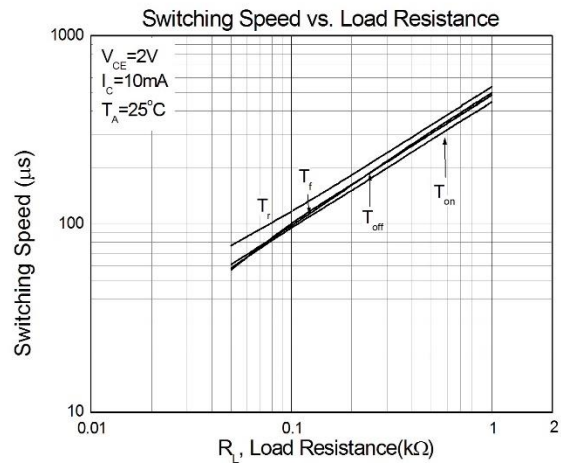


Figure 10

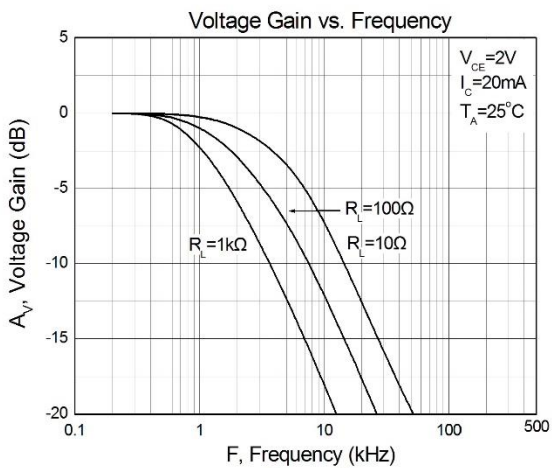


Figure 11

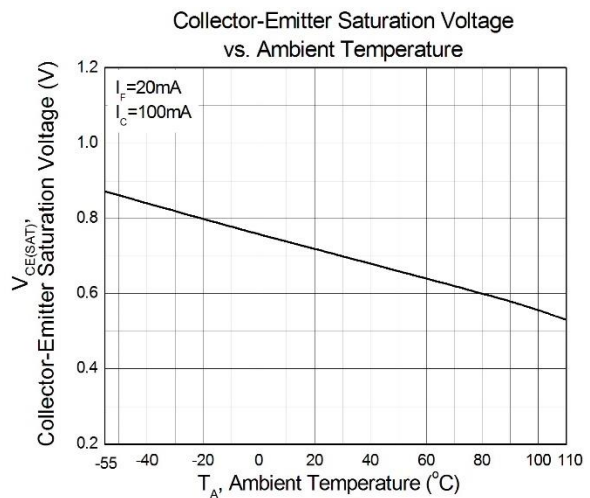


Figure 12



Test Circuit

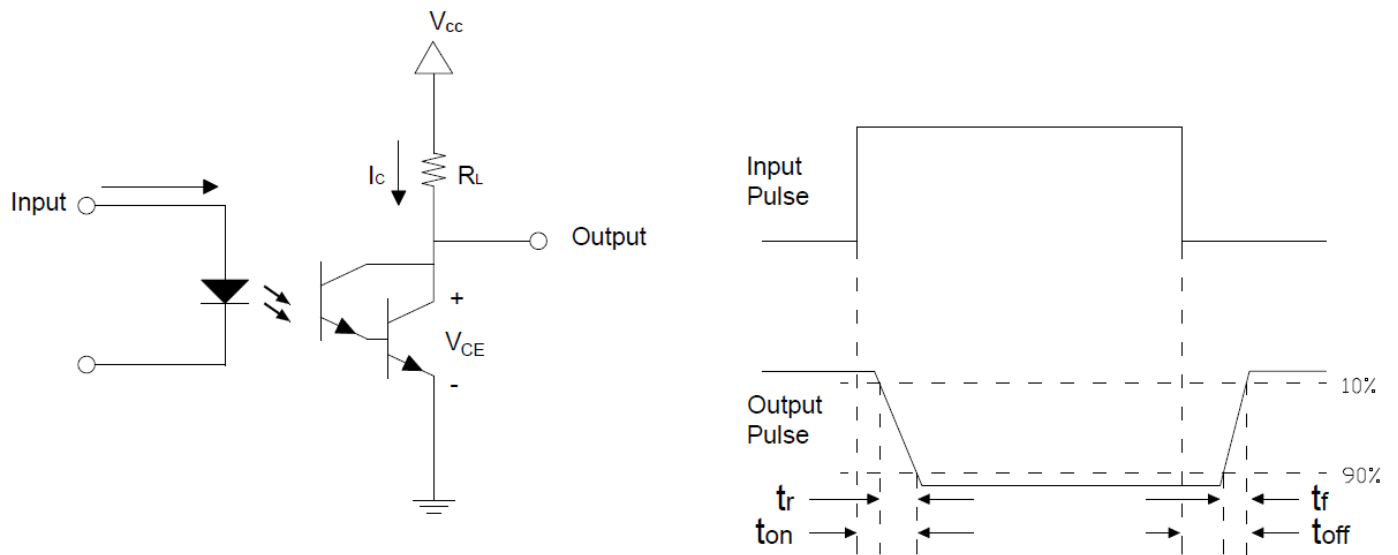
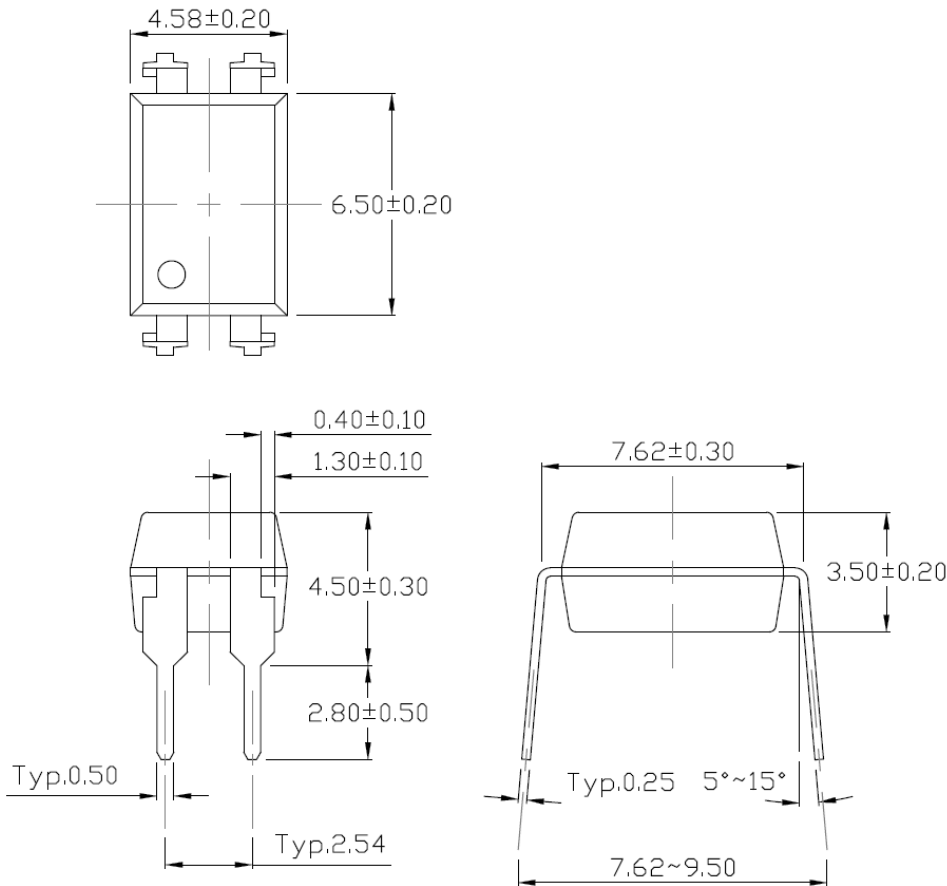


Figure 13: Switching Time Test Circuits

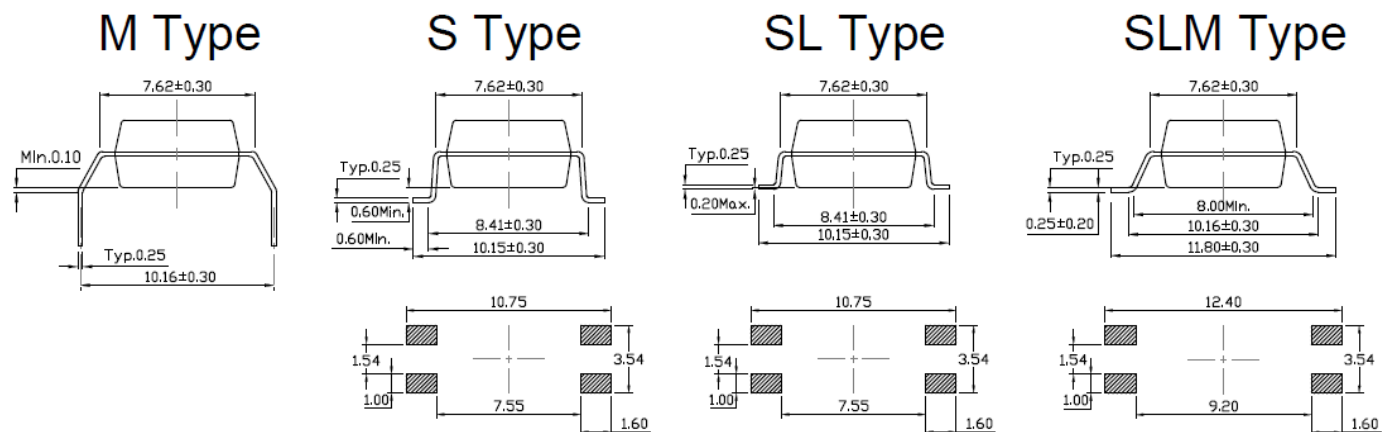


Package Dimension *Dimensions in mm unless otherwise stated*

Standard DIP – Through Hole



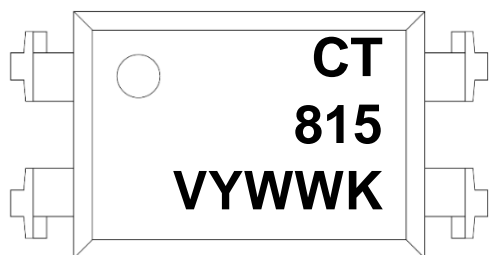
Forming Option





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Marking Information



Note:

- CT : Denotes “CT Micro”
- 815 : Part Number
- V : VDE Safety Mark Option (Blank or V)
- Y : One Digit Year Code
- WW : Two Digit Work Week
- K : Manufacturing Code

Ordering Information

CT815(Y)(Z)-G

- CT = Denotes “CT Micro”
- 815 = Part Number
- Y = Lead Form Option (S, SL, M, SLM or Blank)
- Z = Tape and Reel Option (Blank, T1 or T2)
- G = Material Option (G: Halogen Free, Blank: Non-Halogen Free)

Option	Description	Quantity
None	Standard 4 Pin DIP	100 Units/Tube
M	Gullwing (400mil) Lead Forming	100 Units/Tube
S(T1)	Surface Mount Lead Forming – With Option 1 Taping	1500 Units/Reel
S(T2)	Surface Mount Lead Forming – With Option 2 Taping	1500 Units/Reel
SL(T1)	Surface Mount (Low Profile) Lead Forming– With Option 1 Taping	1500 Units/Reel
SL(T2)	Surface Mount (Low Profile) Lead Forming – With Option 2 Taping	1500 Units/Reel
SLM(T1)	Surface Mount (Gullwing) Lead Forming– With Option 1 Taping	1500 Units/Reel
SLM(T2)	Surface Mount (Gullwing) Lead Forming – With Option 2 Taping	1500 Units/Reel

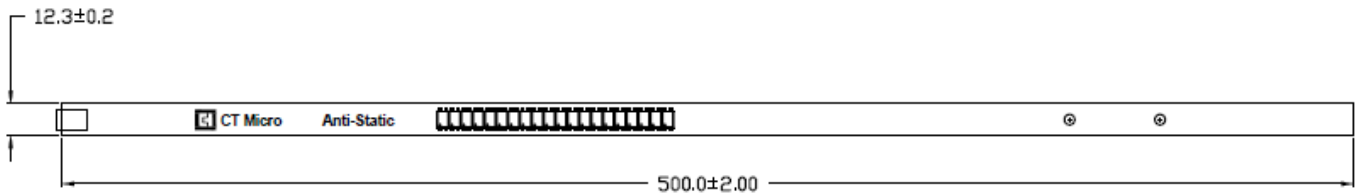


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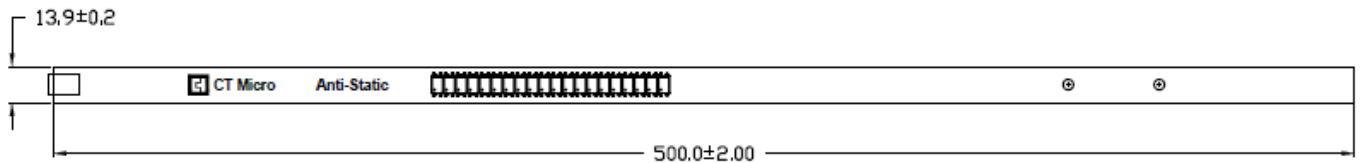
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Carrier Specifications *Dimensions in mm unless otherwise stated*

Tube Option Standard DIP

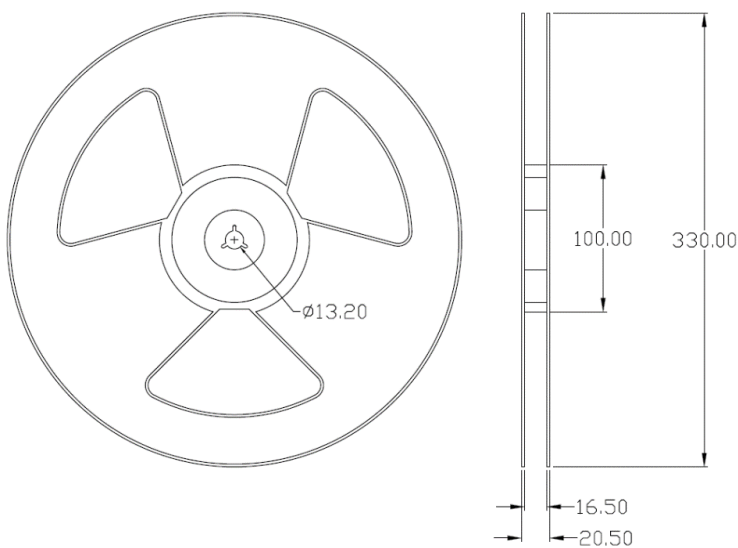


Tube Option M Type

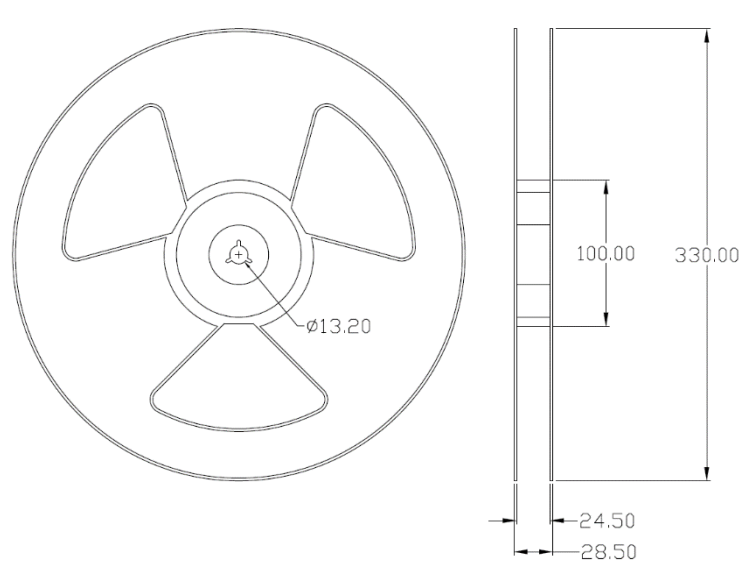


Reel Dimension *All dimensions are in mm, unless otherwise stated*

Option S(T1/T2) & SL(T1/T2)



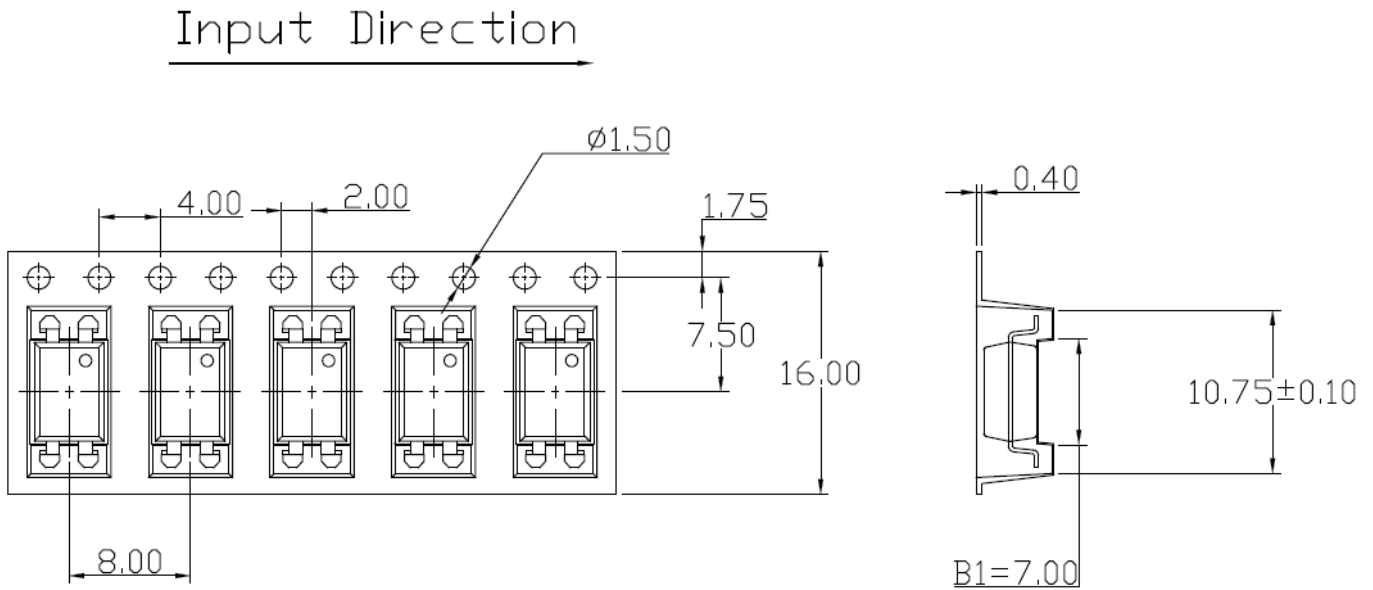
Option SLM(T1/T2)



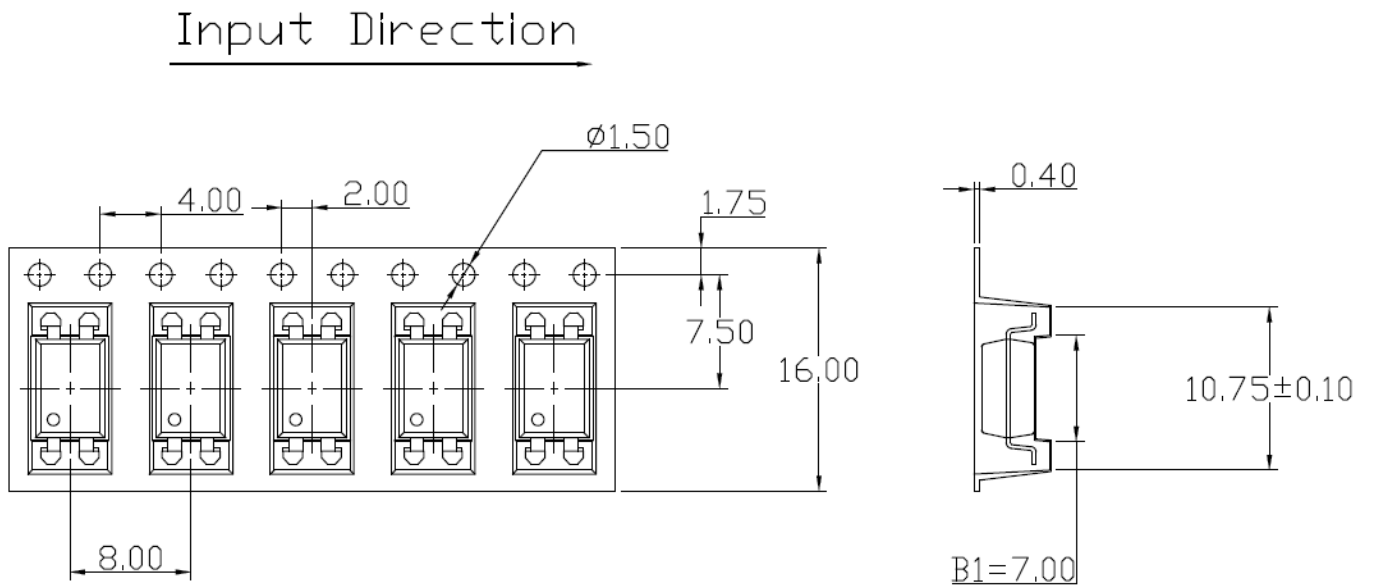


Carrier Tape Specifications *Dimensions in mm unless otherwise stated*

Option S(T1) & SL(T1)



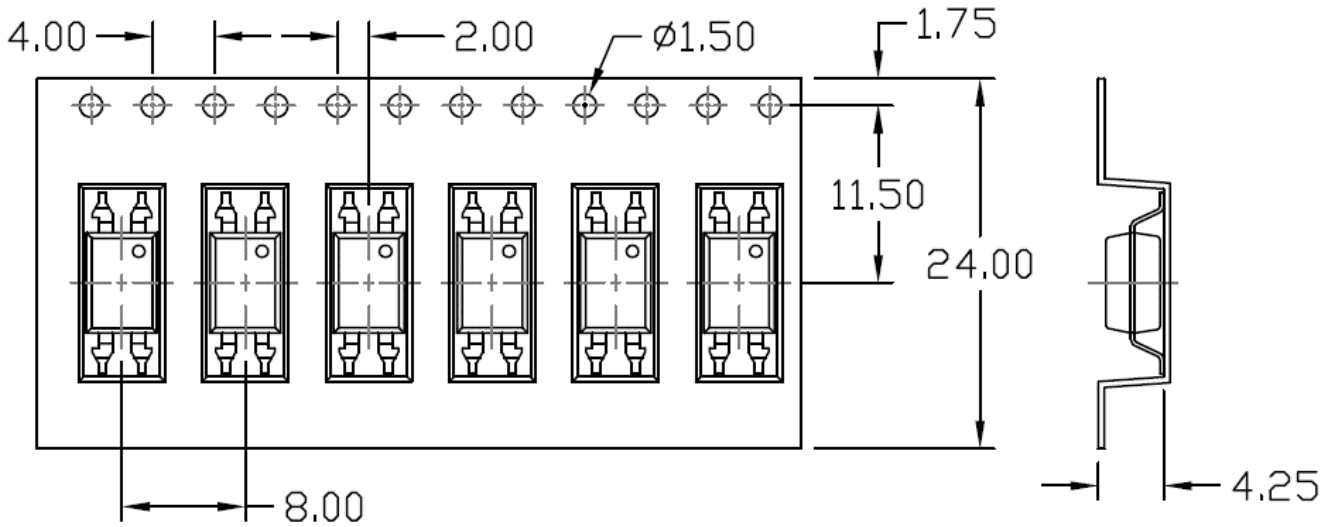
Option S(T2) & SL(T2)





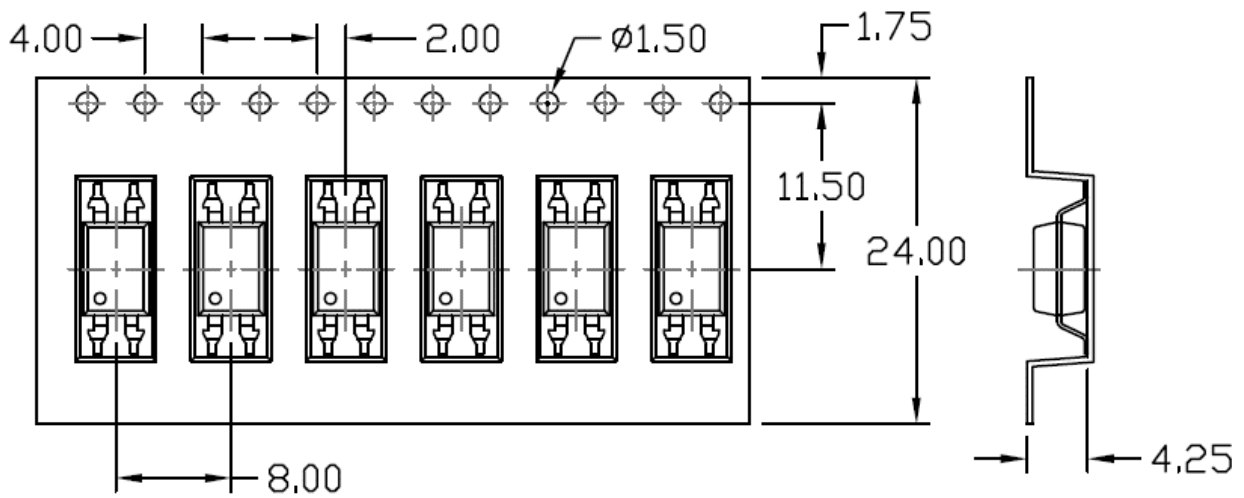
Option SLM(T1)

Input Direction



Option SLM(T2)

Input Direction





Solderability spec (Follow the JEDEC standard JESD22-B102)

Reflow Soldering: Immersed surface, other than the end of pin as cut-surface, must be covered by solder.

Solder-Bath: More than 95% of the electrode must be covered with solder.

Wave soldering (Follow the JEDEC standard JESD22-A111)

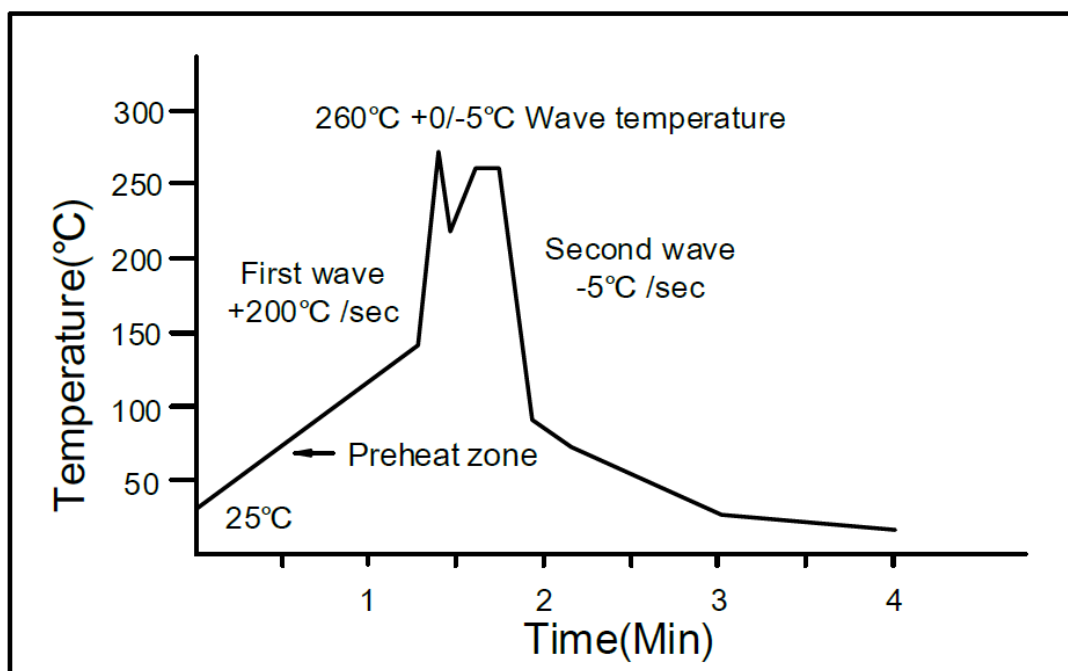
One time soldering is recommended within the condition of temperature.

Temperature: $260 \pm 0/-5^\circ\text{C}$.

Time: 10 sec.

Preheat temperature: 25 to 140°C .

Preheat time: 30 to 80 sec.



Iron soldering (Follow the standard MIL-STD 202G, Method 210F)

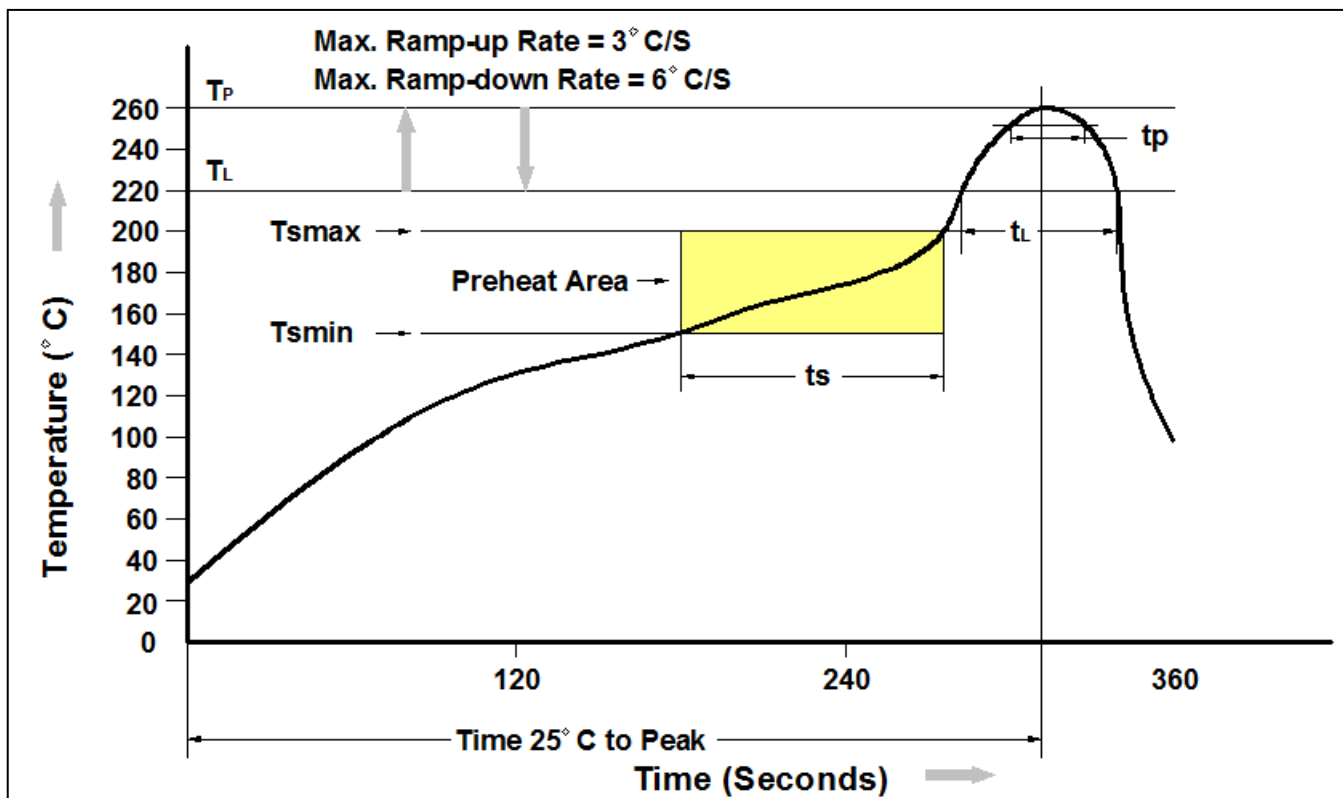
Allow single lead soldering in every single process.

One time soldering is recommended. Temperature: $350 \pm 10^\circ\text{C}$

Time: 5 sec max.



Reflow Profile (Follow the JEDEC standard J-STD-020)



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (ts) from (Tsmmin to Tsmax)	60-120 seconds
Ramp-up Rate (tL to tp)	3°C/second max.
Liquidous Temperature (TL)	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (tp) within 5°C of 260°C	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.



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