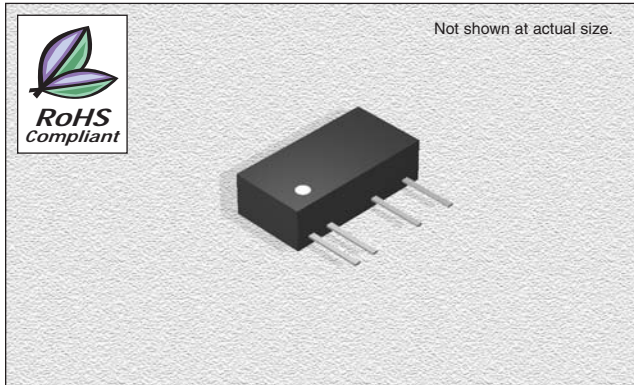


CTDD1907SF-S3K-2 Series

Fixed Input Isolated & Unregulated
2W Output, Single Output



FEATURES

- Efficiency:** To 86%
- Temperature Range:** -40°C to 85°C
- Isolation:** 3KVDC
- Package:** UL94-V0
- Power Density:** 1.42W/cm³
- Miscellaneous:** Single Output Voltage. No heatsink required. No external component required. Industry standard pinout. Small footprint. Custom service available.
- Samples available.**

CHARACTERISTICS

- Short Circuit Protection:** 1 second
- Temperature Rise at Full Load:** 25°C Max. 15°C Typ.
- Cooling:** Free air convection
- No-load power consumption:** 10% nominal power (typical)
- Operating Temperature Range:** -40°C to +85°C
- Storage Temperature Range:** -55°C to +125°C
- Lead Temperature:** 300°C (1.5mm from case for 10 seconds)
- Storage Humidity Range:** ≤ 95%
- Case Material:** Plastic (UL94-V0)
- MTBF:** >3,500,000 hours
- Miscellaneous:** RoHS Compliant.

SPECIFICATIONS

Part Number	Vin Nom.	Input Range	Vout	Iout Max. (mA)	Iout Min. (mA)	Effi(Typ.)
CTDD1907SF-0503S3K-2	5VDC	4.5-5.5VDC	3.3VDC	400	40	70%
CTDD1907SF-0505S3K-2	5VDC	4.5-5.5VDC	5VDC	400	40	80%
CTDD1907SF-0509S3K-2	5VDC	4.5-5.5VDC	9VDC	222	22.2	81%
CTDD1907SF-0512S3K-2	5VDC	4.5-5.5VDC	12VDC	167	16.7	82%
CTDD1907SF-0515S3K-2	5VDC	4.5-5.5VDC	15VDC	133	13.3	84%
CTDD1907SF-1205S3K-2	12VDC	10.8-13.2VDC	5VDC	400	40	80%
CTDD1907SF-1209S3K-2	12VDC	10.8-13.2VDC	9VDC	222	22.2	83%
CTDD1907SF-1212S3K-2	12VDC	10.8-13.2VDC	12VDC	167	16.7	84%
CTDD1907SF-1215S3K-2	12VDC	10.8-13.2VDC	15VDC	133	13.3	85%
CTDD1907SF-2405S3K-2	24VDC	21.6-26.4VDC	5VDC	400	40	81%
CTDD1907SF-2409S3K-2	24VDC	21.6-26.4VDC	9VDC	222	22.2	84%
CTDD1907SF-2412S3K-2	24VDC	21.6-26.4VDC	12VDC	167	16.7	85%
CTDD1907SF-2415S3K-2	24VDC	21.6-26.4VDC	15VDC	133	13.3	86%

ISOLATION SPECIFICATIONS

Item	Test Condition	MIN	TYP	MAX	Units
Isolation Voltage	Tested for 1 minute	3000			VDC
Isolation Resistance	Test at 500VDC	1000			MΩ

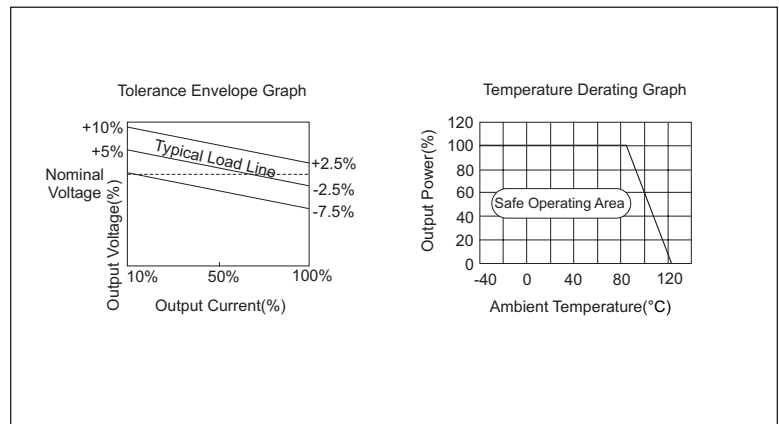
OUTPUT SPECIFICATIONS

Item	Test Condition	MIN	TYP	MAX	Units
Output Power		0.2		2	W
Line regulation	For Vin change of 1%			1.5	%
Load regulation	10% to 100% full load		10	15	%
Output voltage accuracy	See tolerance envelope graph				
Temperature drift	100% full load			0.03	%/°C
Ripple & noise	20Hz bandwidth		100	200	mVp-p
Switching frequency	Full load, nominal input voltage		75		KHz

Note:

1. All specifications measured at TA=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.

TYPICAL CHARACTERISTICS

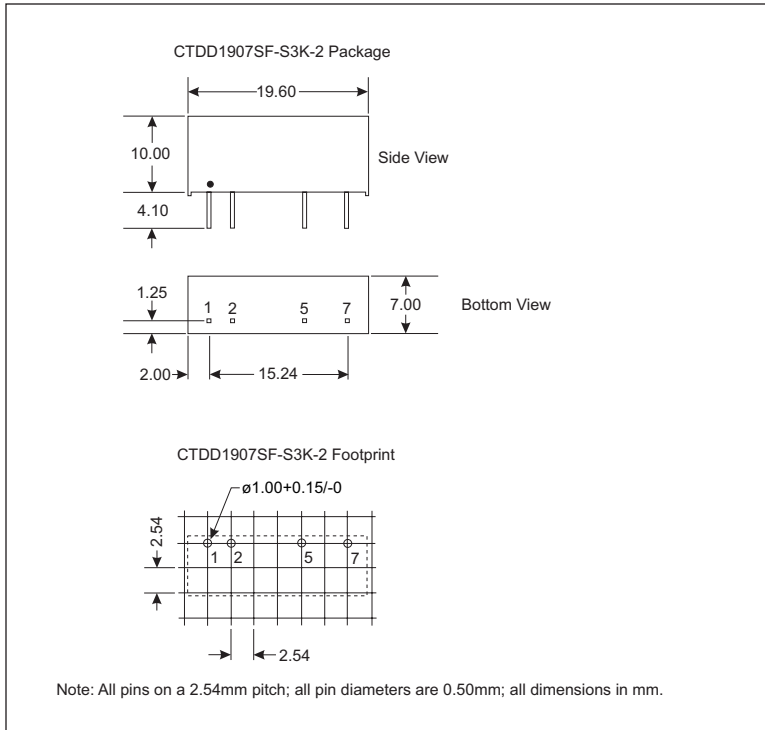


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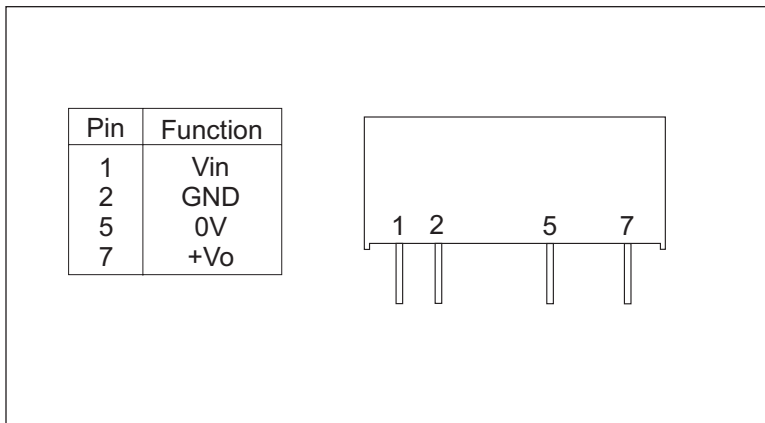
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OUTLINE DIMENSIONS & RECOMMENDED FOOTPRINT DETAILS



PIN CONNECTIONS



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