

# CTL0015PS-R3

**P-Channel Enhancement MOSFET** 

## Features

- Drain-Source Breakdown Voltage V<sub>DSS</sub> -50 V
- Drain-Source On-Resistance
  R<sub>DS(ON)</sub> 5Ω, at V<sub>GS</sub>= -5.0V, I<sub>D</sub>= -0.1A
- Continuous Drain Current at Tc=25°C ID = -0.13A
- Advanced high cell density Trench Technology
- RoHS Compliance & Halogen Free

## Description

The CTL0015PS-R3 uses high performance Trench Technology to provide excellent R<sub>DS(ON)</sub> and low gate charge which is suitable for most of the synchronous buck converter applications.

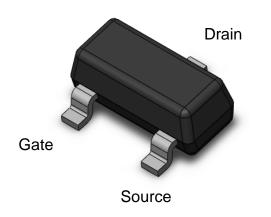
## Applications

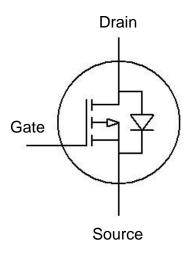
#### • DC to DC Converter

- Load switching
- Battery

## Package Outline

# Schematic







# CTL0015PS-R3

# **P-Channel Enhancement MOSFET**

# Absolute Maximum Rating at 25°C

Symbol	Parameters	Test Conditions	Min	Notes
Vds	Drain-Source Voltage	-50	V	
Vgs	Gate-Source Voltage	±20	V	
lo	Continuous Drain Current	-0.13	А	1
Ідм	Pulsed Drain Current	-0.52	А	1
PD	Total Power Dissipation	0.225	W	2
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

#### **Thermal Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
R <sub>0JA4</sub>	Thermal Resistance			175			1 /
<b>N</b> 0JA4	Junction-Ambient (t=10s)			175		°C W	1,4



## **Electrical Characteristics** $T_A = 25^{\circ}C$ (unless otherwise specified)

#### Static Characteristics

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Bvdss	Drain-Source Breakdown Voltage	Vgs= 0V, Id= -250µA	-50	-	-	V	
ldss	Drain-Source Leakage Current	Vds = -25V, Vgs = 0V	-	-	-0.1	μA	
IDSS	Drain-Source Leakage Current	VDS = -50V, VGS = 0V			15	μA	
lgss	Gate-Source Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±60	μA	

#### **On Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Rds(on)	Drain-Source On-Resistance	$V_{GS}$ = -5.0V, $I_{D}$ = -100mA	-	5.0	10	Ω	3
VGS(th)	Gate-Source Threshold Voltage	$V_{GS} = V_{DS}$ , I I <sub>D</sub> =-250µA	-0.8		-2.0	V	3

#### **Dynamic Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Ciss	Input Capacitance	Vgs =0V,	-	29	-		
Coss	Output Capacitance	Vds =-5V	-	11	-	pF	
Crss	Reverse Transfer Capacitance	f=1MHz	-	4.8	-		

#### **Switching Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
TD(ON)	Turn-On Delay Time		-	2.5	-		
TR	Rise Time	$V_{DS} = -15V ,$	-	1.0	-	20	
TD(OFF)	Turn-Off Delay Time	$R_G = 15\Omega$ ,	-	16	-	ns	
TF	Fall Time	I <sub>D</sub> =-2.5A	-	8.0	-	1	
QG	Total Gate Charge	V <sub>DS</sub> = -15V ,	-	0.6	-	nC	



# CTL0015PS-R3

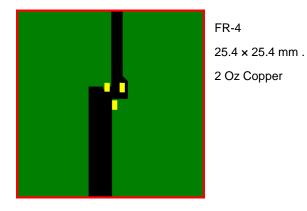
# **P-Channel Enhancement MOSFET**

#### **Drain-Source Diode Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Vsd	Body Diode Forward Voltage	Vgs = 0V, Id = -3.1	-	-2.5	-	V	
Isd	Body Diode Continuous Current		-	-	-130	mA	1

Note:

- 1. The power dissipation is limited by 150°C junction temperature.
- 2. Device mounted on a glass-epoxy board



Actual Size

- 3. The data tested by pulsed , pulse width  $\,\leq\,$  300 $\mu s$  , duty cycle  $\,\leq\,$  2%
- 4. Thermal Resistance follow JESD51-3.



# **CTL0015PS-R3 P-Channel Enhancement MOSFET**

V<sub>GS</sub>=3.5V

3.25V

3.0V

2.75V

2.5V 2.25V

150°C

25°C

-55°C

0.4

0.3

0.5

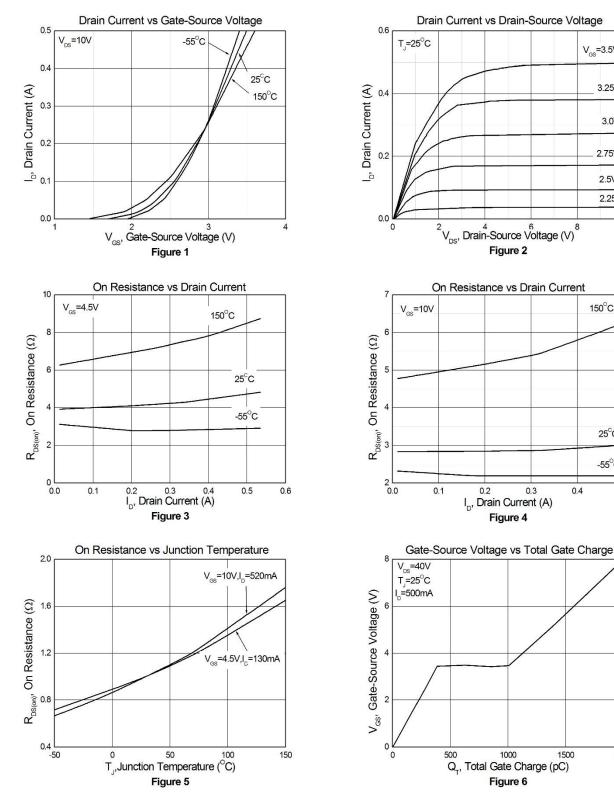
2000

Rev 1

Nov, 2013

10

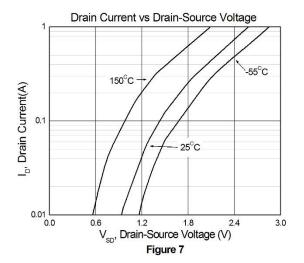
## **Typical Characteristic Curves**



1500



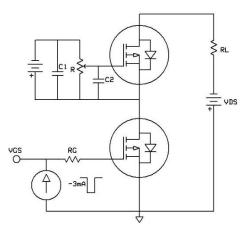
# CTL0015PS-R3 P-Channel Enhancement MOSFET



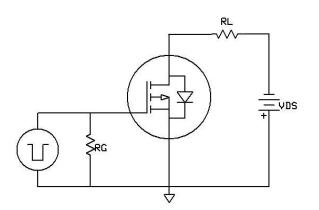


## **Test Circuits & Waveforms**

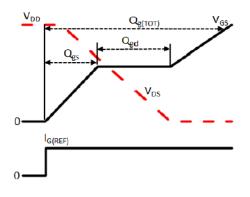
## Figure 8: Gate Charge Test Circuit



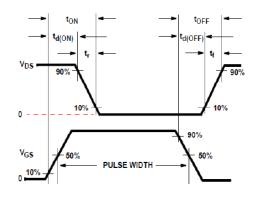
## Figure 10: Switching Time Test Circuit



## Figure 9: Gate Charge Waveform

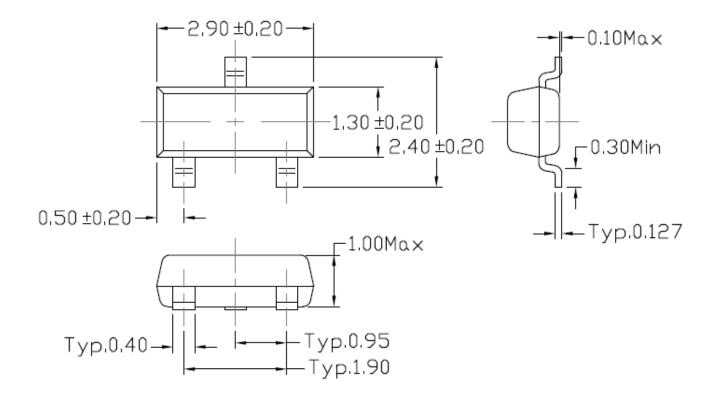


### Figure 111: Switching Time Waveform



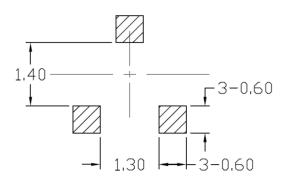


# Package Dimension (SOT-23)



Note: Dimensions in mm

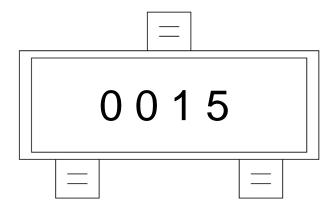
# Recommended pad layout for surface mount leadform



Note: Dimensions in mm



# **Marking Information**



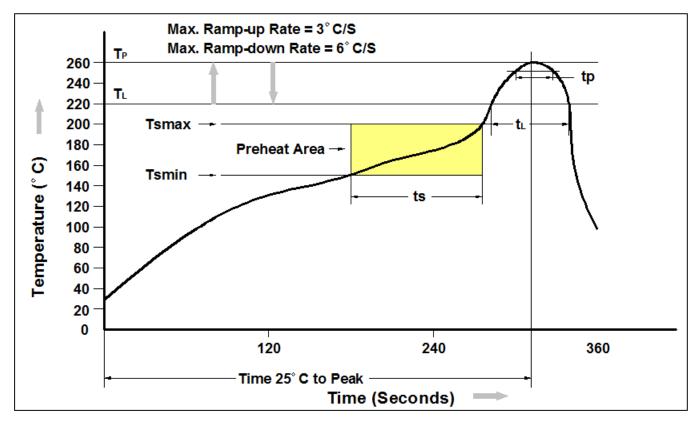
0015: Device Number

# **Ordering Information**

Part Number	Description	Quantity
CTL0015PS-R3	SOT-23 Reel	3000 pcs



## **Reflow Profile**



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds
Ramp-up Rate (t∟ to tթ)	3°C/second max.
Liquidous Temperature (TL)	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds
Ramp-down Rate $(T_P \text{ to } T_L)$	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.



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