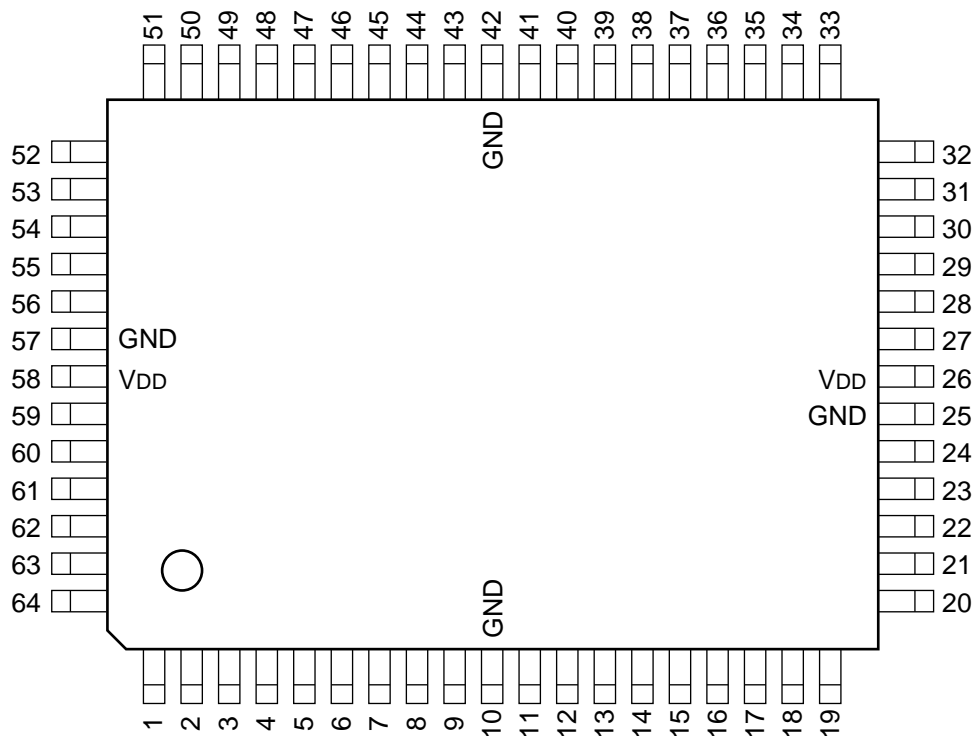


C-MOS MEMORY SEQUENCER (WRITE V CONTROL, VISC CONTROL)

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	WCK	17	O	BIDDIR	33	O	VISC RST	49	I	RSH
2	I	TIN0	18	O	CNTV	34	O	FCP	50	O	SQV
3	I	DIAG1	19	I/O	YBID	35	O	VISC DET	51	I	YGH
4	I	DIAG2	20	O	YGINC	36	I	V SHIFT	52	I	\overline{DT}
5	I	DIAG3	21	I	YINC	37	I	H SHIFT	53	I	\overline{NOS}
6	I	TMOD	22	I	RZ	38	I	VISC CONT	54	I	CWZ
7	I	RVD	23	I	HCK	39	I	PB O/ \overline{E}	55	I	DTV
8	I	REF O/ \overline{E}	24	I	READ CONT	40	I	CINC	56	I	SVV
9	I	SWP	25	—	GND	41	I	CGINC	57	—	GND
10	—	GND	26	—	VDD	42	—	GND	58	—	VDD
11	O	PB2H	27	O	\overline{NORPB}	43	I	MODE	59	I	\overline{CLR}
12	I/O	\overline{CBID}	28	I	$\overline{VISC EX}$	44	O	W O/ \overline{E}	60	O	WV1
13	O	SYM \overline{X}	29	O	SOUT	45	O	TOT1	61	O	WV2
14	O	PSW	30	I	SCK	46	O	TOT2	62	I	SHW
15	O	TOT0	31	I	SIN	47	O	XINS	63	I	YWZ
16	I	PB V	32	I	STB	48	I	\overline{FRB}	64	O	GSHW

21	YINC	YGING	20
40	CINC	CGING	41
16	PB V	PB2H	11
1	WCK		
		W O/E	44
56	SVV	WV 1	60
		WV 2	61
9	SWP	SYMX	13
48	FRB	PSW	14
49	RSH	FCP	34
		XINS	47
24	READ CONT		
38	VISC CONT		
55	DTV	SQV	50
52	DT		
36	V SHIFT	NORPB	27
37	H SHIFT	VISC RST	33
28	VISC EX	VISC DET	35
8	REF O/E		
39	PB O/E		
23	HCK		
22	RZ	YBID	19
63	YWZ	CBID	12
54	CWZ	BIDDIR	17
7	RVD	CNTV	18
62	SHW	GSHW	64
43	MODE		
3	DIAG 1		
4	DIAG 2		
5	DIAG 3		
59	CLR		
53	NOS		
51	YGH		
32	STB		
31	SIN	SOUT	29
30	SCK		
		TOT 0	15
2	TIN 0	TOT 1	45
6	TMOD	TOT 2	46

INPUT

- CINC ; C INC0
- CLR ; CLEAR
- CWZ ; WRITE ZERO OF CHROMA SYSTEM
- DIAG 1 - 3 ; INPUT FOR DIAGNOSIS
- DT ; DT PLAYBACK DESIGNATE
- DTV ; DT V
- FRB ; FAST REVERSE BIDIREX
- HCK ; REFERENCE CLOCK (13.5MHz)
- H SHIFT ; H SHIFT ENABLE
- MODE ; L ; 625 / H ; 525 MODE
- NOS ; NO SIGNAL
- PB O/E ; PLAYBACK ODD/EVEN
- PB V ; PLAYBACK V
- READ CONT ; READ CONTROL (FROM CXD206-104Q)
- REF O/E ; REFERENCE ODD/EVEN
- RSH ; RETURN SH
- RVD ; RETURN VD
- RZ ; READ ZERO
- SCK ; MPU SERIAL INTERFACE CLOCK
- SHW ; SH WINDOW
- SIN ; MPU SERIAL DATA IN
- STB ; MPU SERIAL INTERFACE STROBE
- SVV ; SERVO V
- SWP ; SWITCHING PULSE
- TIN0 ; TEST IN
- TMOD ; TEST MODE
- WCK ; WRITE CLOCK
- VISC CONT ; VISC CONTROL (FROM CXD206-104Q)
- VISC EX ; PLAYBACK VISC EXIST
- V SHIFT ; V SHIFT ENABLE
- YINC ; Y INC0
- YGH ; Y GATED H
- YWZ ; WRITE ZERO OF Y SYSTEM

OUTPUT

- BIDDIR ; DIRECTION CONTROL (H ; FORWARD/L ; REVERSE)
- CGINC ; C GATED INC0
- CNTV ; COUNTER V
- FCP ; FIELD CLAMP PULSE
- GSHW ; GATED SH WINDOW
- NORPB ; DETECTION OUT OF DT/NORMAL PLAYBACK
- PB2H ; PLAYBACK 2H
- PSW ; QUASI-SWITCHING PULSE
- SOUT ; MPU SERIAL DATA OUT
- SQV ; V FOR MEMORY CONTROL
- SYMX ; SYNC MIX CONTROL PULSE
- TOT0 - 2 ; TEST OUT
- VISC DET ; VISC PHASE COMPARATOR ENABLE
- VISC RET ; VISC CONTROL RESET PULSE
- W O/E ; WRITE ODD/EVEN
- WV 1, 2 ; WRITE V1, V2
- XINS ; CRYSTAL INSERT PULSE
- YGING ; Y GATED INC0

INPUT/OUTPUT

- CBID ; CHROMA SYSTEM BIDIREX
- YBID ; Y SYSTEM BIDIREX

