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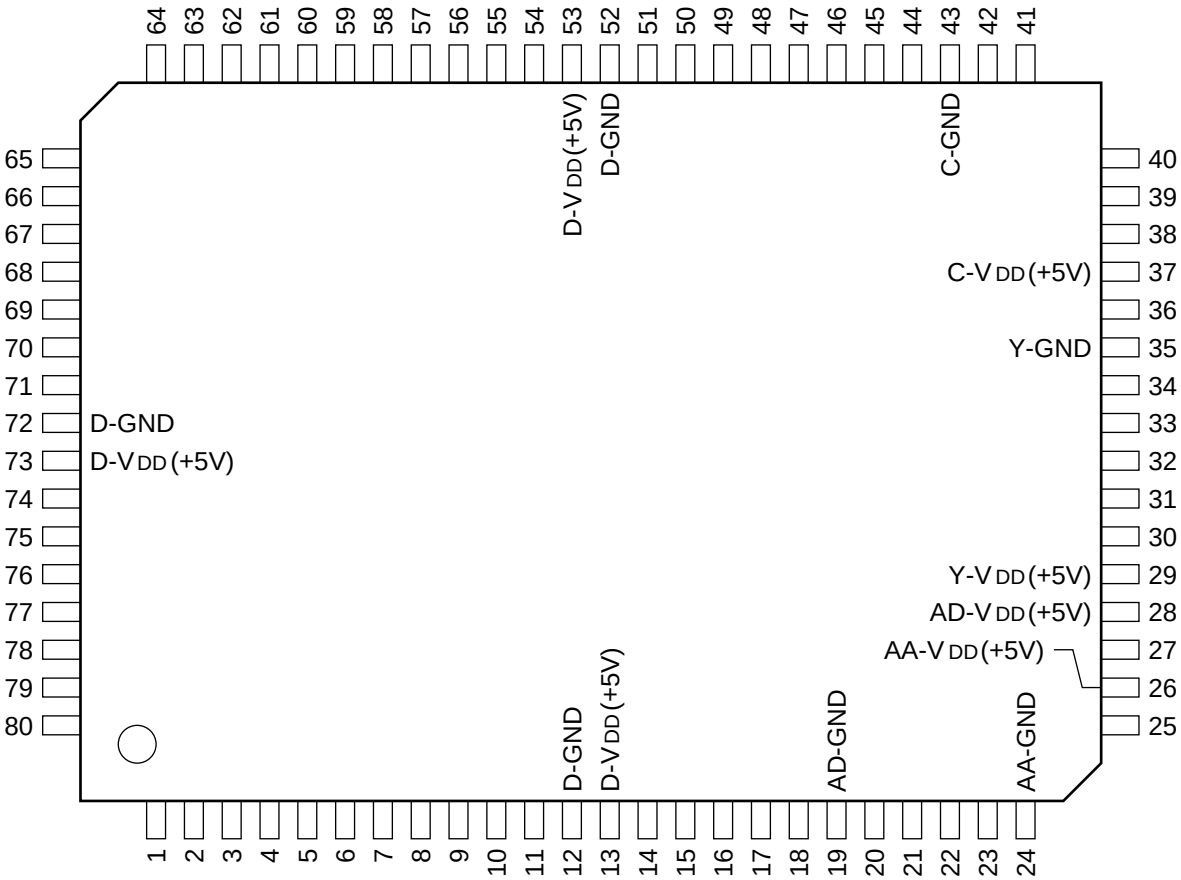
IL08

C-MOS DIGITAL COMB FILTER (NTSC/PAL)

- TOP VIEW -



(V_{DD} =+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	VI8	21	I	CRV	41	I	CVRF	61	O	C6
2	I	VI7	22	O	RB	42	O	CIRF	62	O	C5
3	I	VI6	23	–	GR	43	–	C-GND	63	O	C4
4	I	VI5	24	–	AA-GND	44	O	YA	64	O	C3
5	I	VI4	25	I	ADIN	45	O	Y9	65	O	C2
6	I	VI3	26	–	AA-V_{DD}	46	O	Y8	66	O	C1
7	I	VI2	27	O	RT	47	O	Y7	67	I	XCOE
8	I	VI1	28	–	AD-V_{DD}	48	O	Y6	68	I	APCN
9	I	ADC0	29	–	Y-V_{DD}	49	O	Y5	69	I	RATI
10	I	INSL	30	I	XAYO	50	O	Y4	70	I	NTPL
11	I	OCLK	31	O	AYO	51	O	Y3	71	I	TST
12	–	D-GND	32	O	YVG	52	–	D-GND	72	–	D-GND
13	–	D-V_{DD}	33	I	YVRF	53	–	D-V_{DD}	73	–	D-V_{DD}
14	O	CLK0	34	O	YIRF	54	O	Y2	74	I	PNR
15	I	MCK	35	–	Y-GND	55	O	Y1	75	I	TEST
16	I	ADCK	36	O	VB	56	I	XYOE	76	I	TEST
17	I	CLPI	37	–	C-V_{DD}	57	O	CA	77	I	TEST
18	I	XCPON	38	I	XACO	58	O	C9	78	I	TEST
19	–	AD-GND	39	O	ACO	59	O	C8	79	I	BPF
20	I	ICP	40	O	CVG	60	O	C7	80	I	TEST

INPUT

ADCK ; CLOCK FOR A/D CONVERTER

ADCO ; A/D CONVERTER OUTPUT SELECT
(H : DIGITAL OUTPUT MODE, L : STANDARD MODE)

ADIN ; COMB FILTER ANALOG DATA

APCN ; APERTURE COMPENSATION
(H : FREQUENCY RESPONSE DEGRADATION COMPENSATE BY APERTIRE EFFECT,
L : STANDARD)

BPF ; Y/C SEPARATE PROCESS MODE SETTING
(H : BPF SEPARATE MODE, L : ADAPTABILITY PROCESS MODE)

CLPI ; CLAMP PULSE FOR A/D CONVERTER

CRV ; CLAMP REFERENCE VOLTAGE

CVRF ; FULL SCALE VALUE SETTING OF ANALOG CHROMA SIGNAL

ICP ; VOLTAGE INTERGRATION FOR CLAMP CONTROL

INSL ; INPUT DATA SELECT OF COMB FILTER (H : DIGITAL INPUT, L : ANALOG INPUT)

MCK ; MASTER CLOCK

NTPL ; NTSC/PAL MODE SETTING (H : PAL, L : NTSC)

OCLK ; CLOCK AMPLIFIER

PNR ; DOT INTERFERENCE (PAL H : MINIMUM, L : BEFORE IMPROVEMENT
NTSC : L FIXED)

RATI ; RATIO SETTING (H : PAL (WHEN THE PNR IS ON, SET TO L FORCED), L : NTSC)

TEST ; TEST (NORMAL : L FIXED)

TST ; Y OUTPUT THROUGH MODE
(H : COMPOSITE VIDEO SIGNAL (TO AYO, YA-Y1) AND Y/C SEPARATED
CHROMA SIGNAL (TO ACO, CA-C1), L : Y-C SEPARATION MODE)

VI1-VI8 ; DIGITAL DATA

XACO ; ANALOG CHROMA SIGNAL REVERSE CURRENT (CONNECTED TO C-GND)

XAYO ; ANALOG Y SIGNAL REVERSE CURRENT (CONNECTED TO Y-GND)

XCOE ; DIGITAL CHROMA SIGNAL OUTPUT CONTROL
(H : HIGH IMPEDANCE, L : STANDARD OUTPUT)

XCPON ; CLAMP SETTING FOR A/D CONVERTER
(H : A/D CONVERTER CAPABILITY, L : CLAMP CAPABILITY)

XYOE ; DIGITAL Y SIGNAL OUTPUT CONTROL
(H : HIGH IMPEDANCE, L : STANDARD OUTPUT)

YVRF ; FULL SCALE VALUE SETTING OF ANALOG Y SIGNAL

OUTPUT

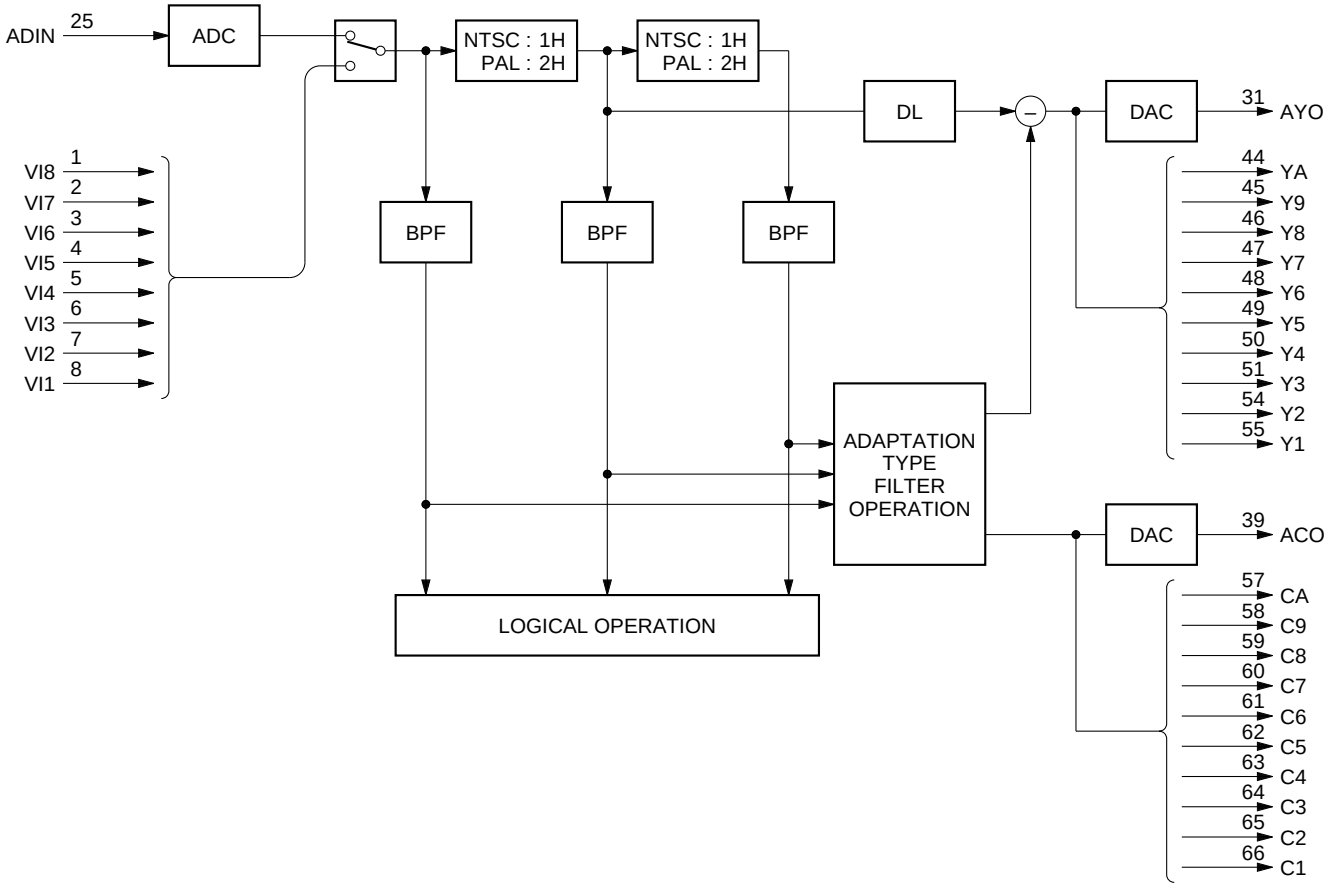
ACO ; ANALOG CHROMA SIGNAL
AYO ; ANALOG Y SIGNAL
C1-C9 ; DIGITAL CHROMA SIGNAL
CA ; DIGITAL CHROMA SIGNAL
CIRF ; EXTERNAL RESISTOR CONNECTION
CLKO ; CLOCK AMPLIFIER
CVG ; EXTERNAL CAPACITOR CONNECTION
RB ; STANDARD VALUE (+0.5V) OF REFERENCE VOLTAGE (BOTTOM)
RT ; STANDARD VALUE (+2.6V) OF REFERENCE VOLTAGE (TOP)
VB ; EXTERNAL CAPACITOR
Y1-Y9 ; DIGITAL Y SIGNAL
YA ; DIGITAL Y SIGNAL
YIRF ; EXTERNAL RESISTOR CONNECTION
YVG ; EXTERNAL CAPACITOR CONNECTION

V_{DD} (SUPPLY VOLTAGE=+5V)

AA-V_{DD} ; ANALOG SUPPLY VOLTAGE FOR A/D CONVERTER
AD-V_{DD} ; DIGITAL SUPPLY VOLTAGE FOR A/D CONVERTER
C-V_{DD} ; ANALOG SUPPLY VOLTAGE FOR D/A CONVERTER (CHROMA)
D-V_{DD} ; DIGITAL SUPPLY VOLTAGE
Y-V_{DD} ; ANALOG SUPPLY VOLTAGE FOR D/A CONVERTER (Y)

GND

AA-GND ; ANALOG GND FOR A/D CONVERTER
AD-GND ; DIGITAL GND FOR A/D CONVERTER
C-GND ; ANALOG GND FOR D/A CONVERTER (CHROMA)
D-GND ; DIGITAL GND
GR ; GARD RING (CONNECTED TO AA-GND)
Y-GND ; ANALOG GND FOR D/A CONVERTER (Y)


ADIN
25
ADC
NTSC : 1H
PAL : 2H
NTSC : 1H
PAL : 2H
DL
DAC
31
AYO
VI8
1
VI7
2
VI6
3
VI5
4
VI4
5
VI3
6
VI2
7
VI1
8
BPF
BPF
BPF
44
YA
45
Y9
46
Y8
47
Y7
48
Y6
49
Y5
50
Y4
51
Y3
54
Y2
55
Y1
ADAPTATION
TYPE
FILTER
OPERATION
DAC
39
ACO
LOGICAL OPERATION
57
CA
58
C9
59
C8
60
C7
61
C6
62
C5
63
C4
64
C3
65
C2
66
C1