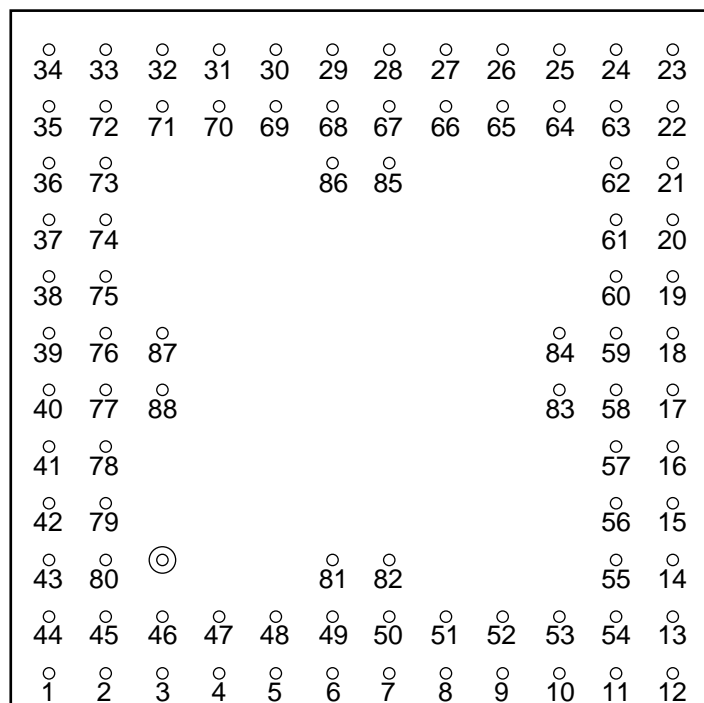


C-MOS GATE ARRAY

-BOTTOM VIEW-



(VDD = +5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	IFDD	23	—	NC	45	I	SL0C	67	O	EXTH
2	I	IHDD	24	I	RS5F	46	I	SL2D	68	O	OHD
3	I	SL1D	25	I	ILK	47	I	CK13D	69	O	OVD
4	I	SL0D	26	I	I48K	48	O	FCNT0	70	O	OFD
5	O	FCNT1	27	I	IFDB	49	O	FCNT2	71	O	O48K
6	O	FCNT3	28	I	IHDB	50	O	FCNT4	72	O	O61M
7	O	FCNT5	29	I	ICSY	51	O	FCNT6	73	O	OSTP
8	O	FCNT7	30	O	OFTP	52	I	TSTA	74	O	OHTP
9	O	FCNT8	31	I	SLCN	53	I	TSTB	75	I	CK27M
10	O	FCNT9	32	O	OREFB	54	O	SCNT2	76	O	O5FR
11	O	SCNT0	33	O	OVALB	55	O	SCNT4	77	O	CK90M
12	—	GND	34	—	GND	56	—	NC	78	O	OFRM
13	O	SCNT1	35	O	OREFA	57	O	SCNT7	79	I	CK13C
14	O	SCNT3	36	O	OVALA	58	O	SCNT9	80	I	SL1C
15	O	SCNT5	37	O	CK15M	59	O	SCNTB	81	—	GND
16	O	SCNT6	38	O	CK22M	60	O	SCNTD	82	—	VDD
17	O	SCNT8	39	O	CK67M	61	I	CK12M	83	—	VDD
18	O	SCNTA	40	O	CK13M	62	I	MD56	84	—	GND
19	O	SCNTC	41	O	OHDA	63	I	TST1	85	—	GND
20	I	SLVA	42	I	IFDC	64	O	LOCKA	86	—	VDD
21	I	RSLA	43	I	IHDC	65	O	LOCKB	87	—	VDD
22	I	RSLB	44	I	SL2C	66	O	OMD	88	—	GND

		OREFA	35
29	ICSY	OVALA	36
		OREFB	32
25	ILK	OVALB	33
		CK13M	40
61	CK12M	CK15M	37
75	CK27M	CK22M	38
		CK67M	39
27	IFDB	CK90M	77
28	IHDB		
		OFTP	30
42	IFDC	OSTP	73
43	IHDC	OHTP	74
79	CK13C		
		O5FR	76
45	SL0C	OFRM	78
80	SL1C	O48K	71
44	SL2C	O61M	72
		LOCKA	64
1	IFDD	LOCKB	65
2	IHDD		
47	CK13D	EXTH	67
		OMD	66
4	SL0D	OVD	69
3	SL1D	OFD	70
46	SL2D	OHD	68
		OHDA	41
21	RSLA		
22	RSLB	FCNT0	48
		FCNT1	5
24	RS5F	FCNT2	49
		FCNT3	6
62	MD56	FCNT4	50
		FCNT5	7
20	SLVA	FCNT6	51
31	SLCN	FCNT7	8
		FCNT8	9
26	I48K	FCNT9	10
		SCNT0	11
52	TSTA	SCNT1	13
53	TSTB	SCNT2	54
		SCNT3	14
63	TST1	SCNT4	55
		SCNT5	15
		SCNT6	16
		SCNT7	57
		SCNT8	17
		SCNT9	58
		SCNTA	18
		SCNTB	59
		SCNTC	19
		SCNTD	60

INPUT

CK12M ; AUDIO 12.288MHz CLOCK
 CK13C ; CLOCK FOR REFERENCE INPUT C
 CK13D ; CLOCK FOR REFERENCE INPUT D
 CK27M ; VIDEO 27.0MHz CLOCK
 I48K ; AUDIO 48kHz INPUT
 ICSY ; COMPOSITE SYNC INPUT
 IFDB ; FRAME PULSE OF REFERENCE INPUT B
 IFDC ; FRAME PULSE OF REFERENCE INPUT C
 IFDD ; FRAME PULSE OF REFERENCE INPUT D
 IHDB ; H SYNC PULSE OF REFERENCE INPUT B
 IHDC ; H SYNC PULSE OF REFERENCE INPUT C
 IHDD ; H SYNC PULSE OF REFERENCE INPUT D
 ILK ; INHIBITION OF AUDIO PLL REFERENCE
 MD56 ; 525 / 625 SYSTEM MODE
 RS5F ; AUDIO 5 FRAME RESET INPUT
 RSLA, RSLB ; REFERENCE INPUT SELECT CONTROL
 SL0C – SL2C ; DELAY CONTROL OF REFERENCE INPUTS C
 SL0D – SL2D ; DELAY CONTROL OF REFERENCE INPUTS D
 SLCN ; SEGMENT / HORIZONTAL COUNT SELECT CONTROL
 SLVA ; VIDEO / AUDIO REFERENCE SELECT
 TST1 ; TEST USE
 TSTA, TSTB ; TEST USE

OUTPUT

CM13M ; 13.5MHz CLOCK
 CK15M ; 1.5MHz CLOCK
 CK22M ; 2.25MHz CLOCK
 CK67M ; 6.75MHz CLOCK
 CK90M ; 9.0MHz CLOCK
 EXTH ; REFERENCE INPUT DETECT
 FCNT0 – FCNT9 ; FRAME COUNT OUTPUTS (FCNT9 : MSB)
 LOCK A, LOCK B ; LOCK STATE OUTPUTS OF PLL A (VIDEO), PLL B (AUDIO)
 O48K ; 48kHz OUTPUT
 O5FR ; AUDIO 5 FRAME OUTPUT
 O61M ; 6.144MHz OUTPUT
 OFD ; FRAME PULSE OF SELECTED REFERENCE
 OFRM ; FRAME PULSE
 OFTP ; FRAME TOP PULSE
 OHD ; H SYNC PULSE OF SELECTED REFERENCE
 OHDA ; H SYNC PULSE
 OHTP ; H SYNC TOP PULSE
 OMD ; DETECTED 525 / 625 SYSTEM MODE
 OREFA, OREFB ; REFERENCE PULSE OF PLL A, PLL B
 OSTP ; SEGMENT / HORIZONTAL COUNT TOP PULSE
 OVALA, OVALB ; VARIABLE PULSE OF PLL A, PLL B
 OVD ; V PULSE OF COMPOSITE SYNC INPUT
 SCNT0 – SCNT9 ; SEGMENT / HORIZONTAL COUNTER OUTPUTS
 SCNTA – SCNTD ; SEGMENT / HORIZONTAL COUNTER OUTPUTS (SCNTD : MSB)