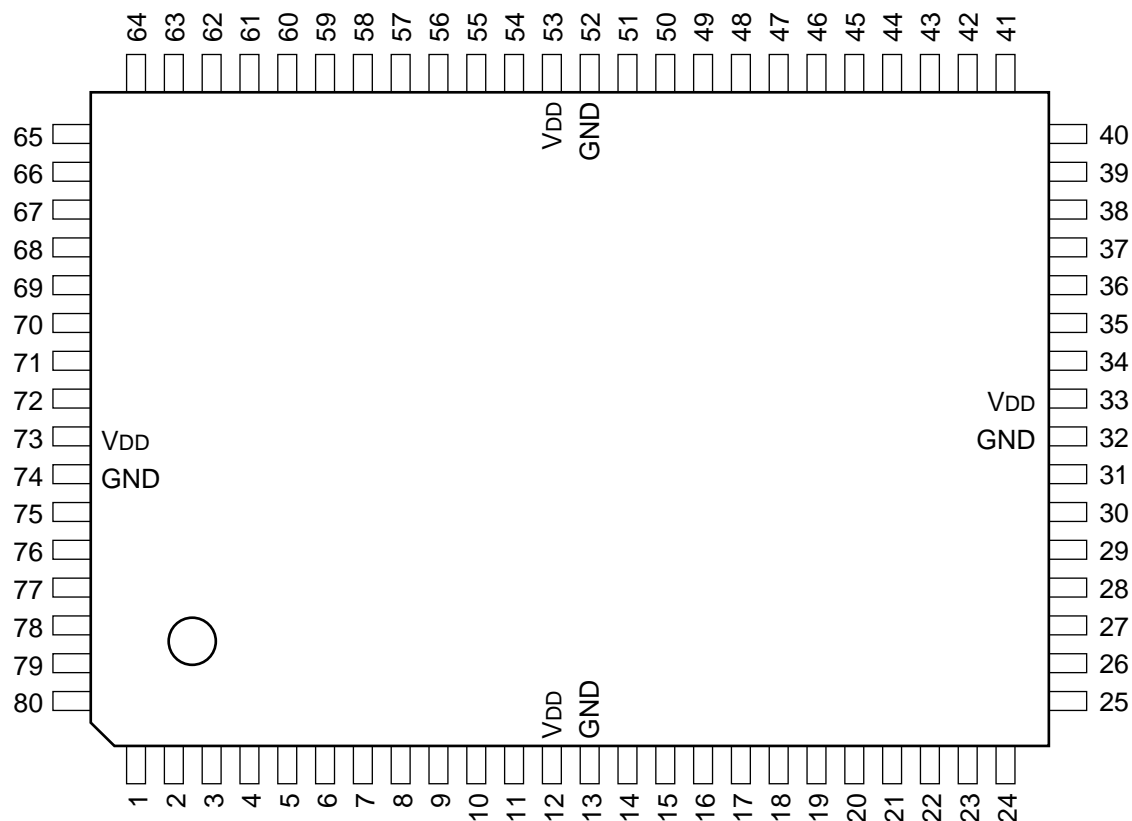


WIPE MIXER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	A0	21	I	WCS	41	I	MODE2	61	O	P15
2	I/O	K14	22	I	CKX	42	I	MODE1	62	O	P16
3	I/O	K13	23	I	CKD	43	I	MODE0	63	I	OE
4	I/O	K12	24	I	B14	44	O	P0	64	I	A14
5	I/O	K11	25	I	B13	45	O	P1	65	I	A13
6	I/O	K10	26	I	B12	46	O	P2	66	I	A12
7	I/O	K9	27	I	B11	47	O	P3	67	I	A11
8	I/O	K8	28	I	B10	48	O	P4	68	I	A10
9	I/O	K7	29	I	B9	49	O	P5	69	I	A9
10	I/O	K6	30	I	B8	50	O	P6	70	I	A8
11	I/O	K5	31	I	B7	51	O	P7	71	I	A7
12	—	VDD	32	—	GND	52	—	GND	72	I	CK
13	—	GND	33	—	VDD	53	—	VDD	73	—	VDD
14	I/O	K4	34	I	B6	54	O	P8	74	—	GND
15	I/O	K3	35	I	B5	55	O	P9	75	I	A6
16	I/O	K2	36	I	B4	56	O	P10	76	I	A5
17	I/O	K1	37	I	B3	57	O	P11	77	I	A4
18	I/O	K0	38	I	B2	58	O	P12	78	I	A3
19	O	TST0	39	I	B1	59	O	P13	79	I	A2
20	I	SDAT	40	I	B0	60	O	P14	80	I	A1

INPUTS

A0 - A14 ; 15-BIT DIGITAL IN A (2'S COMPLEMENT)
 B0 - B14 ; 15-BIT DIGITAL IN B (2'S COMPLEMENT)
 CK ; SYSTEM CLOCK
 CKD ; SERIAL INTERFACE CLOCK
 CKX ; SWITCHING TIMING PULSE
 MODE0 - MODE2 ; MODE SELECT 0-2
 OE ; OUTPUT ENALBE (LOW: ENABLE)
 SDAT ; SERIAL DATA
 WCS ; WRITE CHIP SELECT (LOW: WRITE)

OUTPUTS

P0 - P16 ; 17-BIT DIGITAL (2'S COMPLEMENT)
 TSTO ; TEST

INPUTS/OUTPUTS

K0 - K14 ; 15-BIT DIGITAL (2'S COMPLEMENT)

