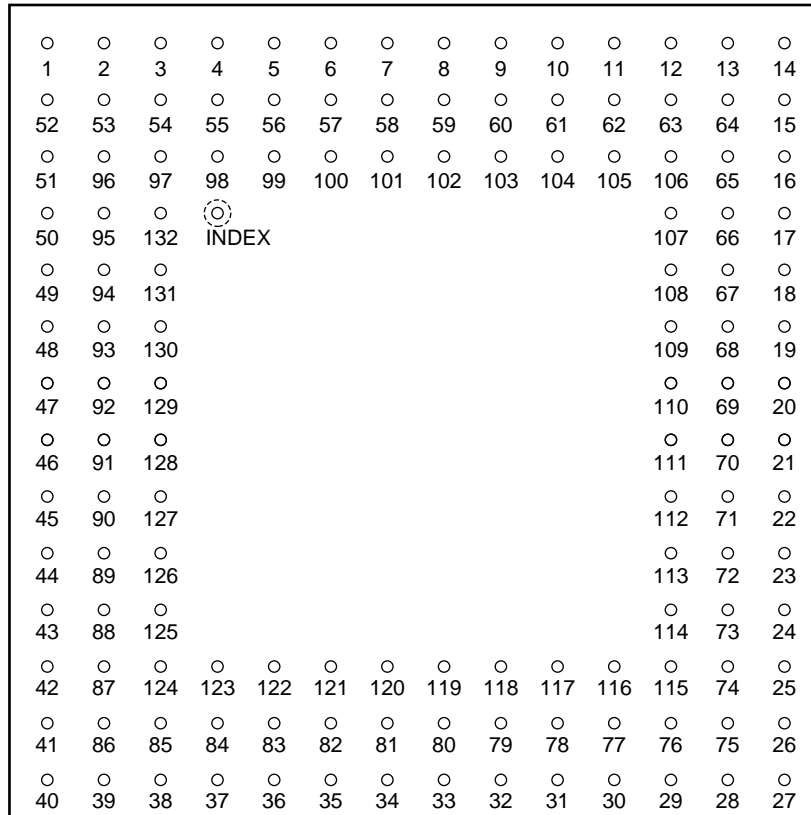


**C-MOS 4 : 2 : 2 COMPONENT DIGITAL VIDEO
PARALLEL INTERFACE ENCODER
-BOTTOM VIEW-**



V_{DD} = +5V

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	-	NC	34	I	TUC1	67	I	ANB4	100	O	ANEH
2	I	MA1	35	I	TUC2	68	I	ANB2	101	-	GND
3	I	CR8	36	I	INCH	69	I	ANB0	102	-	GND
4	I	CR6	37	I	SLNP	70	I	VC20	103	O	MUD0
5	I	CR4	38	I	SLET	71	I	VC22	104	O	MUD1
6	I	CR2	39	I	VDCS	72	O	MUD7	105	-	GND
7	I	CR0	40	I	COEN	73	O	MUD8	106	-	GND
8	I	ANA8	41	I	LIOF	74	O	MUD9	107	O	MUD2
9	I	ANA6	42	I	Y8	75	-	GND	108	O	MUD3
10	I	ANA4	43	I	Y6	76	I	TSLF	109	O	MUD4
11	I	ANA2	44	I	Y4	77	I	TSST	110	-	GND
12	I	ANA0	45	I	Y2	78	I	TSRT	111	-	GND
13	I	ANSL	46	I	Y0	79	I	EXTH	112	O	MUD5
14	-	NC	47	I	CB8	80	I	EXTF	113	O	MUD6
15	I	ANIS	48	I	CB6	81	I	INEH	114	-	V _{DD}
16	I	ANB9	49	I	CB4	82	I/O	CM7	115	-	GND
17	I	ANB7	50	I	CB2	83	I/O	CM5	116	O	OC27
18	I	ANB5	51	I	CB0	84	I/O	CM3	117	I/O	CM9
19	I	ANB3	52	I	MA2	85	I	CK27	118	I/O	CM8
20	I	ANB1	53	-	GND	86	-	V _{DD}	119	-	GND
21	I	VC21	54	I	CR9	87	I	Y9	120	-	GND
22	I	VC23	55	I	CR7	88	I	Y7	121	I/O	CM6
23	I	VC10	56	I	CR5	89	I	Y5	122	I/O	CM4
24	I	VC11	57	I	CR3	90	I	Y3	123	-	GND
25	I	VC12	58	I	CR1	91	I	Y1	124	-	GND
26	I	VC13	59	I	ANA9	92	I	CB9	125	I/O	CM2
27	-	NC	60	I	ANA7	93	I	CB7	126	I/O	CM1
28	I	LDAT	61	I	ANA5	94	I	CB5	127	I/O	CM0
29	I	TSCK	62	I	ANA3	95	I	CB3	128	-	GND
30	I	TSLD	63	I	ANA1	96	I	CB1	129	-	GND
31	I	TUB1	64	-	V _{DD}	97	-	GND	130	O	MFP
32	I	TUB2	65	I	ANB8	98	O	OC6	131	O	OC13
33	I	TUS	66	I	ANB6	99	O	ANEV	132	-	V _{DD}

46	Y0	VC10	83
45	Y1	VC11	84
44	Y2	VC12	85
43	Y3	VC13	86
42	Y4	VC20	70
41	Y5	VC21	71
40	Y6	VC22	72
39	Y7	VC23	73
38	Y8	MA1	82
37	Y9	MA2	83
36	CB0	INCH	86
35	CB1	COEN	80
34	CB2	INEN	81
33	CB3	SLNP	37
32	CB4	SLET	38
31	CB5	VDCS	89
30	CB6	LIOF	41
29	CB7		
28	CB8		
27	CB9		
26	CR0		
25	CR1		
24	CR2		
23	CR3		
22	CR4		
21	CR5		
20	CR6		
19	CR7		
18	CR8		
17	CR9		
16	ANA0		
15	ANA1		
14	ANA2		
13	ANA3		
12	ANA4		
11	ANA5		
10	ANA6		
9	ANA7		
8	ANA8		
7	ANA9		
6	ANB0		
5	ANB1		
4	ANB2		
3	ANB3		
2	ANB4		
1	ANB5		
0	ANB6		
	ANB7		
	ANB8		
	ANB9		
13	ANSL		
15	ANIS		
85	CK27		
80	EXTF		
79	EXTH		
31	TUB1		
32	TUB2		
33	TUS		
34	TUC1		
35	TUC2		
29	LDAT		
29	TSCK		
30	TSLD		
76	TSLF		
77	TSST		
78	TSRT		

INPUT

ANA0-ANA9	; ANCILLARY SIGNAL (13.5MHz 20 BITS OR 27MHz 10 BITS)
ANB0-ANB9	; ANCILLARY SIGNAL (13.5MHz 20 BITS)
ANIS	; ANC SIGNAL INSERT SELECTOR
ANSL	; ANC 10 BITS/20 BITS SIGNAL SELECTOR
CB0-CB9	; Cb OR Cb • Cr MPX SIGNAL
CK27	; 27MHz CLOCK
COEN	; CONNECT WITH "+5V"
CR0-CR9	; Cr OR Cb • Cr MPX SIGNAL (INPUT CHECK)
EXTF	; REFERENCE F
EXTH	; REFERENCE H
INEH	; CONNECT WITH "+5V"
INCH	; CONNECT WITH "+5V"
TUB1,YUB2	; CONNECT WITH "GND"
TUS,TUC1,TUC2	; CONNECT WITH "GND"
LDAT,TSCK,TSLD	; FOR TEST
LIOF	; DATA LIMIT SELECTOR (ON/OFF)
MA1,MA2	; ROUND MODE SELECTOR (ROUND, RAISE, CUT OFF, THROUGH)
SLET	; DATA SELECTOR (8 BITS OR 10 BITS)
SLNP	; 525 LINE/625 LINE SELECTOR
TSLF,TSST,TSRT	; FOR TEST
VC10-VC13	; 525/60 (FIELD1) V BLANKING CONTROL
VC20-VC23	; 525/60 (FIELD2) V BLANKING CONTROL
VDCS	; CHROMA SIGNAL SELECTOR (Cb • Cr MPX/Cb, Cr)
Y0-Y9	; Y SIGNAL

OUTPUT

ANEH	; DATA INTERVAL SIGNAL ("HIGH" AT BETWEEN EAV AND SAV)
ANEV	; DATA VALID SIGNAL ("HIGH" AT THE DATA VALID PERIOD IN V BLK INTERVAL)
CM0-CM9	; F, H COUNTER
MFP	; POSITION OF F COUNTER VALUE (MULTIPLEX SIGNAL)
MUD0-MUD9	; MULTIPLEX DATA
OC6	; 6.75MHz CLOCK
OC13	; 13.5MHz CLOCK
OC27	; 27MHz CLOCK

