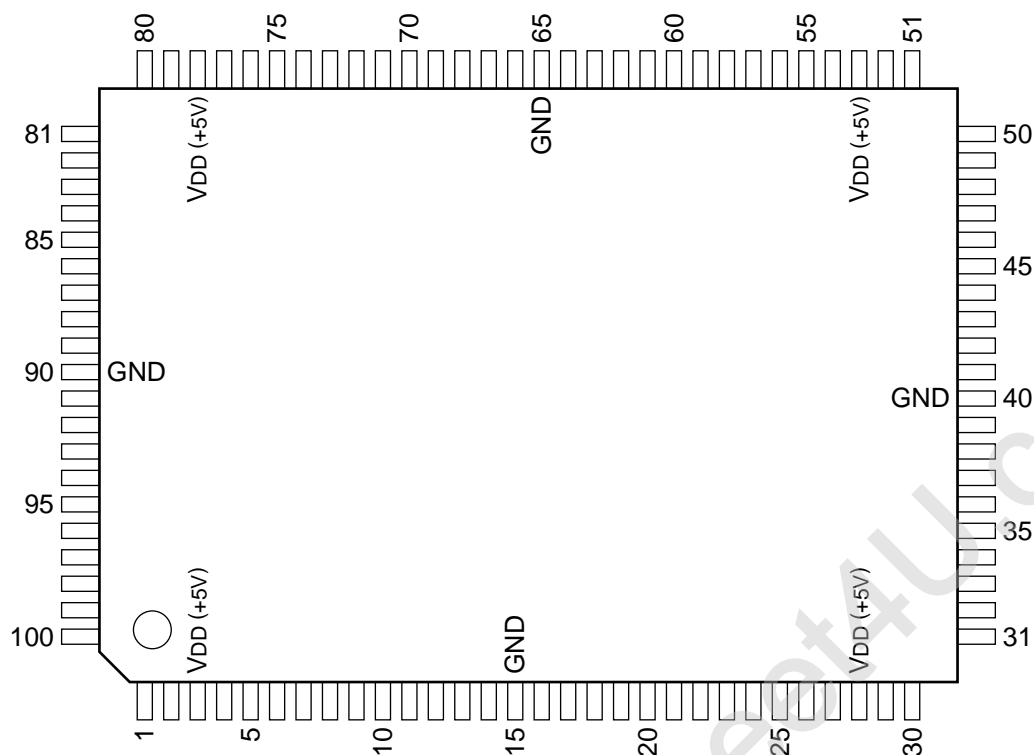

C-MOS DIGITAL MIX EFFECTS

-TOP VIEW-



(VDD = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	CCF	21	I	YC1	41	I	CLK	61	O	CQ7	81	I	CA3
2	I	CC7	22	I	YC2	42	I	YB7	62	O	CQ6	82	I	CA2
3	—	VDD	23	I	YC3	43	I	YBS	63	O	CQ5	83	I	CA1
4	I	CC6	24	I	YC4	44	I	YA0	64	O	CQ4	84	I	CA0
5	I	CC5	25	I	YC5	45	I	YA1	65	—	GND	85	I	CBS
6	I	CC4	26	I	YC6	46	I	YA2	66	O	CQ3	86	I	CB7
7	I	CC3	27	I	YC7	47	I	YA3	67	O	CQ2	87	I	CB6
8	I	CC2	28	—	VDD	48	I	YA4	68	O	CQ1	88	I	CB5
9	I	CC1	29	I	YCS	49	I	YA5	69	O	CQ0	89	I	CB4
10	I	CC0	30	I	YCE	50	I	YA6	70	I	D4	90	—	GND
11	O	YQ0	31	I	YCP	51	I	YA7	71	I	D3	91	I	CB3
12	O	YQ1	32	I	DDL	52	I	YAS	72	I	D2	92	I	CB2
13	O	YQ2	33	I	YB0	53	—	VDD	73	I	D1	93	I	CB1
14	O	YQ3	34	I	YB1	54	I	A1	74	I	D0	94	I	CB0
15	—	GND	35	I	YB2	55	I	A0	75	I	CAS	95	I	CDRS
16	O	YQ4	36	I	YB3	56	I	WE1	76	I	CA7	96	I	CCG
17	O	YQ5	37	I	YB4	57	I	WE0	77	I	CA6	97	I	CCP
18	O	YQ6	38	I	YB5	58	I	D7	78	—	VDD	98	I	CCE
19	O	YQ7	39	I	YB6	59	I	D6	79	I	CA5	99	I	CCS
20	I	YC0	40	—	GND	60	I	D5	80	I	CA4	100	I	CCRS

	58	59	60	70	71	72	73	74	54	55	56	57	41				
51	YA7	D7	D6	D5	D4	D3	D2	D1	D0	A1	A0	WE1	WE0	CLK			
50	YA6														CA7	76	
49	YA5														CA6	77	
48	YA4														CA5	79	
47	YA3														CA4	80	
46	YA2														CA3	81	
45	YA1														CA2	82	
44	YA0														CA1	83	
42	YB7														CA0	84	
39	YB6														CB7	86	
38	YB5														CB6	87	
37	YB4														CB5	88	
36	YB3														CB4	89	
35	YB2														CB3	91	
34	YB1														CB2	92	
33	YB0														CB1	93	
27	YC7														CB0	94	
26	YC6														CC7	2	
25	YC5														CC6	4	
24	YC4														CC5	5	
23	YC3														CC4	6	
22	YC2														CC3	7	
21	YC1														CC2	8	
20	YC0														CC1	9	
															CC0	10	
52	YAS																19
43	YBS														YQ7		18
29	YCS														YQ6		17
75	CAS														YQ5		16
85	CBS														YQ4		14
99	CCS														YQ3		13
31	YCP														YQ2		12
97	CCP														YQ1		11
															YQ0		
30	YCE																61
98	CCE														CQ7		62
95	CDRS														CQ6		63
32	DDL														CQ5		64
1	CCF														CQ4		66
96	CCG														CQ3		67
100	CCRS														CQ2		68
															CQ1		69
															CQ0		

INPUT

A0, A1 ; INTERNAL REGISTER ADDRESS
CA0 - CA7 ; MIXER (C) A DATA
CAS ; MIXER (C) A DATA SELECTOR
CB0 - CB7 ; MIXER (C) B DATA
CBS ; MIXER (C) B DATA SELECTOR
CC0 - CC7 ; MIXER (C) C DATA
CCE ; MIXER (C) C ENABLE
CCF ; MIXER (C) PRE FILTER CONTROL
CCG ; MIXER (C) C GATE
CCP ; MIXER (C) C POLARITY
CCRS ; MIXER (C) C REGISTER SELECTOR
CCS ; MIXER (C) C DATA SELECTOR
CDRS ; C DATA REGISTER SELECTOR
CLK ; CLOCK
D0 - D7 ; INTERNAL REGISTER DATA
DDL ; DATA DELAY CONTROL
WE0, WE1 ; INTERNAL REGISTER WRITE ENABLE
YA0 - YA7 ; MIXER (Y) A DATA
YAS ; MIXER (Y) A DATA SELECTOR
YB0 - YB7 ; MIXER (Y) B DATA
YBS ; MIXER (Y) B DATA SELECTOR
YC0 - YC7 ; MIXER (Y) C DATA
YCE ; MIXER (Y) C ENABLE
YCP ; MIXER (Y) C POLARITY
YCS ; MIXER (Y) C DATA SELECTOR

OUTPUT

CQ0 - CQ7 ; MIXER (C) DATA OUTPUT
YQ0 - YQ7 ; MIXER (Y) DATA OUTPUT

