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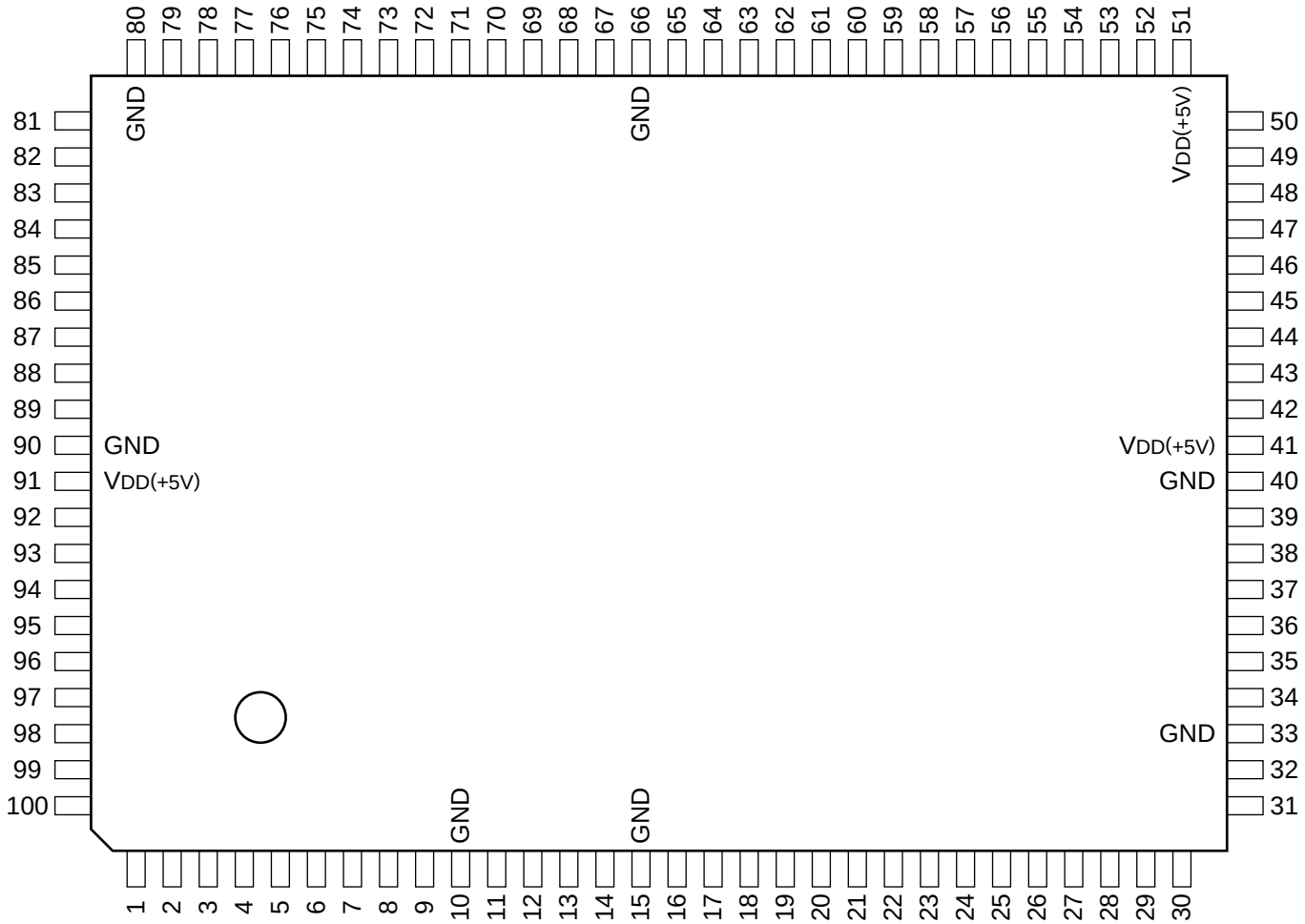
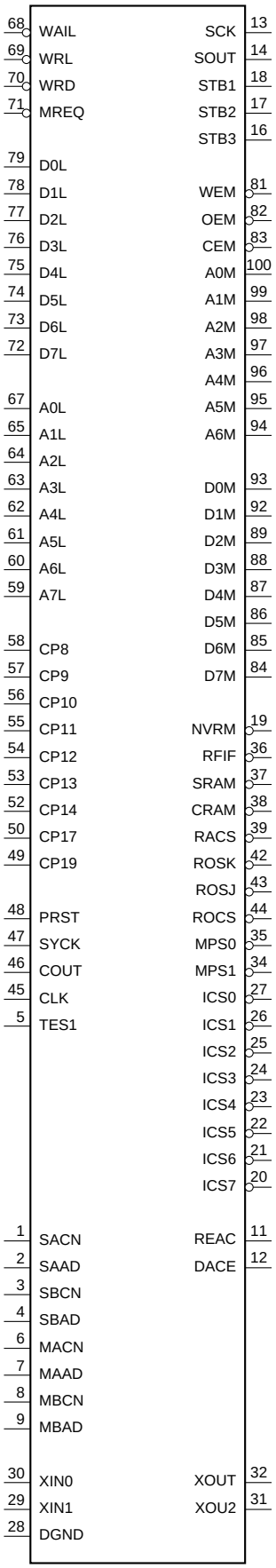
C-MOS RF BLOCK DIGITAL CONTROL INTERFACE

-TOP VIEW-



(V_{DD} = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	SACN	26	O	$\overline{ICS1}$	51	—	V_{DD}	76	I/O	D3L
2	I	SAAD	27	O	$\overline{ICS0}$	52	I	CP14	77	I/O	D2L
3	I	SBCN	28	O	D GND	53	I	CP13	78	I/O	D1L
4	I	SBAD	29	O	XIN1	54	I	CP12	79	I/O	D0L
5	I	TES1	30	I	XIN0	55	I	CP11	80	—	GND
6	I	MACN	31	O	XOU2	56	I	CP10	81	O	$\overline{WEM}$
7	I	MAAD	32	O	XOUT	57	I	CP9	82	O	$\overline{OEM}$
8	I	MBCN	33	—	GND	58	I	CP8	83	O	$\overline{CEM}$
9	I	MBAD	34	O	$\overline{MPS1}$	59	I	A7L	84	I/O	D7M
10	—	GND	35	O	$\overline{MPS0}$	60	I	A6L	85	I/O	D6M
11	O	REAC	36	O	$\overline{RFIF}$	61	I	A5L	86	I/O	D5M
12	O	DACE	37	O	$\overline{SRAM}$	62	I	A4L	87	I/O	D4M
13	O	SCK	38	O	$\overline{CRAM}$	63	I	A3L	88	I/O	D3M
14	O	SOUT	39	O	$\overline{RACS}$	64	I	A2L	89	I/O	D2M
15	—	GND	40	—	GND	65	I	A1L	90	—	GND
16	O	STB3	41	—	V_{DD}	66	—	GND	91	—	V_{DD}
17	O	STB2	42	O	$\overline{ROSK}$	67	I	A0L	92	I/O	D1M
18	O	STB1	43	O	$\overline{ROSJ}$	68	O	$\overline{WAIL}$	93	I/O	D0M
19	O	NVRM	44	O	$\overline{ROCS}$	69	I	$\overline{WRL}$	94	O	A6M
20	O	$\overline{ICS7}$	45	I	CLK	70	I	$\overline{WRD}$	95	O	A5M
21	O	$\overline{ICS6}$	46	O	C OUT	71	I	$\overline{MREQ}$	96	O	A4M
22	O	$\overline{ICS5}$	47	I	SYCK	72	I/O	D7L	97	O	A3M
23	O	$\overline{ICS4}$	48	I	PRST	73	I/O	D6L	98	O	A2M
24	O	$\overline{ICS3}$	49	I	CP19	74	I/O	D5L	99	O	A1M
25	O	$\overline{ICS2}$	50	I	CP17	75	I/O	D4L	100	O	A0M



INPUT

A0L - A7L	;	CPU ADDRESS BUS(LOW ORDER)
CLK	;	CPU CLOCK
CP8 - CP14, CP17, CP19	;	CPU ADDRESS BUS(UPPER ORDER)
D GND	;	CRYSTAL OSCILLATOR CIRCUIT GND
MAAD	;	METRIC PULSE INPUT ADV - AC
MACN	;	METRIC PULSE INPUT CONFI - AC
MBAD	;	METRIC PULSE INPUT ADV - BD
MBCN	;	METRIC PULSE INPUT CONFI - BD
MREQ	;	CPU MEMORY REQUEST
PRST	;	POWER ON RESET
SAAD	;	HEAD SELECT INPUT ADV - AC
SACN	;	HEAD SELECT INPUT CONFI - AC
SBAD	;	HEAD SELECT INPUT ADV - BD
SBCN	;	HEAD SELECT INPUT CONFI - BD
SYCK	;	SYSTEM CLOCK
WRD	;	CPU READ STROBE
WRL	;	CPU WRITE STROBE
XIN0	;	CRYSTAL INPUT 0
XIN1	;	CRYSTAL INPUT 1

OUTPUT

A0M - A6M	;	EXTERNAL RAM ADDRESS BUS
CEM	;	EXTERNAL RAM CHIP SELECT
COUT	;	CKL INPUT 2FREQUENCY
CRAM	;	ADDRESS DECODER
DACE	;	METRIC DATA COUNT END
ICS0 - ICS7	;	ADDRESS DECODER
MPS0, MPS1	;	ADDRESS DECODER
NVRM	;	ADDRESS DECODER
OEM	;	EXTERNAL OUTPUT ENABLE
RACS	;	ADDRESS DECODER
REAC	;	METRIC DATA A / C DETECTOR
RFIF	;	ADDRESS DECODER
ROCS	;	ADDRESS DECODER
ROSJ	;	ADDRESS DECODER
ROSK	;	ADDRESS DECODER
SCK	;	SERIAL TRANSMISSION CLOCK
SOUT	;	SERIAL TRANSMISSION DATA
STB1 - STB3	;	SERIAL TRANSMISSION STROBE1 - 3
SRAM	;	ADDRESS DECODER
WAIL	;	CPU WAIT
WEM	;	EXTERNAL RAM WRITE STROBE
XOUT	;	CRYSTAL OSCILLATOR OUTPUT
XOU2	;	CRYSTAL OSCILLATOR OUTPUT 2

INPUT/OUTPUT

D0L - D7L	;	CPU DATA BUS
D0M - D7M	;	EXTERNAL RAM DATA BUS