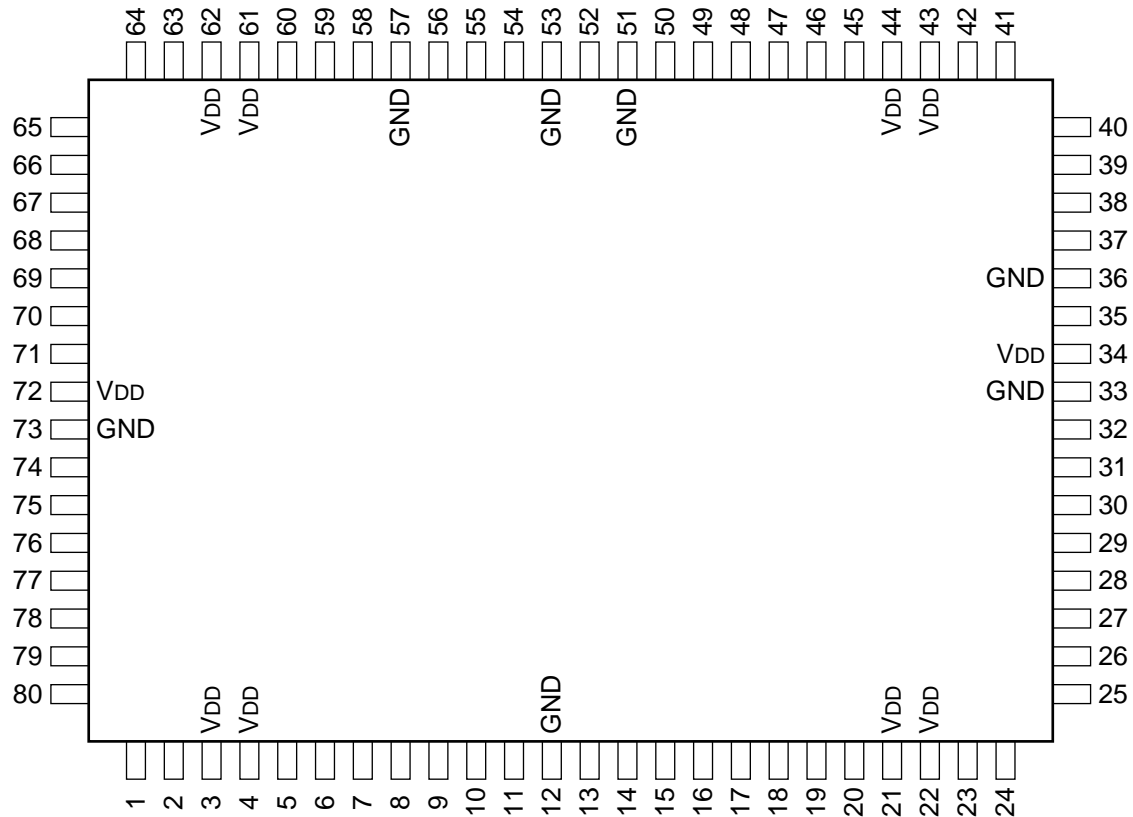


## C-MOS VIDEO TIMING SIGNAL GENERATOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	AD0	21	—	VDD	41	O	SYNCD6	61	—	VDD
2	I	AD1	22	—	VDD	42	O	SYNCD5	62	—	VDD
3	—	VDD	23	—	NC	43	—	VDD	63	I	CK4FSC
4	—	VDD	24	O	DA135	44	—	VDD	64	O	SAMPP
5	I	AD2	25	O	SAWD7	45	O	SYNCD4	65	O	SAMPN
6	I	AD3	26	O	SAWD6	46	O	SYNCD3	66	O	FSC
7	I	SCK	27	O	SAWD5	47	O	SYNCD2	67	I	BURST
8	I	SIN	28	O	SAWD4	48	O	SYNCD1	68	I	SYNC
9	O	SOUT	29	O	SAWD3	49	O	SYNCD0	69	I	LOCKERR
10	I	START	30	O	SAWD2	50	O	SYNCNT	70	O	MASKSYNC
11	O	COMPSYNC	31	O	SAWD1	51	—	GND	71	I	CFSET
12	—	GND	32	O	SAWD0	52	I	D1SYNC	72	—	VDD
13	O	HDG	33	—	GND	53	—	GND	73	—	GND
14	O	VDG	34	—	VDD	54	I	CFRST	74	I	TEST
15	O	OEG	35	I	CK27	55	I	FRRST	75	O	SYNCGATE
16	O	CFG	36	—	GND	56	I	EXT4FSC	76	O	DSELECT
17	I	TEST1	37	O	DA4FSC	57	—	GND	77	I	DSYNC1
18	I	TEST2	38	O	SYNCD9	58	O	SYNCEX	78	O	DETCF
19	O	TESTOUT	39	O	SYNCD8	59	O	BURSTEX	79	I	DSYNC2
20	I	RESET	40	O	SYNCD7	60	O	BURSTG	80	O	SCH

8	SIN	SOUT	9
7	SCK	FSC	66
10	START	SAMPN	65
		SAMPP	64
1	AD0	BURSTG	60
2	AD1		
5	AD2	BURSTEX	59
6	AD3	SYNCEX	58
		SAWD0	32
69	LOCKERROR	SAWD1	31
63	CK4FSC	SAWD2	30
56	EXT4FSC	SAWD3	29
55	FRRST	SAWD4	28
		SAWD5	27
68	SYNC	SAWD6	26
		SAWD7	25
67	BURST		
35	CK27	HD	13
77	DSYNC(1)	COMPSYNC	11
54	CFRST	CFG	16
71	CFSET	OEG	15
52	D1 SYNC	VDG	14
20	RESET		
		SYNCD0	49
		SYNCD1	48
74	TEST	SYNCD2	47
17	TEST1	SYNCD3	46
18	TEST2	SYNCD4	45
		SYNCD5	42
		SYNCD6	41
19	TEST OUT	SYNCD7	40
		SYNCD8	39
		SYNCD9	38
		DSELECT	76
		SYNCGATE	75

### INPUT

AD0 - AD3	: SERIAL PRESET ADDRESS
BURST	: REFERENCE BURST INPUT
CFRST	: CF RESET FOR D2 LOCK
CFSET	: EXTERNAL CF PULSE INPUT
CK27	: 27 MHz CLOCK
CK4FSC	: 4 fsc CLOCK
D1SYNC	: D1 SYNC FOR D1 LOCK
DSYNC1	: DELAYED SYNC FOR CF DET
DSYNC2	: DELAYED SYNC FOR SCH DET
EXT4FSC	: EXTERNAL 4 fsc CLOCK
FRRST	: FRAME RESET FOR D2 LOCK
LOCKERR	: 4 fsc PLL LOCK ERROR
RESET	: POWER ON RESET
SCK	: SERIAL CLOCK
SIN	: SERIAL DATA INPUT
START	: SERIAL START PULSE
SYNC	: REFERENCE SYNC INPUT
TEST	: TEST PIN
TEST1, 2	: TEST PINS

### OUTPUT

BURSTEX	: BURST EXIST : H
BURSTG	: BURST SAMPLE PULSE FOR NTSC
CFG	: COLOR FRAMING PULSE
COMPSYNC	: COMPOSITE SYNC
DA135	: 13.5 MHz CLOCK
DA4FSC	: 4 fsc CLOCK
DETCF	: DETECTED COLOR FRAMING
DSELECT	: HYSTERESIS CONTROL FOR CF DET
FSC	: FSC CLOCK
HDG	: HD PULSE
MASKSYNC	: GATED SYNC
OEG	: ODD / EVEN PULSE
SAMPN	: BURST SAMPLE (-) FOR PAL
SAMPP	: BURST SAMPLE (+) FOR PAL
SAWD0 - SAWD7	: 27 MHz SAW-TOOTH DATA (0 - 7)
SCH	: DETECTED SCH
SOUT	: SERIAL DATA OUTPUT
SYNCD0 - SYNCD9	: 4 fsc SYNC DATA (0 - 9)
SYNCEX	: SYNC EXIST : H
SYNCGATE	: SELECTED SYNC FOR CF DET
SYNCNT	: 4 fsc SYNC FOR NTSC
TESTOUT	: TEST PIN
VDG	: VD PULSE

