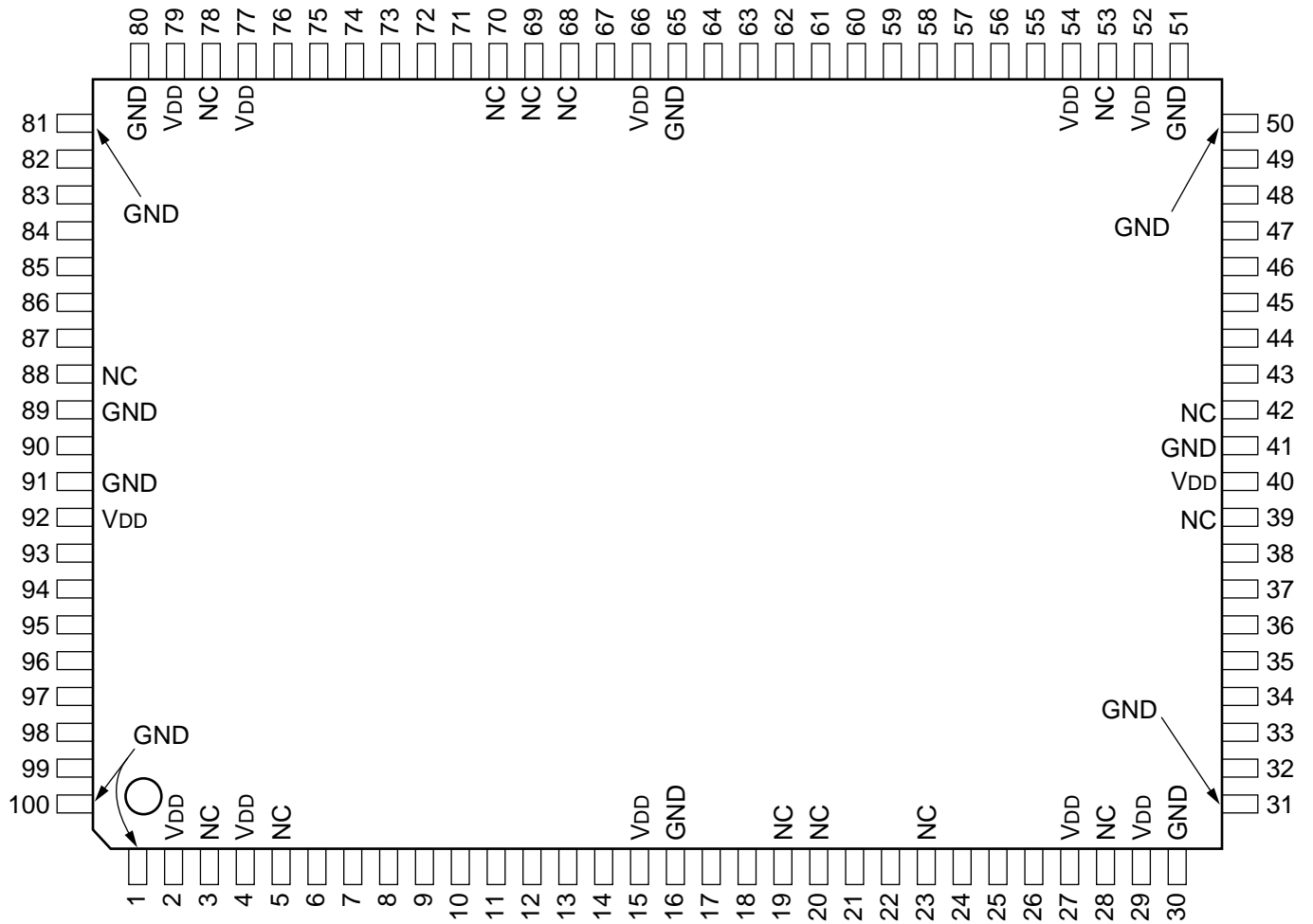


C-MOS AUDIO TIMING GENERATOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	26	I	T1	51	—	GND	76	O	DCK2
2	—	VDD	27	—	VDD	52	—	VDD	77	—	VDD
3	—	NC	28	—	NC	53	—	NC	78	—	NC
4	—	VDD	29	—	VDD	54	—	VDD	79	—	VDD
5	—	NC	30	—	GND	55	I	CRN	80	—	GND
6	O	FA2	31	—	GND	56	I	CFPI	81	—	GND
7	O	FA1	32	O	DE0	57	I	SA0	82	O	HD2
8	O	FA0	33	O	DE7	58	I	SA1	83	O	VD2
9	O	PC2	34	O	DE8	59	I	SA2	84	O	CF2
10	O	PC1	35	O	FP0	60	I	SA3	85	O	HD3
11	O	PC0	36	O	FP5	61	I	SCK	86	O	VD3
12	O	LOCK	37	O	FP6	62	I	STB	87	O	CF3
13	O	FREF	38	O	FP7	63	I	SI	88	—	NC
14	O	FCOMP	39	—	NC	64	O	SO	89	—	GND
15	—	VDD	40	—	VDD	65	—	GND	90	I	CLK
16	—	GND	41	—	GND	66	—	VDD	91	—	GND
17	I	FSCK	42	—	NC	67	O	TO	92	—	VDD
18	I	FPCK	43	O	FP8	68	—	NC	93	O	DCK4
19	—	NC	44	O	FS0	69	—	NC	94	O	HD4
20	—	NC	45	O	FS5	70	—	NC	95	O	VD4
21	I	ACS	46	O	FS6	71	O	HD0	96	O	CF4
22	I	INCF	47	O	FS7	72	O	VD0	97	O	HD5
23	—	NC	48	O	FS8	73	O	FOE1	98	O	VD5
24	I	T3	49	O	FS9	74	O	HD1	99	O	CF5
25	I	T2	50	—	GND	75	O	VD1	100	—	GND

		HD1	74
		VD1	75
		FOE1	73
		DCK2	76
		HD2	82
		VD2	83
62	STB	CF2	84
61	SCK		
63	SI	HD3	85
64	SO	VD3	86
		CF3	87
57	SA0		
58	SA1	DCK4	93
59	SA2	HD4	94
60	SA3	VD4	95
		CF4	96
55	CRN		
		HD5	97
		VD5	98
90	CLK	CF5	99
56	CFPI	HD0	71
22	INCF	VD0	72
		FA2	6
		FA1	7
21	ACS	FA0	8
18	FPCK	DE0	32
		DE7	33
26	T1	DE8	34
25	T2	FP0	35
24	T3	FP5	36
		FP6	37
9	PC2	FP7	38
10	PC1	FP8	43
11	PC0	FS0	44
67	TO	FS5	45
		FS6	46
		FS7	47
		FS8	48
		FS9	49
		FCOMP	14
17	FSCK	FREF	13
		LOCK	12

INPUT

- ACS : H: 48 kHz AUDIO L: 44.1 kHz AUDIO
- CFPI : INPUT CF PULSE
- CLK : CLOCK
- CRN : POWER ON RESET
- FPCK : AUDIO CLOCK (256FS) FOR PROGRAM PLAY
- FSCK : AUDIO CLOCK (512FS)
- INCF : CF SIGNAL FOR VD2 REFORMING
- SA0 - 3 : SERIAL PRESET ADDRESS
- SCK : SERIAL CLOCK
- SI : SERIAL INPUT DATA
- STB : SERIAL START PULSE
- T1 - T3 : TEST PIN FOR IC

OUTPUT

- CF2 : CF INFORMATION PULSE OF THE #2 GROUP (PROGRAMMABLE)
- CF3 : CF INFORMATION PULSE OF THE #3 GROUP (PROGRAMMABLE)
- CF4 : CF INFORMATION PULSE OF THE #4 GROUP (PROGRAMMABLE)
- CF5 : CF INFORMATION PULSE OF THE #5 GROUP (PROGRAMMABLE)
- DCK2 : 13.5 MHz CLOCK OF THE #2 GROUP (PROGRAMMABLE)
- DCK4 : 13.5 MHz CLOCK OF THE #4 GROUP (PROGRAMMABLE)
- DE0 - 8 : AUDIO CLOCK FOR PITCH CONTROL
- FA0 - 2 : AUDIO 5-FIELD SEQ. ID
- FCOMP : AUDIO CLOCK FOR PLL
- FOE1 : ODD/EVEN SIGNAL OF THE #1 GROUP (PROGRAMMABLE)
- FP0 - 8 : AUDIO CLOCK FOR PROGRAM PLAY
- FREF : REFERENCE CLOCK FOR AUDIO PLL
- FS0 - 9 : AUDIO CLOCK (FS9 : 512FS)
- HD0 : HD PULSE (UNPROGRAMMABLE)
- HD1 : HD PULSE OF THE #1 GROUP (PROGRAMMABLE)
- HD2 : HD PULSE OF THE #2 GROUP (PROGRAMMABLE)
- HD3 : HD PULSE OF THE #3 GROUP (PROGRAMMABLE)
- HD4 : HD PULSE OF THE #4 GROUP (PROGRAMMABLE)
- HD5 : HD PULSE OF THE #5 GROUP (PROGRAMMABLE)
- LOCK : AUDIO PLL LOCK DET
- PC0 - 2 : TEST PIN FOR IC
- SO : SERIAL OUTPUT DATA
- TO : TEST PIN FOR IC
- VD0 : VD PULSE (UNPROGRAMMABLE)
- VD1 : VD PULSE OF THE #1 GROUP (PROGRAMMABLE)
- VD2 : VD PULSE OF THE #2 GROUP (PROGRAMMABLE)
- VD3 : VD PULSE OF THE #3 GROUP (PROGRAMMABLE)
- VD4 : VD PULSE OF THE #4 GROUP (PROGRAMMABLE)
- VD5 : VD PULSE OF THE #5 GROUP (PROGRAMMABLE)

