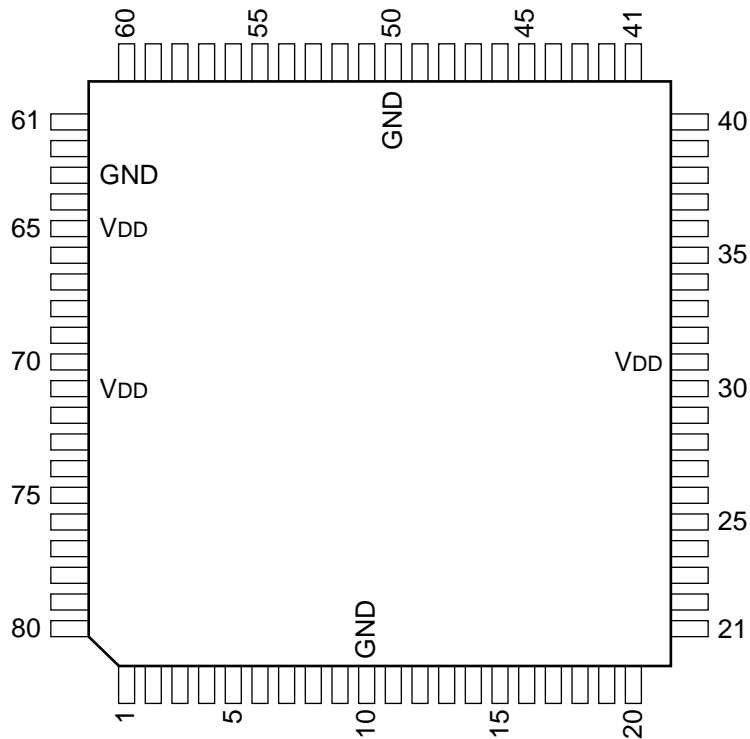


## C-MOS GATE ARRAY

—TOP VIEW—



PIN No.	I/O	SYMBOL	PIN No.	I/O	SYMBOL	PIN No.	I/O	SYMBOL	PIN No.	I/O	SYMBOL	PIN No.	I/O	SYMBOL
1	I	TSTSEL	17	O	TSTCOMPA5	33	O	TDATA9	49	I	DCLK	65	—	VDD
2	I	TSTLOAD	18	O	TSTCOMPA6	34	O	TDATA10	50	—	GND	66	I	ICSEL2
3	O	NHEADACTIVE	19	O	TSTCOMPA7	35	O	TDATA11	51	I	NDMAON	67	I	ICSEL1
4	O	PRINTPULSE	20	O	TDATA8	36	O	TDATA12	52	O	NDMAREQ	68	I	ICSEL0
5	I	PORTENABLE0	21	O	HDDATA1	37	O	TDATA13	53	O	TDATA14	69	O	NCLATCH
6	I	PORTENABLE1	22	O	HDDATA2	38	I	NRESET	54	O	TDATA15	70	O	NUMBER0
7	I	PORTENABLE2	23	O	HDDATA3	39	I	NHEADACTIVEF	55	I	TADD2	71	—	VDD
8	I	PRINTTYPE0	24	O	HDDATA4	40	I	PRINTPULSEF	56	I	TADD3	72	O	NUMBER1
9	I	PRINTTYPE1	25	O	HDDATA5	41	I	DATA7	57	I	PRINTPULSEI	73	O	NUMBER2
10	—	GND	26	O	HDDATA6	42	I	DATA6	58	I	NHEADACTIVEI	74	O	NUMBER3
11	I	TESTSEL	27	O	HDDATA7	43	I	DATA5	59	I	TADD4	75	O	NUMBER4
12	O	TSTCOMPA0	28	O	HDDATA8	44	I	DATA4	60	I	TADD5	76	O	NUMBER5
13	O	TSTCOMPA1	29	O	NHDLATCH	45	I	DATA3	61	I	TADD6	77	O	NUMBER6
14	O	TSTCOMPA2	30	O	HDCLK	46	I	DATA2	62	I	CLK1S4	78	O	NUMBER7
15	O	TSTCOMPA3	31	—	VDD	47	I	DATA1	63	—	GND	79	O	NUMBER8
16	O	TSTCOMPA4	32	O	NHDSTB	48	I	DATA0	64	I	CLK	80	O	NUMBER9