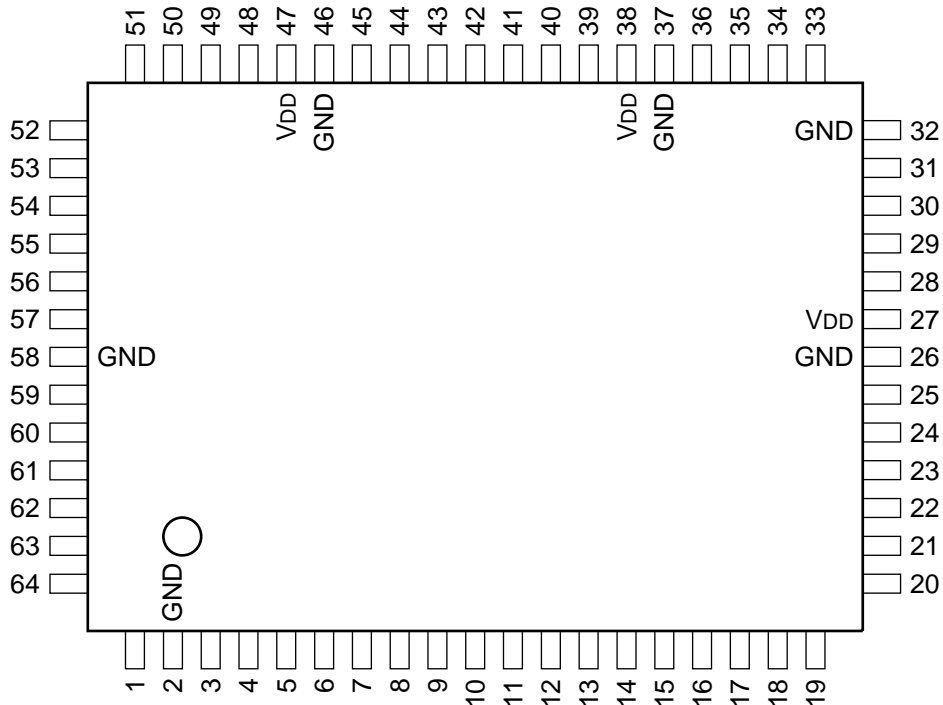


## OUTPUT PROCESSOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	Y/U/V9	17	I	INM2	33	O	V4	49	O	U6
2	—	GND	18	I	CHR	34	O	V5	50	O	U7
3	I	CK	19	I	SADD	35	O	V6	51	O	U8
4	I	HD	20	I	CS	36	O	V7	52	O	U9
5	I	FD	21	I	CKX	37	—	GND	53	O	Y/U/V0
6	I	IN0	22	I	CKD	38	—	VDD	54	O	Y/U/V1
7	I	IN1	23	I	SMPL	39	O	V8	55	O	Y/U/V2
8	I	IN2	24	I	RST	40	O	V9	56	I/O	SDAT
9	I	IN3	25	O	SYNC	41	O	U0	57	I	TST
10	I	IN4	26	—	GND	42	O	U1	58	—	GND
11	I	IN5	27	—	VDD	43	O	U2	59	O	Y/U/V3
12	I	IN6	28	O	V0	44	O	U3	60	O	Y/U/V4
13	I	IN7	29	O	V1	45	O	U4	61	O	Y/U/V5
14	I	IN8	30	O	V2	46	—	GND	62	O	Y/U/V6
15	I	IN9	31	O	V3	47	—	VDD	63	O	Y/U/V7
16	I	INM1	32	—	GND	48	O	U5	64	O	Y/U/V8

CXD8884Q(2/3)**INPUTS**

CK	:	SYSTEM CLOCK
CKD	:	SERIAL INTERFACE CLOCK
CKX	:	SERIAL INTERFACE ENABLE TIMING
CS	:	CHIP SELECT
SMPL	:	OUTPUT SAMPLE TIMING PULSE
FD	:	EXTERNAL FIELD O/E
HD	:	EXTERNAL H SYNC
IN0 - IN9	:	VIDEO
		MSB : IN9
		LSB : IN0
INM1, INM2	:	VIDEO
		MSB : M1
		LSB : M2
CHR	:	1BIT CHARACTER KEY
RST	:	RESET PULSE
SADD	:	SERIAL ADDRESS

**OUTPUTS**

SYNC	:	COMPOSITE SYNC
U0 - U9	:	U PHASE
V0 - V9	:	V PHASE
Y/U/V0 - Y/U/V9	:	IF DIGITAL MODE 4 : 2 : 2
		IF ANALOG MODE Y

**INPUT/OUTPUT**

SDAT	:	SERIAL DATA
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