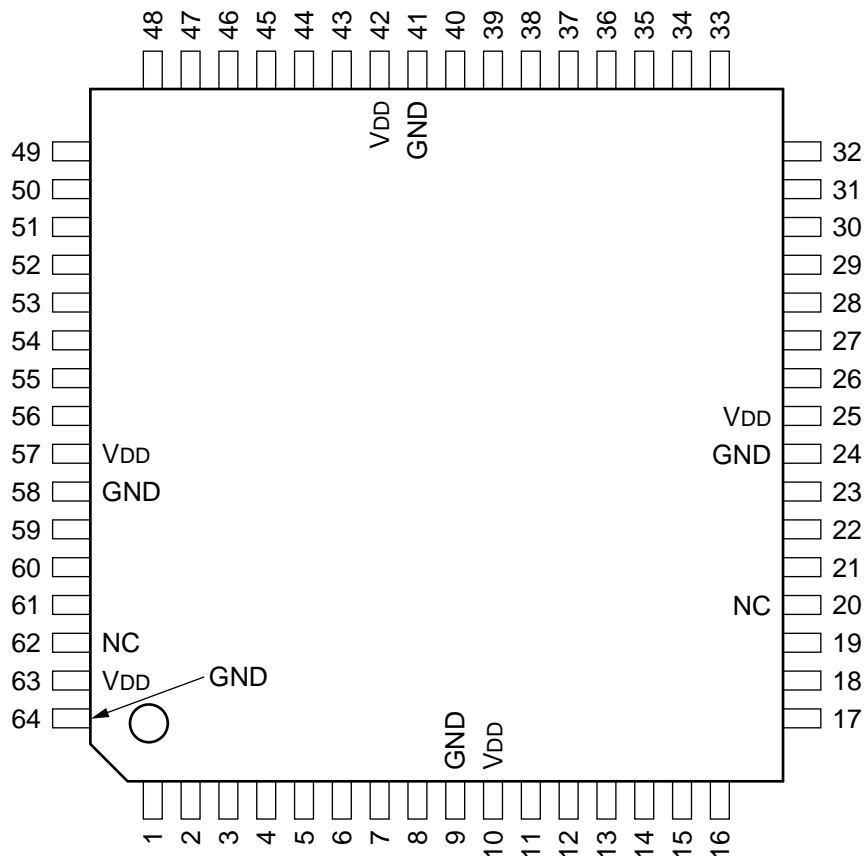


## C-MOS SERIAL I/O

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	D0	17	—	IC	33	—	IC	49	—	IC
2	I/O	D1	18	—	IC	34	—	IC	50	—	IC
3	I/O	D2	19	—	IC	35	—	IC	51	—	IC
4	I/O	D3	20	—	NC	36	—	IC	52	—	IC
5	I/O	D4	21	O	BCKO	37	I	SEL	53	—	IC
6	I/O	D5	22	O	IVH	38	I	RVD	54	—	IC
7	I/O	D6	23	O	TVD	39	I	RESE	55	—	IC
8	I/O	D7	24	—	GND	40	I	WR	56	—	IC
9	—	GND	25	—	VDD	41	—	GND	57	—	VDD
10	—	VDD	26	—	IC	42	—	VDD	58	—	GND
11	—	IC	27	—	IC	43	I	RD	59	I	BRDI
12	—	IC	28	—	IC	44	I	CS	60	O	BRD0
13	—	IC	29	—	IC	45	I	CLK	61	O	BTVD
14	—	IC	30	—	IC	46	I	A2	62	—	NC
15	—	IC	31	—	IC	47	I	A1	63	—	VDD
16	—	IC	32	—	IC	48	I	A0	64	—	GND

IC : INTERNAL CONNECTION (NO CONNECTION)

CXD8889R (2/2)**INPUT**

A0 - A2 : ADDRESS  
BRDI : BI-PHASE RECEIVE DATA  
CLK : CLOCK  
 $\overline{\text{CS}}$  : CHIP SELECT  
 $\overline{\text{RD}}$  : READ ENABLE  
RESE : RESET  
RVD : SERIAL DATA  
SEL : CPU INTERFACE TIMING SELECT  
 $\overline{\text{WR}}$  : WRITE ENABLE

**OUTPUT**

BCKO : INTERNAL CLOCK  
BRDO : DEMODULATED BI-PHASE RECEIVE DATA  
BTVD : MODULATED BI-PHASE TRANSMISSION DATA  
IVH : RECEIVE DATA INTERRUPT  
TVL : SERIAL DATA

**INPUT/OUTPUT**

D0 - D7 : PARALLEL DATA