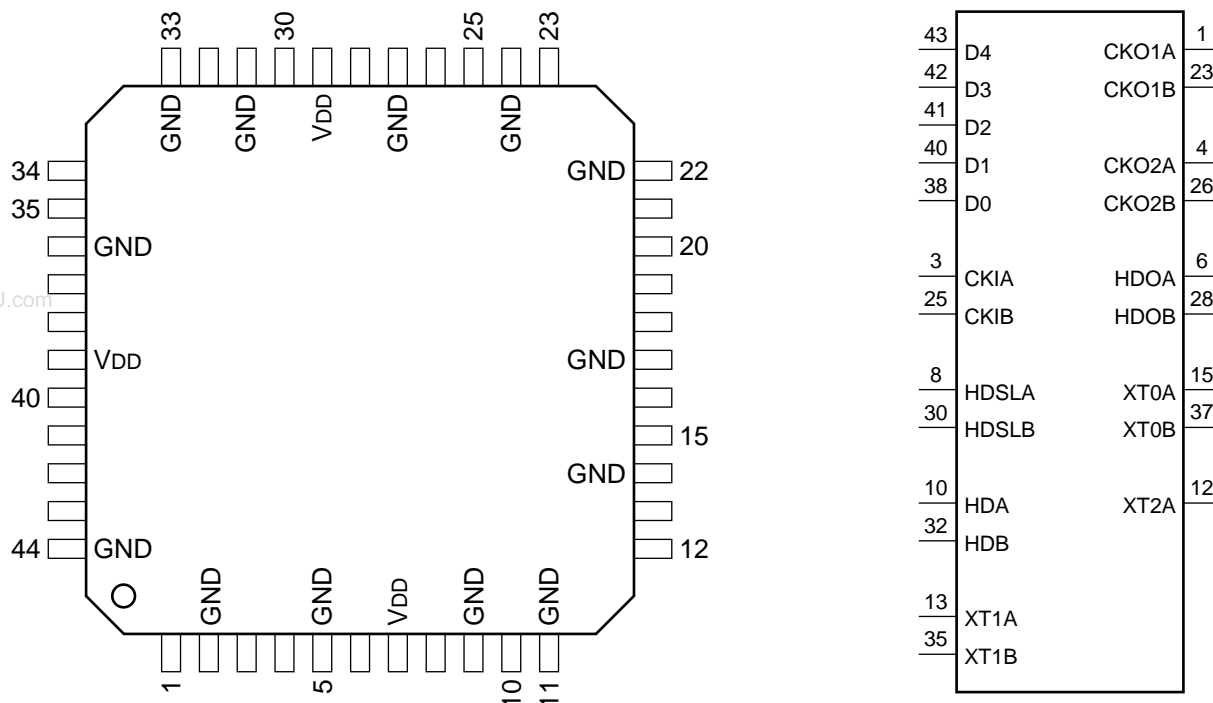


### C-MOS GATE ARRAY

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	CKO1A	12	O	XT2A	23	O	CKO1B	34	—	NC
2	—	GND	13	I	XT1A	24	—	GND	35	I	XT1B
3	I	CKIA	14	—	GND	25	I	CKIB	36	—	GND
4	O	CKO2A	15	O	XT0A	26	O	CKO2B	37	O	XT0B
5	—	GND	16	—	NC	27	—	GND	38	I	D0
6	O	HDOA	17	—	GND	28	O	HDOB	39	—	VDD
7	—	VDD	18	—	NC	29	—	VDD	40	I	D1
8	I	HDSLA	19	—	NC	30	I	HDSLB	41	I	D2
9	—	GND	20	—	NC	31	—	GND	42	I	D3
10	I	HDA	21	—	NC	32	I	HDB	43	I	D4
11	—	GND	22	—	GND	33	—	GND	44	—	GND