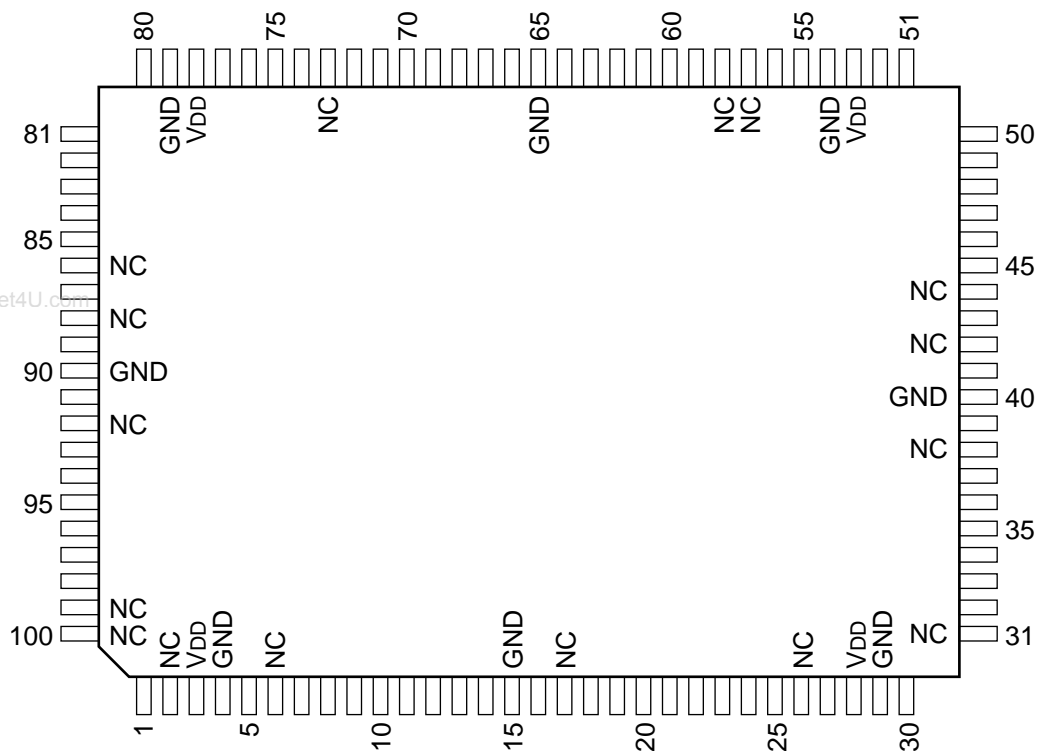


C-MOS ATRAC DECODER CONTROL (CXD8922Q) SUB IC

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	SWDTOS	21	O	ODTOP	41	I	RESET	61	O	ZRDETP3	81	I	DATAS5
2	—	NC	22	O	ROWOP0	42	—	NC	62	O	ZRDETP2	82	I	DATAS6
3	—	VDD	23	O	ROWOP1	43	I	XABS4P	63	O	ZRDETP1	83	I	DATAS7
4	—	GND	24	O	ROWOP2	44	—	NC	64	O	ZRDETP0	84	I	RCKS
5	O	AND34S	25	I	OEP	45	I	RENP	65	—	GND	85	I	RENS
6	—	NC	26	—	NC	46	I	RCKP	66	O	ZRDETS0	86	—	NC
7	I	OES	27	O	AND34P	47	I	DATAP7	67	O	ZRDETS1	87	I	XABS4S
8	I	ROWIS0	28	—	VDD	48	I	DATAP6	68	O	ZRDETS2	88	—	NC
9	I	ROWIS1	29	—	GND	49	I	DATAP5	69	O	ZRDETS3	89	I	FS256
10	I	ROWIS2	30	O	SWDTOP	50	I	DATAP4	70	O	ZRDETS4	90	—	GND
11	O	ODTOS	31	—	NC	51	I	DATAP3	71	O	ZRDETS5	91	O	LRSELS
12	O	ROWOS0	32	I	XLATP4	52	I	DATAP2	72	I	TEST	92	—	NC
13	O	ROWOS1	33	I	XLATP3	53	—	VDD	73	—	NC	93	I	SCKS
14	O	ROWOS2	34	I	XLATP2	54	—	GND	74	I	DATAS0	94	I	XLATS0
15	—	GND	35	I	XLATP1	55	I	DATAP1	75	I	DATAS1	95	I	XLATS1
16	I	TASTRT	36	I	XLATP0	56	I	DATAP0	76	I	DATAS2	96	I	XLATS2
17	—	NC	37	I	SCKP	57	—	NC	77	I	DATAS3	97	I	XLATS3
18	I	ROWIP0	38	—	NC	58	—	NC	78	—	VDD	98	I	XLATS4
19	I	ROWIP1	39	O	LRSELP	59	O	ZRDETP5	79	—	GND	99	—	NC
20	I	ROWIP2	40	—	GND	60	O	ZRDETP4	80	I	DATAS4	100	—	NC