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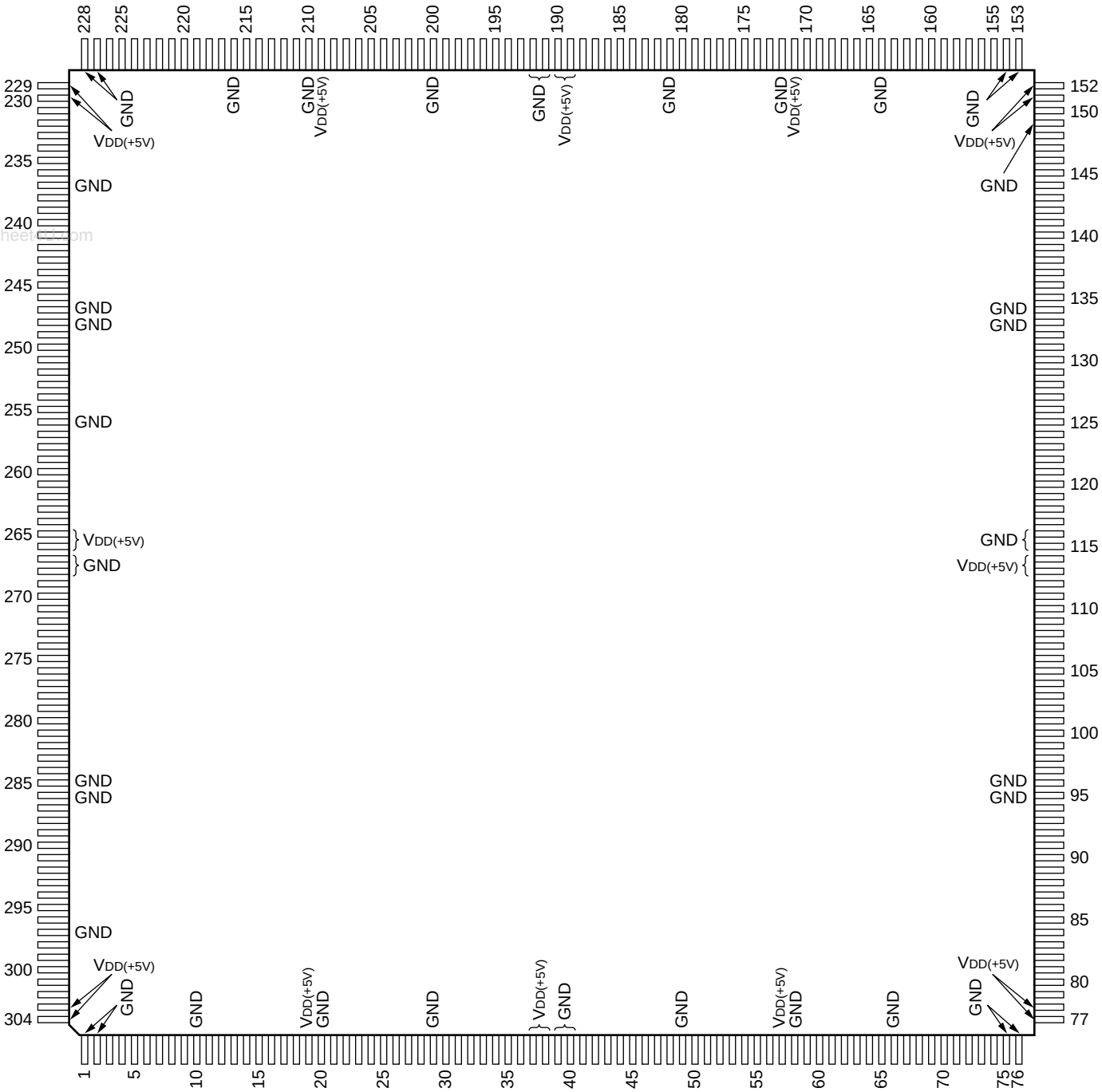
C-MOS GATE ARRAY

–TOP VIEW–



(V_{DD} = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	62	O	SVTXRDY	123	O	APC0	184	I	A16	245	O	P07
2	—	GND	63	O	SVSO	124	O	APC1	185	I	A17	246	O	P10
3	O	AB13	64	I	SVSI	125	O	APC2	186	I	A18	247	—	GND
4	O	AB14	65	I	SVSCK	126	O	APC3	187	I	A19	248	—	GND
5	O	AB15	66	—	GND	127	O	APC4	188	I	ASTB	249	O	P11
6	O	AB16	67	O	FLCLK	128	O	APC5	189	—	V_{DD}	250	O	P12
7	O	AB17	68	I	FLSCK	129	O	APC6	190	—	V_{DD}	251	O	P13
8	O	AB18	69	O	FLSO	130	O	APC7	191	—	GND	252	O	P14
9	O	AB19	70	O	FLTXINT	131	O	APD0	192	—	GND	253	O	P15
10	—	GND	71	I	FLBUSY	132	O	APD1	193	I	UBE	254	O	P16
11	I/O	DB0	72	I	FLERR	133	—	GND	194	I	IORD	255	O	P17
12	I/O	DB1	73	O	SL00	134	—	GND	195	I	IOWR	256	—	GND
13	I/O	DB2	74	O	SL01	135	O	APD2	196	I	MRD	257	I	P20
14	I/O	DB3	75	—	GND	136	O	APD3	197	I	BUFEN	258	I	P21
15	I/O	DB4	76	—	GND	137	O	APD4	198	I	BUFRW	259	I	P22
16	I/O	DB5	77	—	V_{DD}	138	O	APD5	199	I	XRES	260	O	P23
17	I/O	DB6	78	—	V_{DD}	139	O	APD6	200	—	GND	261	I	P24
18	I/O	DB7	79	O	SLO2	140	O	APD7	201	I	INTA	262	I	P25
19	—	V_{DD}	80	I	RL00	141	I	BPA0	202	O	ICU0INT	263	I	P26
20	—	GND	81	I	RL01	142	I	BPA1	203	O	ICU1INT	264	O	P27
21	I/O	DB8	82	I	RL02	143	I	BPA2	204	O	PCWR	265	—	V_{DD}
22	I/O	DB9	83	I	RL03	144	I	BPA3	205	O	RQ0	266	—	V_{DD}
23	I/O	DB10	84	I	RL04	145	I	BPA4	206	I	AK0	267	—	GND
24	I/O	DB11	85	I	RL05	146	I	BPA5	207	O	RQ1	268	—	GND
25	I/O	DB12	86	I	RL06	147	I	BPA6	208	I	AK1	269	O	EXROMCS
26	I/O	DB13	87	I	RL07	148	I	BPA7	209	—	V_{DD}	270	O	RAMLCS
27	I/O	DB14	88	O	SL10	149	—	GND	210	—	GND	271	O	RAMHCS
28	I/O	DB15	89	O	SL11	150	I	BACLK	211	O	RQ2	272	O	RMEMEN
29	—	GND	90	O	SL12	151	—	V_{DD}	212	I	AK2	273	O	PMEMEM
30	O	IORDAK	91	I	RL10	152	—	V_{DD}	213	O	RQ3	274	O	M4XXXX
31	O	IOWRAK	92	I	RL11	153	—	GND	214	I	AK3	275	O	M5XXXX
32	I	RSVINT	93	I	RL12	154	—	GND	215	O	SYSERR	276	O	SYSLED
33	I	SWPA	94	I	RL13	155	I	PCK	216	—	GND	277	O	SYSSW
34	I	EXSYA	95	—	GND	156	I	CLK10	217	I/O	PD0	278	O	SPEN
35	I	SWPD	96	—	GND	157	—	GND	218	I/O	PD1	279	O	RIOEN
36	I	EXSYD	97	I	RL14	158	I	DSR	219	I/O	PD2	280	O	PIOEN
37	—	V_{DD}	98	I	RL15	159	O	T1I	220	I/O	PD3	281	O	DIOEN
38	—	V_{DD}	99	I	RL16	160	I	CTS	221	I/O	PD4	282	O	TCEN
39	—	GND	100	I	RL17	161	O	RTS	222	I/O	PD5	283	O	I10DX
40	—	GND	101	I	DIAL A	162	O	TXD	223	I/O	PD6	284	O	I10EX
41	O	EXCKA	102	I	DIAL B	163	I	RXD	224	I/O	PD7	285	—	GND
42	I/O	SBDTA	103	I	APA0	164	—	GND	225	O	TXD1	286	—	GND
43	O	DTC1A	104	I	APA1	165	I/O	AD0	226	I	RXD1	287	O	I10FX
44	O	DTC2A	105	I	APA2	166	I/O	AD1	227	—	GND	288	O	I11XX
45	O	DTC1D	106	I	APA3	167	I/O	AD2	228	—	GND	289	O	AB0
46	O	DTC2D	107	I	APA4	168	I/O	AD3	229	—	V_{DD}	290	O	AB1
47	I/O	SBDTD	108	I	APA5	169	I/O	AD4	230	—	V_{DD}	291	O	AB2
48	O	EXCKD	109	I	APA6	170	I/O	AD5	231	O	TXI	292	O	AB3
49	—	GND	110	I	APA7	171	—	V_{DD}	232	O	RXI	293	O	AB4
50	I	LRCK	111	I	APB0	172	—	GND	233	O	CTL5	294	O	AB5
51	I	TMRCK	112	I	APB1	173	I/O	AD6	234	I	LE	295	O	AB6
52	I	SYAO	113	—	V_{DD}	174	I/O	AD7	235	I	RD	296	O	AB7
53	I	SYDO	114	—	V_{DD}	175	I/O	AD8	236	I	WR	297	—	GND
54	I	VSSYNC	115	—	GND	176	I/O	AD9	237	—	GND	298	O	AB8
55	O	ENDA	116	—	GND	177	I/O	AD10	238	O	P00	299	O	AB9
56	O	ENDD	117	I	APB2	178	I/O	AD11	239	O	P01	300	O	AB10
57	—	V_{DD}	118	I	APB3	179	I/O	AD12	240	O	P02	301	O	AB11
58	—	GND	119	I	APB4	180	I/O	AD13	241	O	P03	302	O	AB12
59	O	XRES0	120	I	APB5	181	—	GND	242	O	P04	303	—	V_{DD}
60	O	XRES1	121	I	APB6	182	I/O	AD14	243	O	P05	304	—	V_{DD}
61	I	SVINT	122	I	APB7	183	I/O	AD15	244	O	P06			

AB0 - AB19	;	ADDRESS BUS
DB0 - DB15	;	DATA BUS
IORDAK	;	IORD AND ; AK
IOWRAK	;	IOWR AND ; AK
RSVINT	;	ICU2 - INTP7
SWPA	;	SWITCHING PULSE-A INPUT
EXSYA	;	EXSY-A INPUT
SWPD	;	SWITCHING PULSE-D INPUT
EXSYD	;	EXSY-D INPUT
EXCKA	;	EXCK-A INPUT
SBDTA	;	SUBCODE DATA-A
DTC1A	;	DTC1-A OUTPUT
DTC2A	;	DTC2-A OUTPUT
DTC1D	;	DTC1-D OUTPUT
DTC2D	;	DTC2-D OUTPUT
SBDTD	;	SUBCODE DATA-D
EXCKD	;	EXCK-D OUTPUT
LRCK	;	FS CLOCK (48/44.1kHz) INPUT
TMRCK	;	TIME CLOCK (LRCK/8) INPUT
SYAO	;	SBSY-A INPUT
SYDO	;	SBSY-D INPUT
VSYNC	;	VIDEO SYNC INPUT
END A	;	END-A OUTPUT
END D	;	END-D OUTPUT
XRES0	;	RESET TO KY
XRES1	;	RESET TO SP, SV
SVINT	;	REQUEST SIGNAL OUTPUT FOR SV
SVTXRDY	;	TRANSMIT READY OUTPUT FOR SV
SVSO	;	SERIAL DATA OUTPUT FOR SV
SVSI	;	SERIAL DATA INPUT FOR SV
SVSCK	;	SERIAL CLOCK INPUT FOR SV
FLCLK	;	4MHz CLOCK INPUT FOR FL
FLSCK	;	SERIAL CLOCK INPUT FOR KY
FLSO	;	SERIAL DATA OUTPUT FOR FL
FLTXINT	;	INT OUTPUT TO FL
FLB USY	;	STATUS (BUSY) INPUT FROM FL
FLERR	;	STATUS (ERROR) INPUT FROM FL
SL00 - SL02, SL10 - SL12	;	SCAN LINE OUTPUTS
RL00 - RL17	;	RETURN LINE INPUTS
DIAL A	;	Z-PHASE SIGNAL-A INPUT
DIAL B	;	Z-PHASE SIGNAL-B INPUT
APA 0	;	STOP COMMAND INPUT
APA 1	;	FF COMMAND INPUT
APA 2	;	PLAY COMMAND INPUT
APA 3	;	REWIND COMMAND INPUT
APA 4	;	STAND BY COMMAND INPUT
APA 5	;	MONITOR COMMAND INPUT

APA 6	;	REC COMMAND INPUT
APA 7	;	NEXT COMMAND INPUT
APB 0	;	PREV COMMAND INPUT
APB 1	;	STID COMMAND INPUT
APB 2	;	SKID COMMAND INPUT
APB 3	;	END ID COMMAND INPUT
APB 4	;	CHASE COMMAND INPUT
APB 5	;	RSV0 COMMAND INPUT
APB 6	;	RSV1 COMMAND INPUT
APB 7	;	EXT 96 SELECT INPUT
APC 0	;	STOP START OUTPUT
APC 1	;	FF START OUTPUT
APC 2	;	PLAY START OUTPUT
APC 3	;	REW START OUTPUT
APC 4	;	STAND BY START OUTPUT
APC 5	;	MONITOR START OUTPUT
APC 6	;	REC START OUTPUT
APC 7	;	LOCATE START OUTPUT
APD 0	;	ST ID START OUTPUT
APD 1	;	SK ID START OUTPUT
APD 2	;	END ID START OUTPUT
APD 3	;	CHASE START OUTPUT
APD 4	;	SV START OUTPUT
APD 5	;	RSV START OUTPUT
APD 6	;	STOP START OUTPUT FOR 8P
APD 7	;	PLAY START OUTPUT FOR 8P
BPA 0	;	SPEED-B INPUT
BPA 1	;	SPEED-A INPUT
BPA 2	;	CUE RVS INPUT
BPA 6	;	STOP COMMAND INPUT FOR 8P
BPA 7	;	PLAY COMMAND INPUT FOR 8P
BACLK	;	614.4KHz CLOCK INPUT
PCK	;	16MHz CLOCK INPUT
CLK10	;	10MHz CLOCK INPUT (CPU CLOCK)
DSR	;	DATA SET READY INPUT FOR RS-232C
DTR	;	DAAT TERMINAL READY OUTPUT FOR RS-232C
RTS	;	REQUEST TO SEND OUTPUT FOR RS-232C
TXD	;	TRANSMIT DATA OUTPUT FOR RS-232C
RXD	;	RECEIVE DATA INPUT FOR RS-232C
AD0 - AD15	;	A/D BUS INPUTS/OUTPUTS
A16 - A19	;	ADDRESS BUS INPUT
ASTB	;	ADDRESS STROB OUTPUT
UBE	;	UPPER BYTE ENABLE OUTPUT
IORD	;	IORD (I/O READ) OUTPUT
IOWR	;	IOWR (I/O WRITE) OUTPUT
MRD	;	MRD (MEMORY READ) OUTPUT
BUFEN	;	BUFFER ENABLE OUTPUT

BUFRW	; BUFFER READ/WRITE OUTPUT
XRES	; SYSTEM RESET INPUT
INTA	; INTA OUTPUT
ICU0INT	; INTERRUPT CONTROLLER UNIT 0 INP OUTPUT
ICU1INT	; INTERRUPT CONTROLLER UNIT 1 INP OUTPUT
PCWR	; PCWR OUTPUT
RQ0, RQ1, RQ2, RQ3	; DMA REQUEST OUTPUT
AK0, AK1, AK2, AK3	; DMA ACKNOWLEDGE INPUT
SYSERR	; SYSTEM ERROR OUTPUT
PD0 - PD7	; A/D BUS INPUTS/OUTPUTS FOR 9PIN
TXD1	; TRANSMIT DATA OUTPUT FOR RS-422
RXD1	; RECEIVE DATA INPUT FOR RS-422
TXI	; INTERRUPT OUTPUT FOR 9PIN CPU
RXI	; NON MASKABLE INTERRUPT OUTPUT FOR 9PIN CPU
CTL5	; INTERRUPT OUTPUT FOR 9PIN CPU
LE	; ADDRESS LATCH FOR 9PIN CPU
RD	; RD INPUT FOR 9PIN CPU
WR	; WR INPUT FOR 9PIN CPU
P00 - P07	; PORT D0 - PORT D7 OUTPUTS
P10	; EEPROM DI OUTPUT
P11	; EEPROM CE OUTPUT
P12	; EEPROM WR OUTPUT
P13	; EEPROM PRE OUTPUT
P14	; EEPROM CLK OUTPUT
P15	; EEPROM MONITOR OUTPUT
P17	; RESET INH OUTPUT
P20	; EEPROM DO INPUT
P21	; RF DET-A INPUT
P22	; RF DET-D INPUT
P23	; PRINT INT INPUT
P24	; PRINT ERROR INPUT
P25	; PRINT SLCK INPUT
P26	; PRINT ACK INPUT
P27	; PRINT STB INPUT
EXROMCS	; CHIP SELECT OUTPUT FOR ROM
RAMLCS	; CHIP SELECT OUTPUT FOR RAM LOWER BYTE
RAMHCS	; CHIP SELECT OUTPUT FOR RAM UPPER BYTE
RMEMEN	; CHIP SELECT OUTPUT FOR MEM BLOCK (RECORDER MEM AREA)
PMEMEN	; CHIP SELECT OUTPUT FOR MEM BLOCK (PLAYER MEM AREA)
SYSLED	; SYSLED OUTPUT
SYSSW	; SYS SWITCH INPUT
SPEN	; CHIP SELECT OUTPUT FOR SP BLOCK
RIOEN	; CHIP SELECT OUTPUT FOR MEM BLOCK (RECODER I/O AREA)
PIOEN	; CHIP SELECT OUTPUT FOR MEM BLOCK (PLAYER I/O AREA)
DIOEN	; CHIP SELECT OUTPUT FOR DIO BLOCK
TCEN	; CHIP SELECT OUTPUT FOR TC BLOCK
I10DX	; CHIP SELECT OUTPUT FOR METER IC
AB0 - AB12	; ADDRESS BUS OUTPUTS